TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT   SILICON MONOLITHIC

TC4001BP, TC4001BF, TC4001BFN, TC4001BFT

TC4001B QUAD 2 INPUT NOR GATE

The TC4001B is 2-input positive NOR gate, respectively. Since the outputs of these gates are epuipped with the buffers, the input / output transmission characteristics have been improved and the variation of transmission time due to an increase in the load capacity is kept minimum.
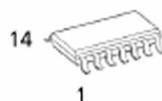
(Note) The JEDEC SOP (FN) is not available in Japan.
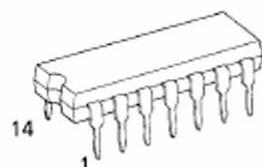
P (DIP14-P-300-2.54)
Weight : 0.96g (Typ.)
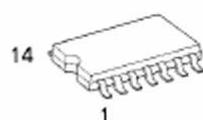
F (SOP14-P-300-1.27)
Weight : 0.18g (Typ.)

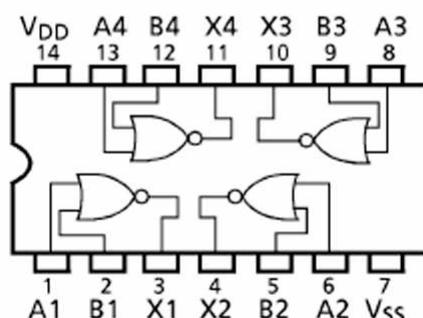FN (SOL14-P-150-1.27)
Weight : 0.12g (Typ.)

FT (TSSOP14-P-0044-0.65)
Weight : 0.06g (Typ.)

MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	V_{DD}	$V_{SS} - 0.5 \sim V_{SS} + 20$	V
Input Voltage	V_{IN}	$V_{SS} - 0.5 \sim V_{DD} + 0.5$	V
Output Voltage	V_{OUT}	$V_{SS} - 0.5 \sim V_{DD} + 0.5$	V
DC Input Current	I_{IN}	± 10	mA
Power Dissipation	P_D	300 (DIP) / 180 (SOIC)	mW
Operating Temperature Range	T_{opr}	$-40 \sim 85$	°C
Storage Temperature Range	T_{stg}	$-65 \sim 150$	°C

PIN ASSIGNMENT (TOP VIEW)

TC4001B

LOGIC DIAGRAM

1 / 4 TC4001B

$X = \overline{A + B}$