

54H/74H61

TRIPLE 3-INPUT EXPANDER

ORDERING CODE: See Section 9

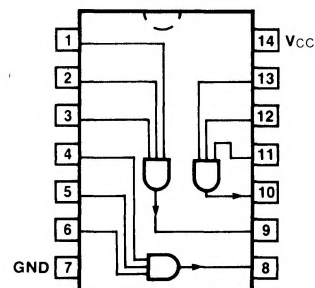
PKGS	PIN OUT	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
		$V_{CC} = +5.0 \text{ V} \pm 5\%$, $T_A = 0^\circ \text{C to } +70^\circ \text{C}$	$V_{CC} = +5.0 \text{ V} \pm 10\%$, $T_A = -55^\circ \text{C to } +125^\circ \text{C}$	
Plastic DIP (P)	A	74H61PC		9A
Ceramic DIP (D)	A	74H61DC	54H61DM	6A
Flatpak (F)	B	74H61FC	54H61FM	3I

INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

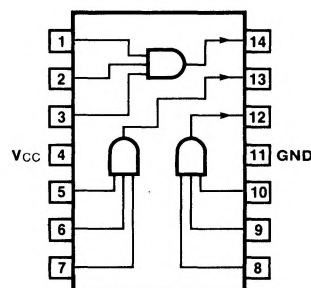
PINS	54/74H (U.L.) HIGH/LOW
Inputs	1.25/1.25
Outputs	*/*

CONNECTION DIAGRAMS

PINOUT A



PINOUT B



DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

SYMBOL	PARAMETER	54/74H		UNITS	CONDITIONS	
		Min	Max			
V _{ON}	Output ON Voltage		1.0	V	T _A = -55°C I _{ON} = 4.5 mA	V _{CC} = Min, V _{IH} = 2.0 V
			1.0		T _A = 0°C I _{ON} = 5.35 mA	
I _{OFF}	Output OFF Current		50	μA	V _{CC} = Min, V _{IL} = 0.8 V T _A = Max, V _{OFF} = 2.2 V	
I _{CC(ON)} I _{CC(OFF)}	Power Supply Current		16	mA	V _{IN} = Open	V _{CC} = Max
			7.0		V _{IN} = Gnd	

OUTPUT CAPACITANCE: V_{CC} and Ground Terminals Open

SYMBOL	PARAMETER	54/74H		UNITS	CONDITIONS
		Min	Max		
C_X	Effective Capacitance of Output Transistor Q_1		1.3**	pF	$f = 1.0 \text{ MHz}, T_A = +25^\circ \text{C}$

*Expander Outputs

**Typical Value