

FJP5555 NPN Silicon Transistor

Features

- High Voltage Switch Mode Application
- Fast Speed Switching
- Wide Safe Operating Area
- Suitable for Electronic Ballast Application



FJP5555 — NPN Silicon Transistor

January 2010

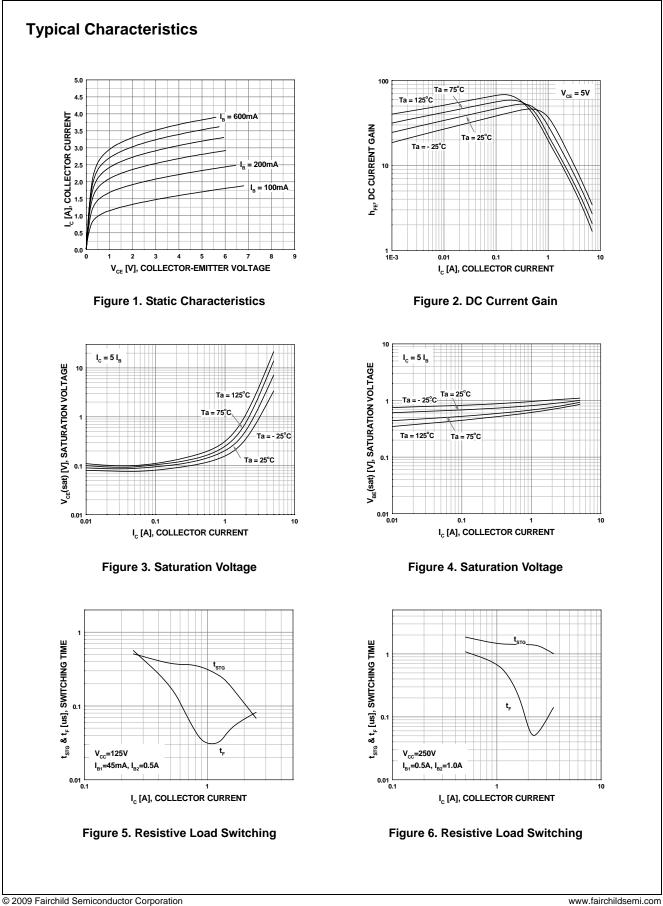
1.Base 2.Collector 3.Emitter

Absolute Maximum Ratings T _A =25°C unless otherwise noted						
Symbol	Parameter	Value	Units			
V _{CBO}	Collector-Base Voltage	1050	V			
V_{CEO}	Collector-Emitter Voltage	400	V V			
V_{EBO}	Emitter-Base Voltage	14				
۱ _C	Collector Current (DC)	5	A			
I _{CP}	Collector Current (Pulse)	10	A			
P _C	Collector Dissipation	75	W			
ТJ	Junction Temperature	150	°C			
T _{STG}	Storage Temperature	- 55 to +150	°C			

Electrical Characteristics T_A=25°C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Voltage	I _C =500μA, I _E =0	1050			V
BV _{CEO}	Collector-Emitter Voltage	I _C =5mA, I _B =0	400			V
BV _{EBO}	Emitter-Base Voltage	I _E =500μA, I _C =0	14			V
h _{FE}	*DC Current Gain	V_{CE} =5V, I _C =10mA V_{CE} =3V, I _C =0.8A	10 20		40	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C =1A, I _B =0.2A I _C =3.5A, I _B =1.0A			0.5 1.5	V V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C =3.5A, I _B =1.0A			1.2	V
C _{ob}	Output Capacitance	V _{CB} =10V, f=1MHz		45		pF
t _{ON}	Turn On Time	V _{CC} =125V, I _C =0.5A			1.0	μS
t _{STG}	Storage Time	I _{B1} =45mA, I _{B2} =0.5A			1.2	μS
t _F	Fall Time	$R_L=250\Omega$			0.3	μS
t _{ON}	Turn On Time	V _{CC} =250V, I _C =2.5A			2.0	μS
t _{STG}	Storage Time	I _{B1} =0.5A, I _{B2} =1.0A			2.5	μS
t _F	Fall Time	R _L =100Ω			0.3	μS
EAS	Avalanche Energy	L= 2mH	6			mJ

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FJP5555 Rev. B0

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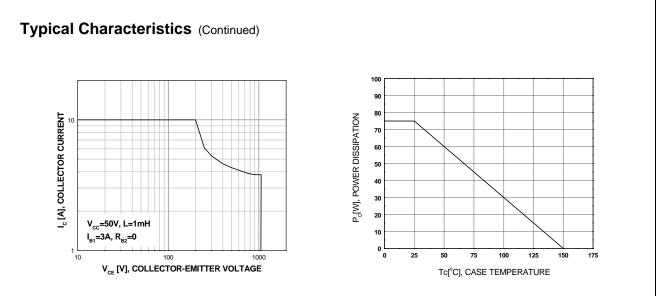


Figure 7. Reverse Biased Safe Operating Area

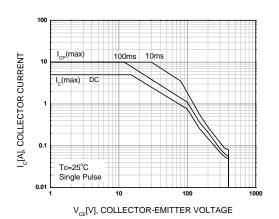


Figure 9. Forward Biased Safe Operating Area

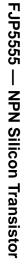
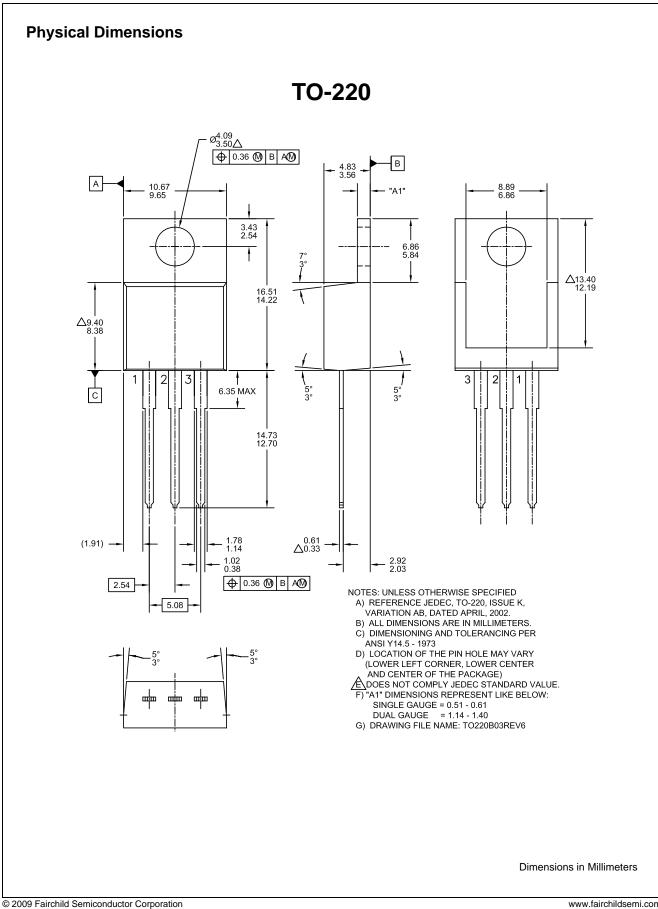


Figure 8. Power Derating



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