

## Dual Micropower Chopper Stabilized Operational Amplifier with Internal Capacitors

### FEATURES

- No External Components Required
- Supply Current 80 $\mu$ A
- Maximum Offset Voltage 10 $\mu$ V
- Maximum Offset Voltage Drift 50nV/ $^{\circ}$ C
- Minimum CMRR 110dB
- Minimum PSRR 110dB
- Single Supply Operation 4.75V to 16V
- Common Mode Range Includes GND
- Output Swings to GND
- Typical Overload Recovery Time 70ms
- Pin Compatible with Industry Standard Dual Op Amps

### APPLICATIONS

- Thermocouple Amplifiers
- Electronic Scales
- Battery Powered Instrumentation
- Strain Gauge Amplifiers
- Remote Located Sensors

### DESCRIPTION

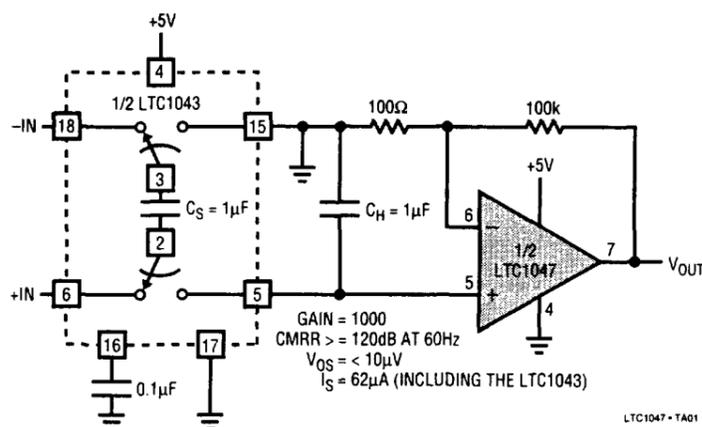
The LTC1047 is a micropower, high performance dual chopper stabilized operational amplifier. The sample-and-hold capacitors usually required by other chopper amplifiers are integrated on-chip, minimizing the need for external components.

The LTC1047 has a typical offset voltage of 3 $\mu$ V, drift of 10nV/ $^{\circ}$ C, input noise voltage typically 3.5 $\mu$ Vp-p, and typical voltage gain of 150dB. The common mode rejection is 110dB minimum, with minimum power supply rejection of 110dB. The LTC1047 also offers 0.2V/ $\mu$ s slew rate and a gain bandwidth product of 200kHz. Overload recovery time from saturation is 70ms, four times faster than chopper amplifiers with external capacitors.

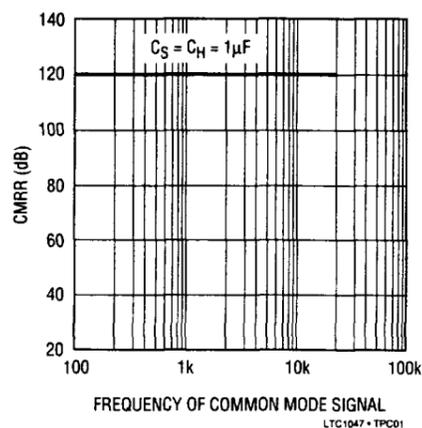
The LTC1047 is available in a standard plastic 8-pin DIP as well as a 16-pin SOL package. The LTC1047 is a plug-in replacement for most dual op amps with improved DC performance and substantial power savings.

### TYPICAL APPLICATION

Micropower Single Supply Instrumentation Amplifier



CMRR vs Frequency



**ABSOLUTE MAXIMUM RATINGS** (Note 1)

Total Supply Voltage ( $V^+$ to $V^-$ ) .....	16V	Operating Temperature Range	
Input Voltage (Note 2) .....	( $V^+ + 0.3V$ ) to ( $V^- - 0.3V$ )	LTC1047C .....	0°C to 70°C
Output Short Circuit Duration .....	Indefinite	Lead Temperature (Soldering, 10 sec.) .....	300°C
Storage Temperature Range .....	-65°C to 150°C		

**PACKAGE/ORDER INFORMATION**

<p>N8 PACKAGE 8-LEAD PLASTIC DIP LTC1047 - P001</p>	ORDER PART NUMBER	<p>S PACKAGE 16-LEAD PLASTIC SOL LTC1047 - P002</p>	ORDER PART NUMBER
	LTC1047CN8		LTC1047CS

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**ELECTRICAL CHARACTERISTICS**  $V_S = \pm 5V$ ,  $T_A =$  Operating Temperature Range, unless otherwise noted.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Input Offset Voltage	$T_A = 25^\circ C$ (Note 3)		$\pm 3$	$\pm 10$	$\mu V$
Average Input Offset Voltage Drift	(Note 3)	●	$\pm 0.01$	$\pm 0.05$	$\mu V/^\circ C$
Long Term Offset Drift			100		nV/rt mo.
Input Bias Current	$T_A = 25^\circ C$	●	$\pm 5$	$\pm 30$ $\pm 300$	pA pA
Input Offset Current	$T_A = 25^\circ C$	●	$\pm 10$	$\pm 60$ $\pm 150$	pA pA
Input Noise Voltage	0.1Hz to 10Hz 0.1Hz to 1Hz		3.5 0.8		$\mu Vp-p$ $\mu Vp-p$
Input Noise Current	$f = 10Hz$ (Note 4)		1.5		fA/rtHz
Common Mode Rejection Ratio	$V_{CM} = V^-$ to $+2.7V$ , $T_A = 25^\circ C$	●	110 105	130	dB dB
Power Supply Rejection Ratio	$V_S = \pm 2.375V$ to $\pm 8V$	●	105	122	dB
Large Signal Voltage Gain	$R_L = 100k$ , $V_{OUT} = \pm 4V$	●	120	150	dB
Maximum Output Voltage Swing	$R_L = 10k$ $R_L = 100k$	●	$+ 4.8/-4.9$	$+ 4.3/-4.8$ $\pm 4.95$	V V
Slew Rate	$R_L = 100k$ , $C_L = 50pF$		0.2		V/ $\mu s$
Gain Bandwidth Product			200		kHz
Supply Current/Amplifier	No Load, $25^\circ C \leq T_A \leq 70^\circ C$ No Load, $0^\circ C \leq T_A \leq 25^\circ C$		60 80	150 275	$\mu A$ $\mu A$
Internal Sampling Frequency	$T_A = 25^\circ C$		680		Hz

The ● denotes specifications which apply over the full operating temperature range.

**Note 1:** Absolute Maximum Ratings are those values beyond which life of the device may be impaired.

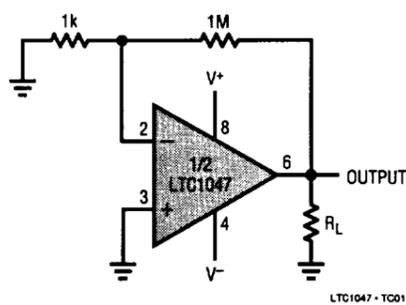
**Note 2:** Connecting any terminal to voltages greater than V+ or less than V- may cause destructive latch-up. It is recommended that no sources operating from external supplies be applied prior to power-up of the LTC1047.

**Note 3:** These parameters are guaranteed by design. Thermocouple effects preclude measurements of these voltage levels in high speed automatic test systems. V<sub>OS</sub> is measured to a limit determined by test equipment capability.

**Note 4:** Current Noise is calculated from the formula:  $I_n = \sqrt{2q \cdot I_B}$  where  $q = 1.6 \times 10^{-19}$  Coulomb.

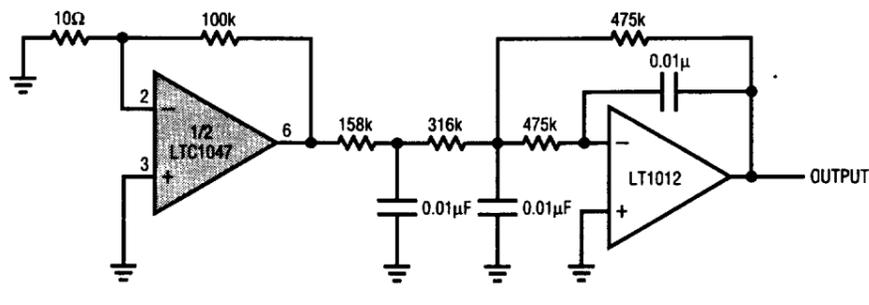
### TEST CIRCUITS

Electrical Characteristics Test Circuit



LTC1047 - TC01

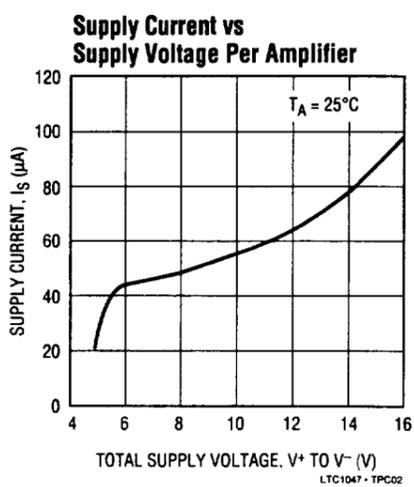
DC-10Hz Noise Test Circuit



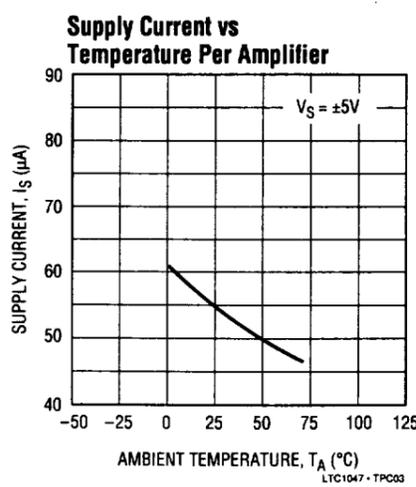
FOR 1Hz NOISE BANDWIDTH, INCREASE ALL CAPACITORS BY A FACTOR OF 10.

LTC1047 - TC02

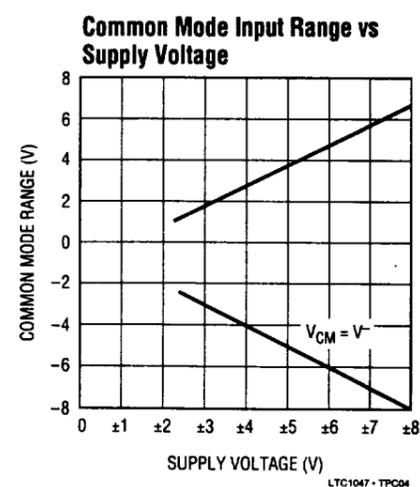
### TYPICAL PERFORMANCE CHARACTERISTICS



LTC1047 - TPC02



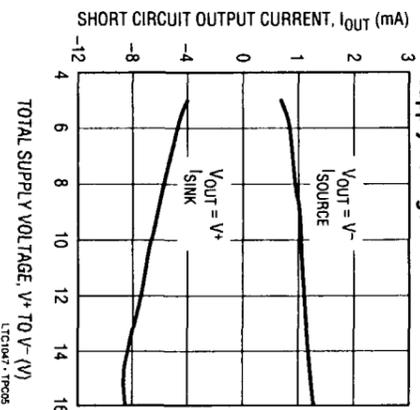
LTC1047 - TPC03



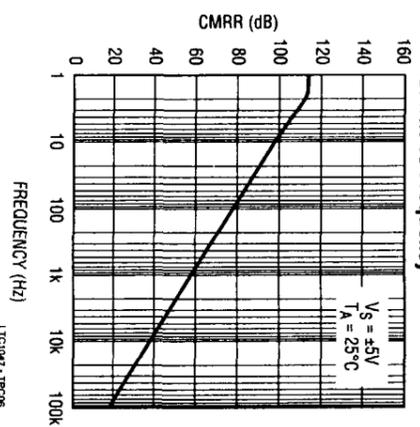
LTC1047 - TPC04

## TYPICAL PERFORMANCE CHARACTERISTICS

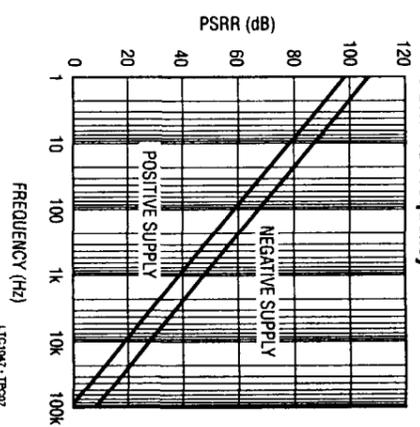
Output Short Circuit Current vs Supply Voltage



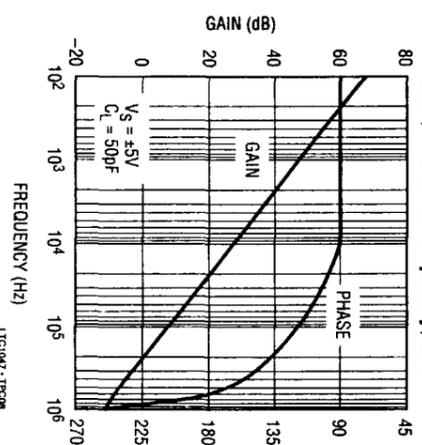
CMRR vs Frequency



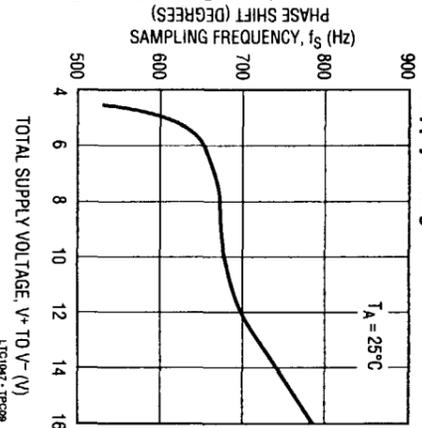
PSRR vs Frequency



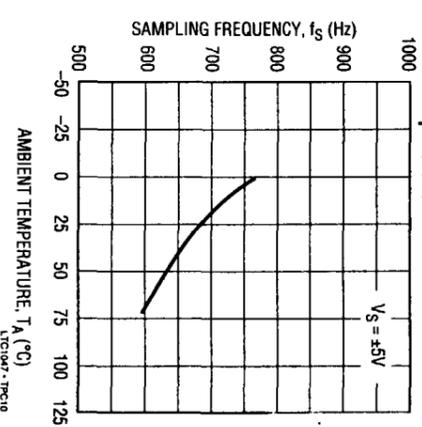
Gain, Phase vs Frequency, ±5V



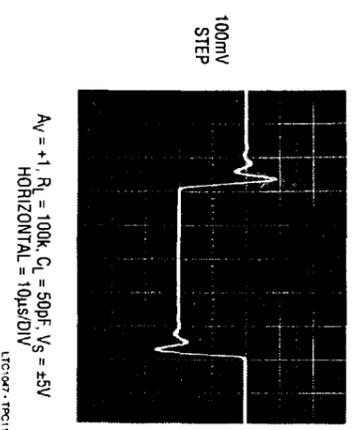
Sampling Frequency vs Supply Voltage



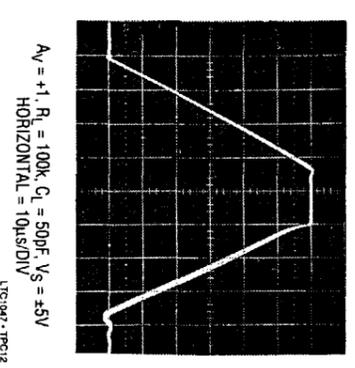
Sampling Frequency vs Temperature



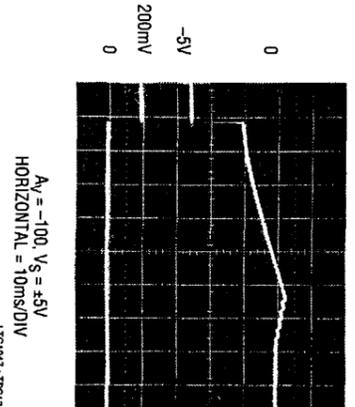
Small Signal Transient Response



Large Signal Transient Response



Overload Recovery



# LTC1047

## APPLICATIONS INFORMATION

### Input Considerations

Frequently circuits built with parts as precise as the LTC1047 show errors at the output far greater than the designer expects. Rarely is the problem the op amp; more often the surrounding circuitry is causing errors several orders of magnitude greater than those due to the LTC1047. Such obscure effects as leakage between pins due to residual solder flux and thermocouple effects between the tin/lead solder and the copper PC board traces can overwhelm the pA-level bias currents and the  $\mu\text{V}$ -level offset of the LTC1047. For a more complete description of these types of problems (and some advice on avoiding them), see the LTC1051/53 data sheet.

### Input Capacitance

The LTC1047 has approximately 12pF of capacitance at each input pin. This will react with large series resistors to form a pole at the input, degrading the LTC1047's phase margin. The problem is especially common with micropower parts like the LTC1047 because high value resistors are often used to minimize power dissipation. As

a rule of thumb, bypass feedback resistors larger than 7k with a 20pF capacitor to minimize this effect.

### Aliasing

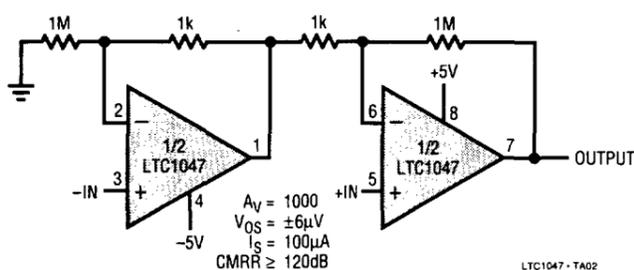
Like all sampled data systems, the LTC1047 will alias input signals near its internal sampling frequency. The design includes internal circuitry to minimize this effect; as a result, most applications do not exhibit aliasing problems. For a complete discussion of the correction circuitry and aliasing behavior, refer to the LTC1051/53 data sheet.

### Single Supply Operation

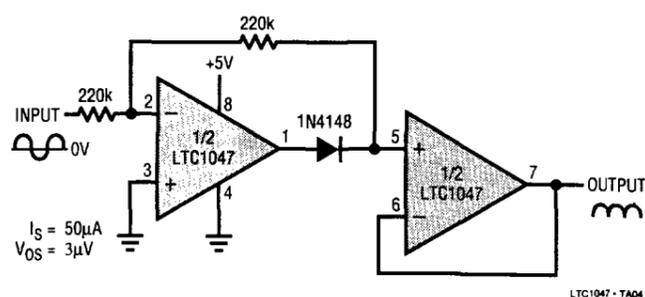
The LTC1047 is compatible with all single supply applications. It has an input common mode range which includes  $V_{-}$ , and an output which will swing within millivolts of the negative power supply. The LTC1047 is guaranteed functional down to 4.75V total supply, allowing it to run from minimum TTL voltage all the way up to 16V. See the Typical Applications section for examples of single supply operation.

## TYPICAL APPLICATIONS

Low Offset, Low Drift Instrumentation Amplifier

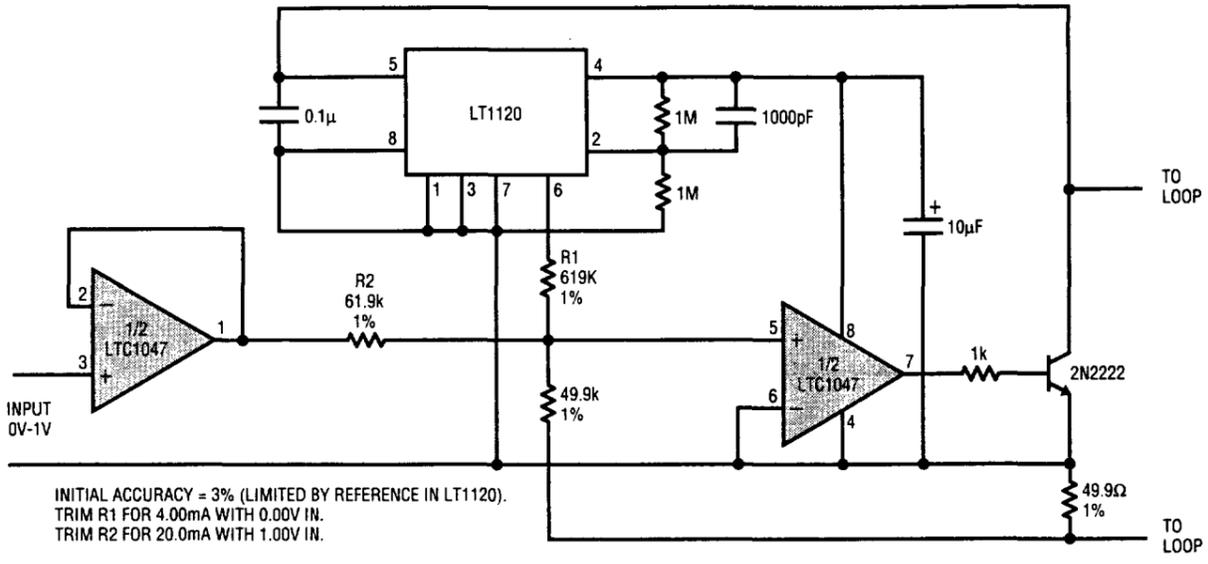


Precise DC Full Wave Rectifier (Absolute Value Circuit)



TYPICAL APPLICATIONS

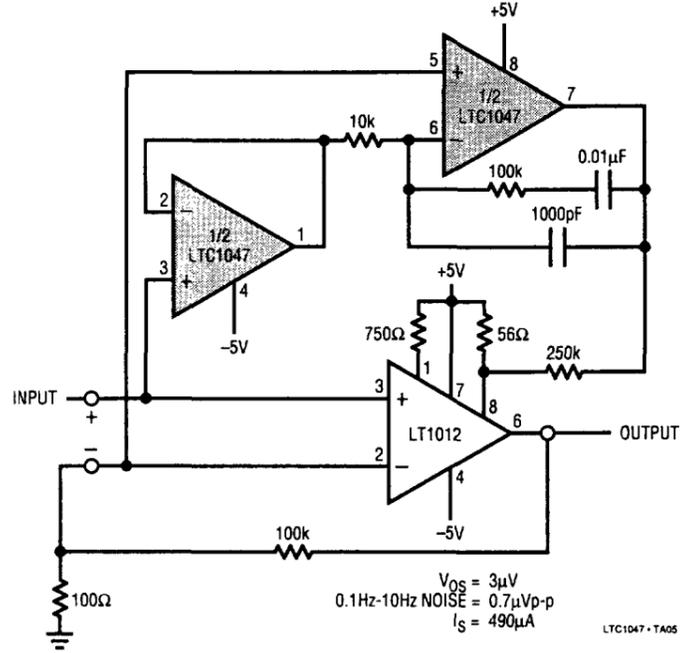
4-20mA Transducer Amplifier



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LTC1047 - TA03

Low Noise, Low Drift Composite Amplifier



LTC1047 - TA05

