



V_{CC1} = Pin 1
 V_{CC2} = Pin 16
 V_{EE} = Pin 8

P_D = 310 mW typ/pkg (No Load)
 t_{pd} = 4.0 ns typ

TRUTH TABLE

\bar{C}	C	D	Q_{n+1}
H	ϕ	ϕ	L
L	L	ϕ	Q_n
L	H	L	L
L	H	H	H

ϕ = Don't Care
 $C = \bar{C}_C + C_E$

Quad Latch

The MC10133 is a high speed, low power, MECL quad latch consisting of four bistable latch circuits with D type inputs and gated Q outputs. Open emitters allow a large number of outputs to be wire ORed together. Latch outputs are gated, allowing direct wiring to a bus. When the clock is high, outputs will follow D inputs. Information is latched on negative going transition of the clock.