



$V_{CC1}$  = Pin 1  
 $V_{CC2}$  = Pin 16  
 $V_{EE}$  = Pin 8

$P_D$  = 300 mW typ/pkg (No Load)  
 $t_{pd}$  = 2.5 ns typ (Control Inputs to B Output)  
 4.0 ns typ (Data Inputs to A Output)  
 6.0 ns typ (Data Inputs to B Output)

INPUTS	OUTPUTS	
	Sum of D Inputs at High Level	Odd Parity Output A
Even	Low	High
Odd	High	Low

## 9 + 2-Bit Parity Generator-Checker

The MC10170 is a 11-bit parity circuit, which is segmented into 9 data bits and 2 control bits. The function is particularly useful for byte organized systems such as disc and tape systems.

Output A generates odd parity on 9 bits; that is, Output A goes high for an odd number of high logic levels on the bit inputs in only 2 gate delays.

The Control Inputs can be used to expand parity to larger numbers of bits with minimal delay or can be used to generate even parity. To expand parity to larger words, the MC10170 can be used with the MC10160 or other MC10170's. The MC10170 can generate both even and odd parity.