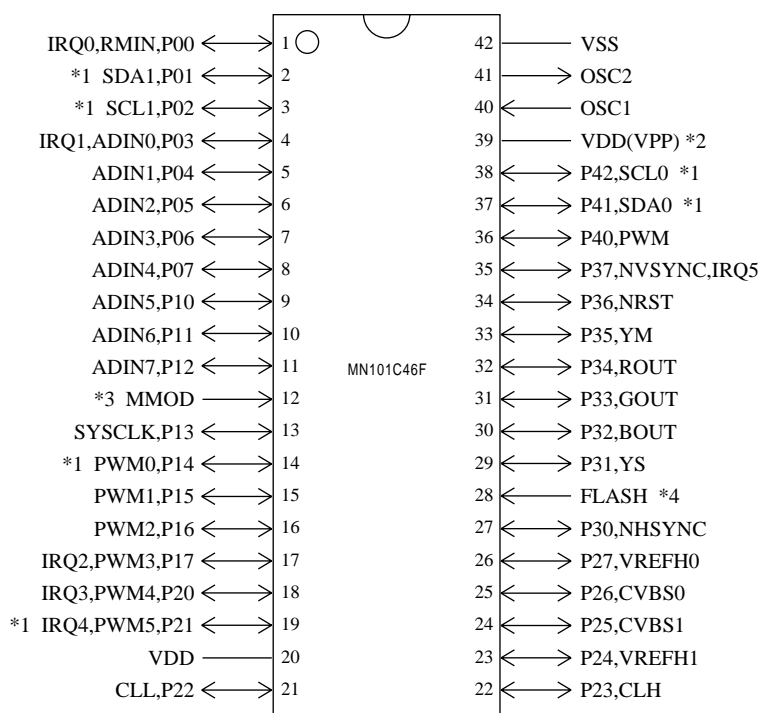


MN101C46F

■ Type			MN101C46F		
■ ROM (×8-bit)			96 K		
■ RAM (×8-bit)			3 K		
■ Package (Conventional Package)			SDIP042-P-0600C *Lead-free (SDIP042-P-0600)		
■ Minimum Instruction Execution Time			279 ns (at 3.0 V to 3.6 V, 14.32 MHz)		
■ Interrupts			External (6 lines) Internal (12 lines) : Timer × 3, A/D, OSD, I ² C, Caption × 4, Remote control, Watchdog		
■ Timer Counter			8-bit timer × 3 Watchdog timer: system clock fs 1/2 ¹⁶ , 1/2 ¹⁸ , 1/2 ²⁰ triple selection		
■ Serial Interface			I ² C × 1: for multimaster mode, bus line (output) has 2 systems		
■ Caption			• Built-in sync separator × 2		
■ I/O Pins	I/O	35	• Common use		
■ A/D Inputs			5-bit × 8-ch. (with S/H)		
■ PWM			8-bit × 6-ch. , 14-bit × 1-ch.		
■ Special Ports			Remote control reception		
■ CRTC			1-layer display (graphics, characters)		
■ Notes			Remote control input discriminant circuit built-in		

Pin Assignment

() : Conventional Package



SDIP042-P-0600C *Lead-free

(SDIP042-P-0600)

*1: 5 V dielectric Nch open drain output pin

*2: MN101C46F (VDD), MN101CF46F (VPP)

*3: MMOD = H (fixed) (Set the test mode pin to the normal mode.)

*4: FLASH = L (fixed) (Set the flash mode pin to the normal mode.)

Support Tool

In-circuit Emulator	PX-ICE101C/D+PX-PRB101C46-SDIP042-P-0600-M	
EPROM Built-in Type	Type	MN101CF46F [ES (Engineering Sample) available]
	ROM (× 8-bit)	96 K
	RAM (× 8-bit)	3 K
	Minimum instruction execution time	279 ns (at 3.0 V to 3.6 V, 14.32 MHz)
	Package	SDIP042-P-0600C *Lead-free
	(Conventional Package)	(SDIP042-P-0600)

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