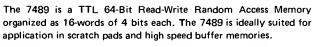
64-BIT READ/WRITE MEMORY (RAM) | N7489

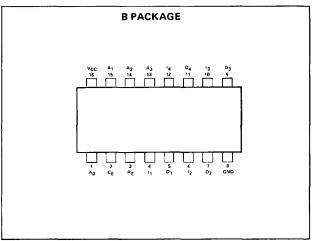
N7489-B

DIGITAL 54/74 TTL SERIES

PIN CONFIGURATION

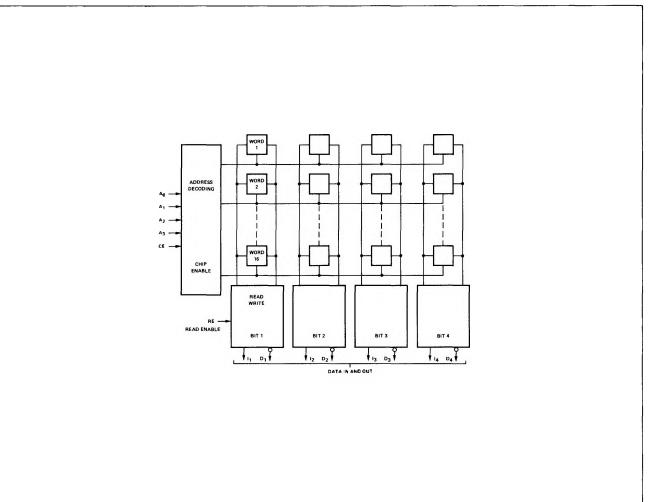


Words are selected through a 4-input binary decoder when the chip select input (CE) is at logic "0". Data is written into the memory when Read Enable (RE) is at logic "0" and read from the memory when RE is at logic "1".



LOGIC DIAGRAM

DESCRIPTION



SIGNETICS DIGITAL 54/74 TTL SERIES N7489

| PARAMETER | TEST CONDITIONS | MIN | TYP | МАХ | UNI |
|--------------|----------------------------------|------|-----|-----|-----|
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| | | | | | |
| See 8225 Dat | ta Sheet for Pin-for-Pin Replace | ment | | | |
| | | | | | |
| | 1 | | | | |
| | | | | | |
| | | | | | |
| | | | | | |

ELECTRICAL CHARACTERISTICS (over recommended operating free-air temperature range unless otherwise noted)