1.0 A Output Current, Dual Power Operational Amplifiers

The NCS2372 is a monolithic circuit intended for use as a power operational amplifier in a wide range of applications, including servo amplifiers and power supplies. No deadband crossover distortion provides better performance for driving coils.

Features

- Output Current to 1.0 A
- Slew Rate of 1.3 V/µs
- Wide Bandwidth of 1.1 MHz
- Internal Thermal Shutdown
- Single or Split Supply Operation
- Excellent Gain and Phase Margins
- Common Mode Input Includes Ground
- Zero Deadband Crossover Distortion
- These Devices are Pb-Free and are RoHS Compliant

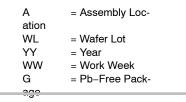


ON Semiconductor®

http://onsemi.com

MARKING DIAGRAM





PIN CONNECTIONS

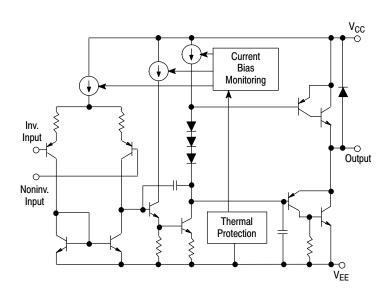
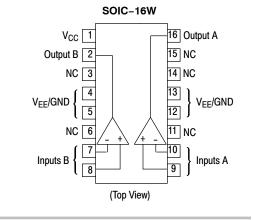


Figure 1. Representative Block Diagram



ORDERING INFORMATION

| Dev | /ice | Package | Shipping [†] |
|--------|--------|-----------------------|-----------------------|
| NCS237 | 2DWR2G | SOIC-16W (Pb-Free) | 1000/Tape & Reel |

+ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|---|-----------------------|-------------|------|
| Supply Voltage (from V_{CC} to V_{EE}) | V _S | 40 | V |
| Input Differential Voltage Range | V _{IDR} | Note 1 | V |
| Input Voltage Range | V _{IR} | Note 1 | V |
| Junction Temperature (Note 2) | Tj | +150 | °C |
| Operating Temperature Range | T _A | -40 to +125 | °C |
| Storage Temperature Range | T _{stg} | –55 to +150 | °C |
| DC Output Current | ۱ _۵ | 1.0 | А |
| Peak Output Current (Nonrepetitive) | I _(max) | 1.5 | А |
| Thermal Resistance, Junction-to-Air | $R_{	hetaJA}$ | 80 | °C/W |
| Thermal Resistance, Junction-to-Case | $R_{	extsf{	heta}JC}$ | 12 | °C/W |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability. 1. Either or both input voltages should not exceed the magnitude of V_{CC} or V_{EE} . 2. Power dissipation must be considered to ensure maximum junction temperature (T_J) is not exceeded.

NCS2372

| Characteristics | Symbol | Min | Тур | Max | Unit |
|--|------------------------------------|--|-------------------------|--------------------------|-------|
| Input Offset Voltage (V _{CM} = 0) $T_A = +25^{\circ}C$ T_A , T_{low} to T_{high} | V _{IO} | | 1.0 _ | 15 20 | mV |
| Average Temperature Coefficient of Offset Voltage | $\Delta V_{IO} / \Delta T$ | - | 20 | - | μV/°C |
| Input Bias Current (V _{CM} = 0) | I _{IB} | - | 100 | 500 | nA |
| Input Offset Current (V _{CM} = 0) | I _{IO} | - | 10 | 50 | nA |
| Large Signal Voltage Gain $V_0 = \pm 10 V$, R _L = 2.0 k | A _{VOL} | 30 | 100 | - | V/mV |
| Output Voltage Swing (I _L = 100 mA) $T_A = +25^{\circ}C$ $T_A = T_{low} \text{ to } T_{high}$ $T_A = +25^{\circ}C$ $T_A = T_{low} \text{ to } T_{high}$ | V _{OH} V _{OL} | 14.0 13.9 - - | 14.2 - -14.2 - | - - -14.0 -13.9 | V |
| $ \begin{array}{l} \text{Output Voltage Swing (I_{L} = 1.0 \text{ A})} \\ \text{V}_{\text{CC}} = +24 \text{ V}, \text{ V}_{\text{EE}} = 0 \text{ V}, \text{T}_{\text{A}} = +25^{\circ}\text{C} \\ \text{V}_{\text{CC}} = +24 \text{ V}, \text{ V}_{\text{EE}} = 0 \text{ V}, \text{T}_{\text{A}} = \text{T}_{\text{low}} \text{ to } \text{T}_{\text{high}} \\ \text{V}_{\text{CC}} = +24 \text{ V}, \text{ V}_{\text{EE}} = 0 \text{ V}, \text{T}_{\text{A}} = +25^{\circ}\text{C} \\ \text{V}_{\text{CC}} = +24 \text{ V}, \text{ V}_{\text{EE}} = 0 \text{ V}, \text{T}_{\text{A}} = \text{T}_{\text{low}} \text{ to } \text{T}_{\text{high}} \end{array} $ | V _{OH} V _{OL} | 22.5 22.5 - - | 22.7 - 1.3 - | - 1.5 1.6 | V |
| Input Common Mode Voltage Range $T_A = +25^{\circ}C$ $T_A = T_{low}$ to T_{high} | V _{ICR} | V _{EE} to (V _{CC} –1.0) V _{EE} to (V _{CC} –1.3) | | V | |
| Common Mode Rejection Ratio (R _S = 10 k) | CMRR | 70 | 90 | - | dB |
| Power Supply Rejection Ratio ($R_S = 100 \Omega$) | PSRR | 70 | 90 | - | dB |
| Power Supply Current $T_A = +25^{\circ}C$ $T_A = T_{low}$ to T_{high} | ۱ _D | | 8.0 _ | 10 14 | mA |

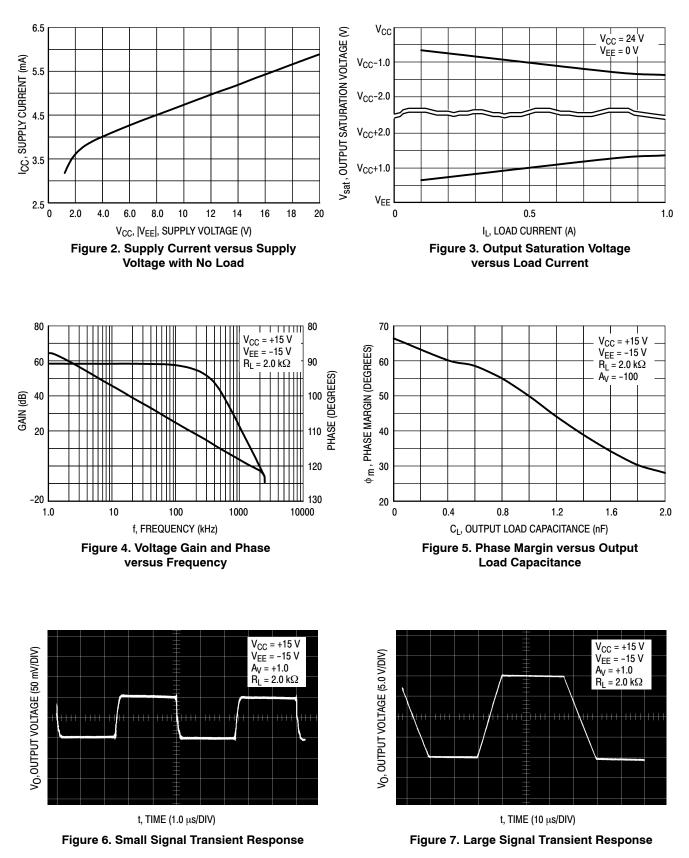
| DC ELECTRICAL CHARACTERISTICS (V _{CC} = +15 V, V _{EE} = -15 V, R _L | connected to ground, $T_A = -40^\circ$ to +125°C.) |
|---|--|
|---|--|

AC ELECTRICAL CHARACTERISTICS (V_{CC} = +15 V, V_{EE} = -15 V, R_L connected to ground, T_A = +25°C, unless otherwise noted.)

| Characteristics | Symbol | Min | Тур | Max | Unit |
|--|----------------|------------|----------|-----|---------|
| Slew Rate (V _{in} = -10 V to +10 V, R _L = 2.0 k, C _L = 100 pF) A_V = -1.0, T _A = T _{low} to T _{high} | SR | 1.0 | 1.4 | - | V/μs |
| Gain Bandwidth Product (f = 100 kHz, C _L = 100 pF, R _L = 2.0 k) T _A = 25°C T _A = T _{low} to T _{high} | GBW | 0.9 0.7 | 1.4 _ | | MHz |
| Phase Margin $T_J = T_{low}$ to T_{high} $R_L = 2.0 \text{ k}, C_L = 100 \text{ pF}$ | Φm | - | 65 | - | Degrees |
| Gain Margin $R_L = 2.0 \text{ k}, C_L = 100 \text{ pF}$ | A _m | - | 15 | - | dB |
| Equivalent Input Noise Voltage $R_S = 100 \ \Omega$, f = 1.0 to 100 kHz | e _n | - | 22 | - | nV/√Hz |
| Total Harmonic Distortion A _V = -1.0, R _L = 50 Ω , V _O = 0.5 VRMS, f = 1.0 kHz | THD | - | 0.02 | - | % |

NOTE: In case V_{EE} is disconnected before V_{CC}, a diode between V_{EE} and Ground is recommended to avoid damaging the device.

NCS2372



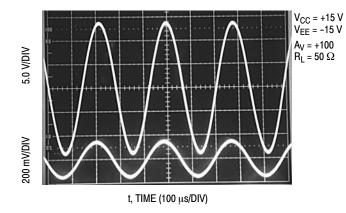


Figure 8. Sine Wave Response

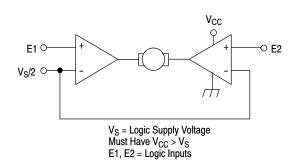
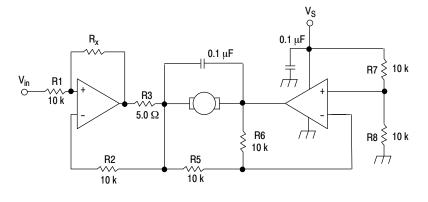


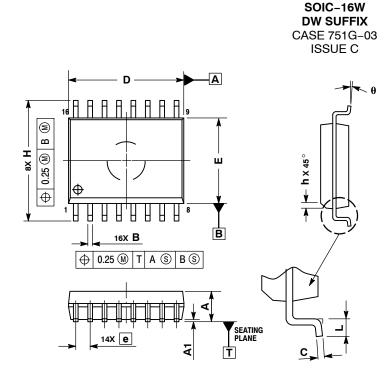
Figure 9. Bidirectional DC Motor Control with Microprocessor-Compatible Inputs



For circuit stability, ensure that $R_x > \frac{2R3 \cdot R1}{R_M}$ where, R_M = internal resistance of motor. The voltage available at the terminals of the motor is: $V_M = 2(V_1 - \frac{V_S}{2}) + |R_0| \cdot I_M$ where, $|R_0| = \frac{2R3 \cdot R1}{R_x}$ and I_M is the motor current.

Figure 10. Bidirectional Speed Control of DC Motors

PACKAGE DIMENSIONS



NOTES:

- DIMENSIONS ARE IN MILLIMETERS. INTERPRET DIMENSIONS AND TOLERANCES
- 2. PER ASME Y14.5M, 1994
- 3. DIMENSIONS D AND E DO NOT INLCUDE MOLD PROTRUSION. 4. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.
- 5
- DIMENSION B DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.13 TOTAL IN EXCESS OF THE B DIMENSION AT MAXIMUM MATERIAL CONDITION.

| | MILLIMETERS | | |
|-----|-------------|-------|--|
| DIM | MIN | MAX | |
| Α | 2.35 | 2.65 | |
| A1 | 0.10 | 0.25 | |
| В | 0.35 | 0.49 | |
| С | 0.23 | 0.32 | |
| D | 10.15 | 10.45 | |
| Е | 7.40 | 7.60 | |
| е | 1.27 BSC | | |
| Н | 10.05 | 10.55 | |
| h | 0.25 | 0.75 | |
| L | 0.50 | 0.90 | |
| θ | 0 ° | 7 ° | |

ON Semiconductor and 💷 are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative