N-Channel Power MOSFET 600 V, 0.95 Ω

Features

- Low ON Resistance
- Low Gate Charge
- ESD Diode–Protected Gate
- 100% Avalanche Tested
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant



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http://onsemi.com

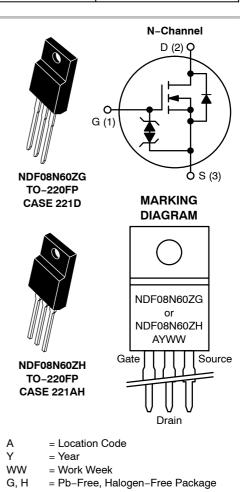
V _{DSS}	R _{DS(ON)} (MAX) @ 3.5 A
600 V	0.95 Ω

ABSOLUTE MAXIMUM RATINGS (T _C = 25°C unless otherwise noted)				
Rating	Symbol	NDF08N60Z	Unit	
Drain-to-Source Voltage	V _{DSS}	600	V	
Continuous Drain Current $R_{\theta JC}$ (Note 1)	Ι _D	8.4	А	
Continuous Drain Current $R_{\theta JC}$ T _A = 100°C (Note 1)	۱ _D	5.3	A	
Pulsed Drain Current, V _{GS} @ 10 V	I _{DM}	30	A	
Power Dissipation	PD	36	W	
Gate-to-Source Voltage	V _{GS}	30	V	
Single Pulse Avalanche Energy, I _D = 7.5 A	E _{AS}	235	mJ	
ESD (HBM) (JESD 22–A114)	V _{esd}	4000	V	
RMS Isolation Voltage (t = 0.3 sec., R.H. \leq 30%, T _A = 25°C) (Figure 14)	V _{ISO}	4500	V	
Peak Diode Recovery (Note 2)	dv/dt	4.5	V/ns	
Continuous Source Current (Body Diode)	۱ _S	7.5	А	
Maximum Temperature for Soldering Leads	ΤL	260	°C	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	–55 to 150	°C	

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Limited by maximum junction temperature

2. $I_D\,\leq\,7.5$ Å, di/dt ≤ 200 Å/µs, $V_{DD}\leq BV_{DSS},\,T_J\leq 150^\circ C.$



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

THERMAL RESISTANCE

Parameter	Symbol	NDF08N60Z	Unit
Junction-to-Case (Drain)	$R_{ ext{ heta}JC}$	3.5	°C/W
Junction-to-Ambient Steady State (Note 3)	R_{\thetaJA}	50	

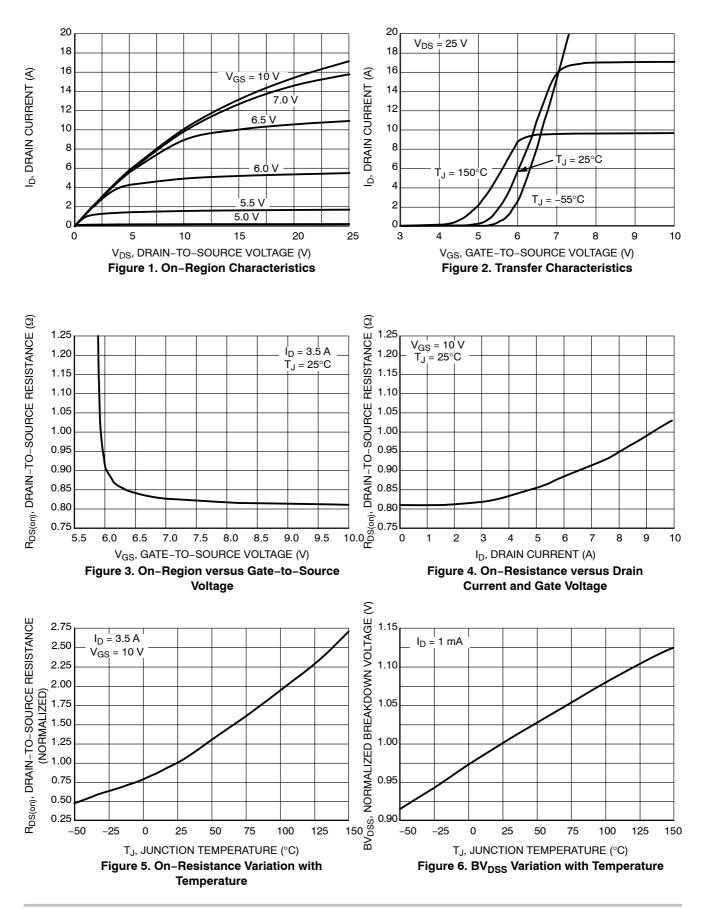
3. Insertion mounted

ELECTRICAL CHARACTERISTICS (T_J = 25° C unless otherwise noted)

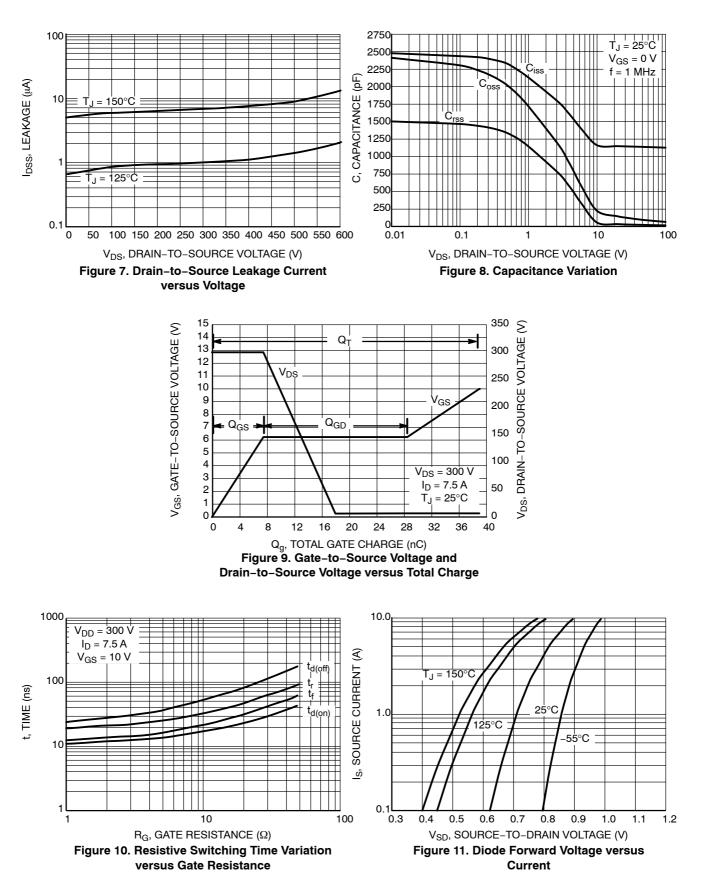
Characteristic	Test Conditions		Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 1 \text{ mA}$		BV _{DSS}	600			V
Breakdown Voltage Temperature Coefficient	Reference to 25°C, $I_D = 1 \text{ mA}$		$\Delta BV_{DSS}/\Delta T_{J}$		0.6		V/°C
Drain-to-Source Leakage Current	V_{DS} = 600 V, V_{GS} = 0 V	25°C 125°C	I _{DSS}			1 50	μΑ
Gate-to-Source Forward Leakage	V _{GS} = ±20 V		I _{GSS}			±10	μA
ON CHARACTERISTICS (Note 4)							
Static Drain-to-Source On-Resistance	V_{GS} = 10 V, I _D = 3.5 /	٩	R _{DS(on)}		0.82	0.95	Ω
Gate Threshold Voltage	$V_{DS} = V_{GS}$, $I_D = 100 \ \mu$	A	V _{GS(th)}	3.0	3.9	4.5	V
Forward Transconductance	V _{DS} = 15 V, I _D = 3.5 /	4	9FS		6.3		S
DYNAMIC CHARACTERISTICS							
Input Capacitance (Note 5)			C _{iss}	913	1140	1370	pF
Output Capacitance (Note 5)	$V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	Ι,	C _{oss}	105	129	160	
Reverse Transfer Capacitance (Note 5)	f = 1.0 MHz		C _{rss}	20	30	40	1
Total Gate Charge (Note 5)			Qg	20	39	58	nC
Gate-to-Source Charge (Note 5)			Q _{gs}	4	7.5	11.5	
Gate-to-Drain ("Miller") Charge (Note 5)	$\label{eq:VDD} \begin{array}{l} V_{DD} = 300 \; V, \; I_{D} = 7.5 \; A, \\ V_{GS} = 10 \; V \end{array}$		Q _{gd}	10	21	31	
Plateau Voltage			V _{GP}		6.2		V
Gate Resistance			Rg		1.6		Ω
RESISTIVE SWITCHING CHARACTER	ISTICS						
Turn-On Delay Time	V_{DD} = 300 V, I_D = 7.5 A, V_{GS} = 10 V, R_G = 5 Ω		t _{d(on)}		14		ns
Rise Time			t _r		22		
Turn-Off Delay Time			t _{d(off)}		36		
Fall Time			t _f		15		
SOURCE-DRAIN DIODE CHARACTER	ISTICS (T _C = 25°C unless oth	erwise note	ed)				
Diode Forward Voltage	I _S = 7.5 A, V _{GS} = 0 V		V _{SD}			1.6	V
Reverse Recovery Time	V _{GS} = 0 V, V _{DD} = 30 ^v	V	t _{rr}		320		ns
Reverse Recovery Charge	$I_{\rm S} = 7.5$ A, di/dt = 100 A/µs		Q _{rr}		2.2		μC

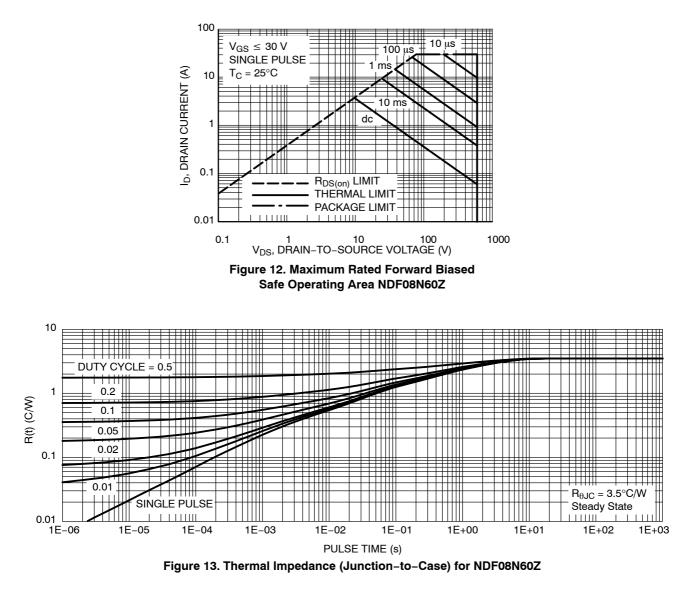
4. Pulse Width \leq 380 µs, Duty Cycle \leq 2%. 5. Guaranteed by design.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS





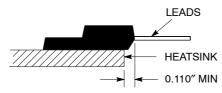


Figure 14. Isolation Test Diagram

Measurement made between leads and heatsink with all leads shorted together.

*For additional mounting information, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ORDERING INFORMATION

Order Number	Package	Shipping
NDF08N60ZG	TO-220FP (Pb-Free, Halogen-Free)	50 Units / Rail
NDF08N60ZH	TO-220FP (Pb-Free, Halogen-Free)	50 Units / Rail

PACKAGE DIMENSIONS

TO-220 FULLPAK CASE 221D-03 **ISSUE K**

-T- SEATING

→ C <



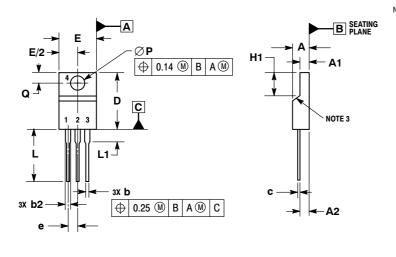
NOTES

221D-01 THRU 221D-02 OBSOLETE, NEW STANDARD 221D-03.

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	INCHES		MILLIM	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.617	0.635	15.67	16.12
В	0.392	0.419	9.96	10.63
С	0.177	0.193	4.50	4.90
D	0.024	0.039	0.60	1.00
F	0.116	0.129	2.95	3.28
G	0.100 BSC		2.54 BSC	
Н	0.118	0.135	3.00	3.43
J	0.018	0.025	0.45	0.63
Κ	0.503	0.541	12.78	13.73
Г	0.048	0.058	1.23	1.47
Ν	0.200 BSC		5.08	BSC
Ø	0.122	0.138	3.10	3.50
R	0.099	0.117	2.51	2.96
S	0.092	0.113	2.34	2.87
c	0.239	0.271	6.06	6.88

TO-220 FULLPACK, 3-LEAD CASE 221AH ISSUF B



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-Y-

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NOTES:

DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.

- CONTROLLING DIMENSION: MILLIMETERS
- 3. CONTOUR UNCONTROLLED IN THIS AREA
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH AND GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE TO BE MEASURED AT OUTERMOST
- EXTREME OF THE PLASTIC BODY. 5. DIMENSION b2 DOES NOT INCLUDE DAMBAR PROTRUSION. LEAD WIDTH INCLUDING PROTRUSION
- SHALL NOT EXCEED 2.00.

	MILLIMETERS		
DIM	MIN	MAX	
Α	4.30	4.70	
A1	2.50	2.90	
A2	2.50	2.70	
b	0.54	0.84	
b2	1.10	1.40	
C	0.49	0.79	
D	14.70	15.30	
Е	9.70	10.30	
е	2.54	BSC	
H1	6.70	7.10	
Г	12.70	14.73	
L1		2.80	
Ρ	3.00	3.40	
Q	2.80	3.20	

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