SLOS070 - JULY 1979 - REVISED SEPTEMBER 1990

- Equivalent Input Noise Voltage 3.5 nV/\Hz
- Unity-Gain Bandwidth . . . 10 MHz Typ
- Common-Mode Rejection Ratio 100 dB Typ
- High DC Voltage Gain . . . 100 V/mV Typ
- Peak-to-Peak Output Voltage Swing
 32 V Typ With V_{CC±} = ±18 V and R_L = 600 Ω
- High Slew Rate ... 13 V/µs Typ
- Wide Supply Voltage Range ±3 V to ±20 V
- Low Harmonic Distortion
- Designed to Be Interchangeable With Signetics NE5534, NE5534A, SE5534, and SE5534A

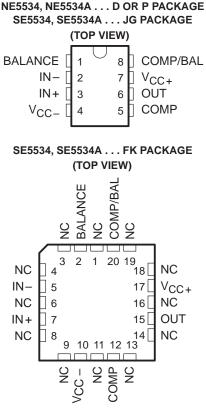
description

The NE5534, NE5534A, SE5534, and SE5534A are monolithic high-performance operational amplifiers combining excellent dc and ac characteristics. Some of the features include very low noise, high output drive capability, high unitygain and maximum-output-swing bandwidths, low distortion, and high slew rate.

These operational amplifiers are internally compensated for a gain equal to or greater than three. Optimization of the frequency response for various applications can be obtained by use of an external compensation capacitor between COMP and COMP/BAL. The devices feature inputprotection diodes, output short-circuit protection, and offset-voltage nulling capability.

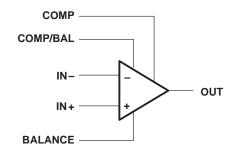
For the NE5534A, a maximum limit is specified for equivalent input noise voltage.

The NE5534 and NE5534A are characterized for operation from 0°C to 70°C. The SE5534 and SE5534A are characterized for operation over the full military temperature range of -55°C to 125°C.



NC - No internal connection

symbol



SE5534A FROM TI NOT RECOMMENDED FOR NEW DESIGNS

AVAILABLE OPTIONS										
	V _{IO} max AT 25°C	PACKAGE								
TA		SMALL OUTLINE (D)	CERAMIC (FK)	CERAMIC DIP (JG)	PLASTIC DIP (P)					
0°C to 70°C	0°C to 70°C 4 mV NE5534D NE5534AD		—	_	NE5534P NE5534AP					
– 55°C to 125°C	2 mV		SE5534FK SE5534AFK	SE5534JG SE5534AJG						

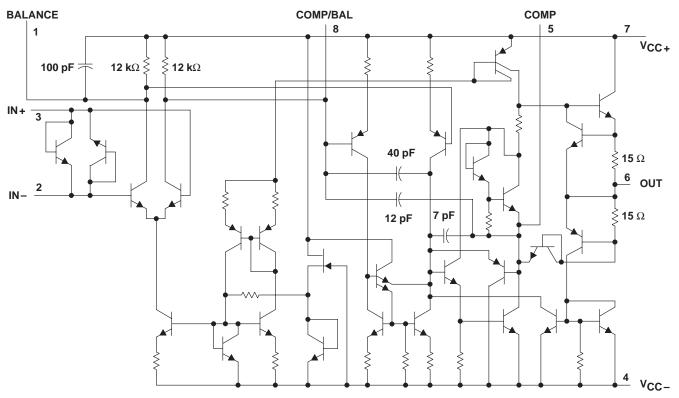
The D package is available taped and reeled. Add the suffix R to the device type (e.g., NE5534DR).

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



SLOS070 - JULY 1979 - REVISED SEPTEMBER 1990

schematic



All component values shown are nominal. Pin numbers shown are for D, JG, and P packages.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V _{CC+} (see Note 1) Supply voltage, V _{CC-} (see Note 1)		
Input voltage either input (see Notes 1 ar	nd 2)	V _{CC+}
Input current (see Note 3)		±10 mA
Duration of output short circuit (see Note	e 4)	unlimited
Continuous total power dissipation		. See Dissipation Rating Table
Operating free-air temperature range:	NE5534, NE5534A	0°C to 70°C
	SE5534, SE5534A	– 55°C to 125°C
Storage temperature range		– 65°C to 150°C
Case temperature for 60 seconds: FK pa	ackage	260°C
Lead temperature range 1,6 mm (1/16 in	nch) from case for 60 seconds: JG pa	ckage 300°C
Lead temperature range 1,6 mm (1/16 in	nch) from case for 10 seconds: D or F	P package 260°C

NOTES: 1. All voltage values, except differential voltages, are with respect to the midpoint between V_{CC+} and V_{CC-}.

- 2. The magnitude of the input voltage must never exceed the magnitude of the supply voltage.
- 3. Excessive current will flow if a differential input voltage in excess of approximately 0.6 V is applied between the inputs unless some limiting resistance is used.
- 4. The output may be shorted to ground or to either power supply. Temperature and/or supply voltages must be limited to ensure the maximum dissipation rating is not exceeded.



SLOS070 - JULY 1979 - REVISED SEPTEMBER 1990

DISSIPATION RATING TABLE								
PACKAGE	T _A ≤ 25°C POWER RATING	DERATING FACTOR ABOVE T _A = 25°C	T _A = 70°C POWER RATING	T _A = 125°C POWER RATING				
D	725 mW	5.8 mW/°C	464 mW	N/A				
FK (see Note 5)	1375 mW	11.0 mW/°C	880 mW	275 mW				
JG	1050 mW	8.4 mW/°C	672 mW	210 mW				
Р	1000 mW	8.0 mW/°C	640 mW	N/A				

NOTE 5: For the FK package, power rating and derating factor will vary with actual mounting technique used. The values stated here are believed to be conservative.

recommended operating conditions

	MIN	NOM MAX	UNIT
Supply voltage, V _{CC+}	5	15	V
Supply voltage, V _{CC} _	- 5	– 15	V

electrical characteristics, V_{CC} \pm = ± 15 V, T_A = 25°C (unless otherwise noted)

	PARAMETER			NE5534, NE5534A			SE5534, SE5534A			UNIT
FARAMETER		TEST CONDITIONS [†]		MIN	TYP	MAX	MIN	TYP	MAX	UNIT
Vie	Input offset voltage	$V_{O} = 0,$ $T_{A} = 2$			0.5	4		0.5	2	mV
VIO	input onset voltage	R _S = 50 Ω	T _A = Full range			5			3	IIIV
lio.	Input offset current	$V_{O} = 0$	T _A = 25°C		20	300		10	200	nA
IIO		vO = 0	T _A = Full range			400			500	ΠA
lin	Input bias current	$V_{O} = 0$	T _A = 25°C		500	1500		400	800	nA
ΙB		V U = 0	$T_A = Full range$			2000			1500	
VICR	Common-mode input voltage range			±12	±13		±12	±13		V
	Maximum peak-to-peak	R ₁ ≥ 600 Ω	$V_{CC\pm} = \pm 15 V$	24	26		24	26		V
VO(PP)	output voltage swing	R[≥ 000 Ω	V _{CC±} = ±18 V	30	32		30	32		V
A. (5	Large-signal differential voltage amplification	$V_{O} = \pm 10 \text{ V},$ $R_{L} \ge 600 \Omega$	T _A = 25°C	25	100		50	100		V/mV
AVD			T _A = Full range	15			25			
Δ.	Small-signal differential voltage amplification	f = 10 kHz	C _C = 0		6			6		V/mV
A _{vd}			C _C = 22 pF		2.2			2.2		V/IIIV
	Maximum-output-swing	$V_{O} = \pm 10 V$,	C ^C = 0		200			200		
Вом		V _O = ±10 V,	C _C = 22 pF		95			95		kHz
DOM	bandwidth	$\begin{array}{l} V_{CC\pm} = \ \pm 18 \ V, \\ R_L \geq 600 \ \Omega, \end{array}$	$V_{O} = \pm 14 V$, $C_{C} = 22 pF$		70			70		1412
B ₁	Unity-gain bandwidth	C _C = 22 pF,	C _L = 100 pF		10			10		MHz
rj	Input resistance			30	100		50	100		kΩ
z ₀	Output impedance	$A_{VD} = 30 \text{ dB},$ $C_C = 22 \text{ pF},$	R _L ≥ 600 Ω, f = 10 kHz		0.3			0.3		Ω
CMRR	Common-mode rejection ratio	$V_{O} = 0,$ R _S = 50 Ω	$V_{IC} = V_{ICR} min,$	70	100		80	100		dB
k SVR	Supply voltage rejection ratio $(\Delta V_{CC}/\Delta V_{IO})$	$V_{CC+}=\pm 9 V to \pm 15 V,$ $V_{O}=0,$	R _S = 50 Ω	80	100		86	100		dB
los	Output short-circuit current				38			38		mA
1	Cupply ourrent	V _O = 0,	T _A = 25°C		4	8		4	6.5	A
ICC	Supply current	No load T _A = Full range							9	mA

[†] All characteristics are measured under open-loop conditions with zero common-mode input voltage unless otherwise specified. Full range is $T_A = 0^{\circ}C$ to 70°C for NE5534 and NE5534A and $-55^{\circ}C$ to 125°C for SE5534 and SE5534A.

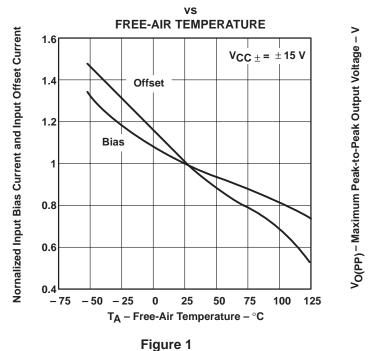


SLOS070 - JULY 1979 - REVISED SEPTEMBER 1990

operating characteristics, V_{CC} \pm = ±15 V, T_A = 25°C

PARAMETER		TEOT			SE5534, NE5534			SE5534A, NE5534A		
		TEST CONDITIONS		MIN	TYP	MAX	MIN	TYP	MAX	UNIT
SR	Slew rate at unity gain	C _C = 0			13		13			V/µs
SK		C _C = 22 pF			6			6		ν/μ5
tr	Rise time	$V_{I} = 50 \text{ mV},$	A _{VD} = 1, C _C = 22 pF,	20		20			ns	
	Overshoot factor	R _L = 600 Ω, C _L = 100 pF			20%			20%		
tr	Rise time	$V_{I} = 50 \text{ mV},$	A _{VD} = 1, C _C = 47 pF,		50			50		ns
	Overshoot factor	R _L = 600 Ω, C _L = 500 pF	CC = 47 pr,		35%			35%		
		f = 30 Hz	lz		7			5.5	7	
Vn	Equivalent input noise voltage	f = 1 kHz			4			3.5	4.5	nV/√Hz
In		f = 30 Hz f = 1 kHz		2.5 0.6			1.5			
	Equivalent input noise current					0.4			pA/√Hz	
F	Average noise figure	$R_S = 5 k\Omega$,	f = 10 Hz to 20 kHz					0.9		dB

TYPICAL CHARACTERISTICS[†]



NORMALIZED INPUT BIAS CURRENT AND INPUT OFFSET CURRENT

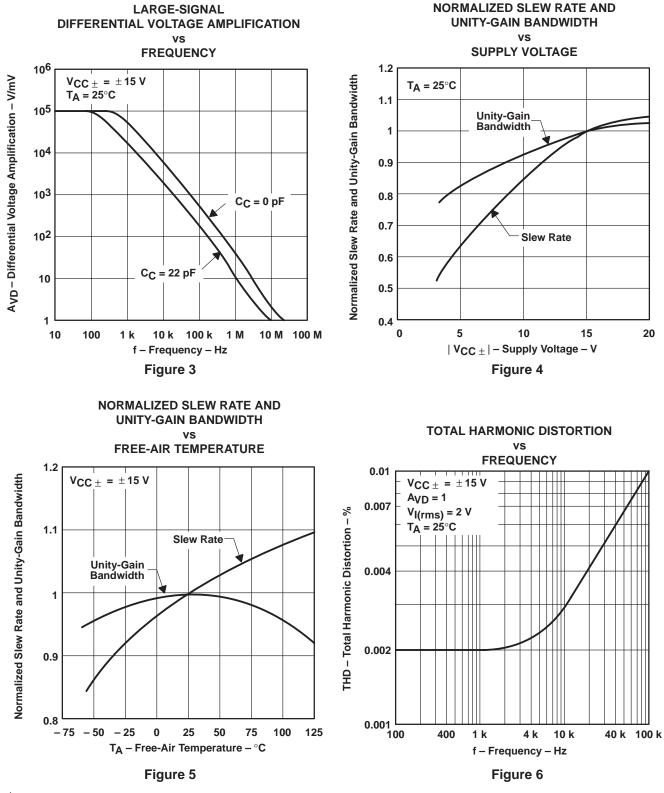
MAXIMUM PEAK-TO-PEAK OUTPUT VOLTAGE vs FREQUENCY 30 CC = 0 25 20 15 10 C_C = 22 pF 5 C_C = 47 pF $V_{CC \pm} = \pm 15 V$ T_A = 25°C 0 100 1 k 10 k 100 k 1 M f - Frequency - Hz

Figure 2

[†] Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.



SLOS070 - JULY 1979 - REVISED SEPTEMBER 1990



TYPICAL CHARACTERISTICS[†]

[†] Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.



SLOS070 - JULY 1979 - REVISED SEPTEMBER 1990

TYPICAL CHARACTERISTICS

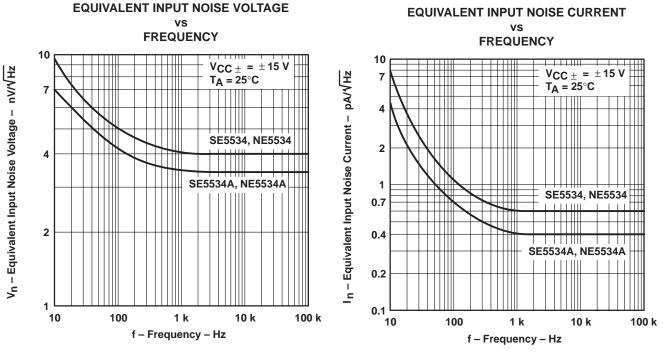
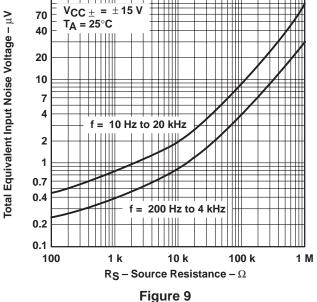


Figure 7

Figure 8







IMPORTANT NOTICE

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.

Copyright © 1998, Texas Instruments Incorporated