INTEGRATED CIRCUITS



Product specification

IC17 Data Handbook

1993 Dec 15



Philips Semiconductors

NE/SA576

DESCRIPTION

The NE/SA576 is a unity gain level programmable compandor designed for low power applications. The NE576 is internally configured as an expandor and a compressor to minimize external component count.

The NE576 can operate at 1.8V. During normal operations, the NE576 can operate from at least a 2V battery. If the battery voltage drops to 1.8V, this part will still continue to function, however, turning on the part at a V_{CC} of 1.8V requires two external resistors to bring V_{REF} to half V_{CC}. One resistor connects between V_{CC} and V_{REF}; the other connects from V_{REF} to ground. A typical value for these external resistors is approximately 20k. A lower value can be used, but the power consumption will go up.

The NE576 is available in a 14-pin plastic DIP and SO packages.

FEATURES

- Operating voltage range: 1.8V to 7V
- Low power consumption (1.4mA @ 3.6V)
- Over 80dB of dynamic range
- Wide input/output swing capability (rail-to-rail)
- Low external component count
- ESD hardened

PIN CONFIGURATION

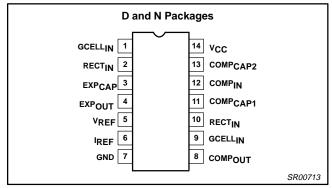


Figure 1. Pin Configuration

APPLICATIONS

- Cordless telephone
- Consumer audio
- Wireless microphones
- Modems
- Electric organs
- Hearing aids
- Automatic level control

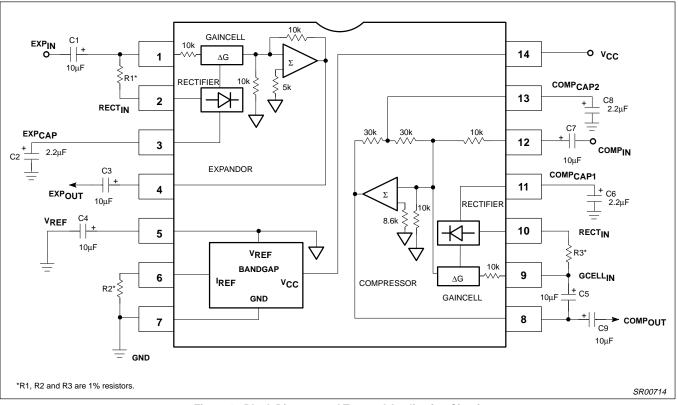
ORDERING INFORMATION

DESCRIPTION	TEMPERATURE RANGE	ORDER CODE	DWG #
14-Pin Plastic Dual In-Line Package (DIP)	0 to +70°C	NE576N	SOT27-1
14-Pin Plastic Small Outline (SO)	0 to +70°C	NE576D	SOT108-1
14-Pin Plastic Dual In-Line Package (DIP)	–40 to +85°C	SA576N	SOT27-1
14-Pin Plastic Small Outline (SO)	–40 to +85°C	SA576D	SOT108-1

ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER	RAT	UNITS	
STWBUL	FARAMETER	NE576	SA576	01113
V _{CC}	Supply voltage	8	8	V
T _A	Operating ambient temperature range	0 to +70	-40 to +85	°C
T _{STG}	Storage temperature range	-65 to +150	-65 to +150	°C
θ_{JA}	Thermal impedance DIP SO	90 125	90 125	°C/W °C/W

NE/SA576



BLOCK DIAGRAM and TEST AND APPLICATION CIRCUIT

Figure 2. Block Diagram and Test and Application Circuit

ELECTRICAL CHARACTERISTICS

 $T_A = 25^{\circ}C$, $V_{CC} = 3.6VDC$, compandor 0dB level = $-20dBV = 100mV_{RMS}$, output load $R_L = 10k\Omega$, Freq = 1kHz, unless otherwise specified. R1, R2 and R3 are 1% resistors.

				LIMITS		
SYMBOL	PARAMETER	TEST CONDITIONS		UNITS		
			MIN	TYP	MAX	1
V _{CC}	Supply voltage ¹		2	3.6	7	V
Icc	Supply current	No signal $R_2 = 100 k\Omega$		1.4	3	mA
V _{REF}	Reference voltage ²	V _{CC} = 3.6V		1.8		V
RL	Summing amp output load		10			kΩ
THD	Total harmonic distortion	1kHz, 0dB, BW = 3.5kHz		0.25	1.5	%
E _{NO}	Expandor output noise voltage	BW = 20kHz, $R_S = 0\Omega$		10	30	μV
0dB	Unity gain level	0dB at 1kHz	-1.5	0.18	1.5	dB
V _{OS}	Output voltage offset	No signal	-150	1	150	mV
	Expandor output DC shift	No signal to 0dB	-100	7	100	mV
	Tracking error relative to 0dB output	-20dB expandor	-1.0	0.3	1.0	dB
	Crosstalk, COMP to EXP	1kHz, 0dB, C _{REF} = 10μF		-80		dB
V	Output swing low			0.2		V
Vo	Output swing high			V _{CC} – 0.2		V

NOTE:

1. Operation down to V_{CC} = 1.8V is possible, see description on front page of NE576 data sheet.

2. Reference voltage, V_{REF} , is typically at 1/2 V_{CC} .

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TYPICAL PERFORMANCE CHARACTERISTICS

 $V_{CC} = 3.6V, T_A = 25^{\circ}C, R1 = R3 = 7.15k\Omega, R2 = 100k\Omega, 0dB level = 100mV, Freq. = 1kHz$

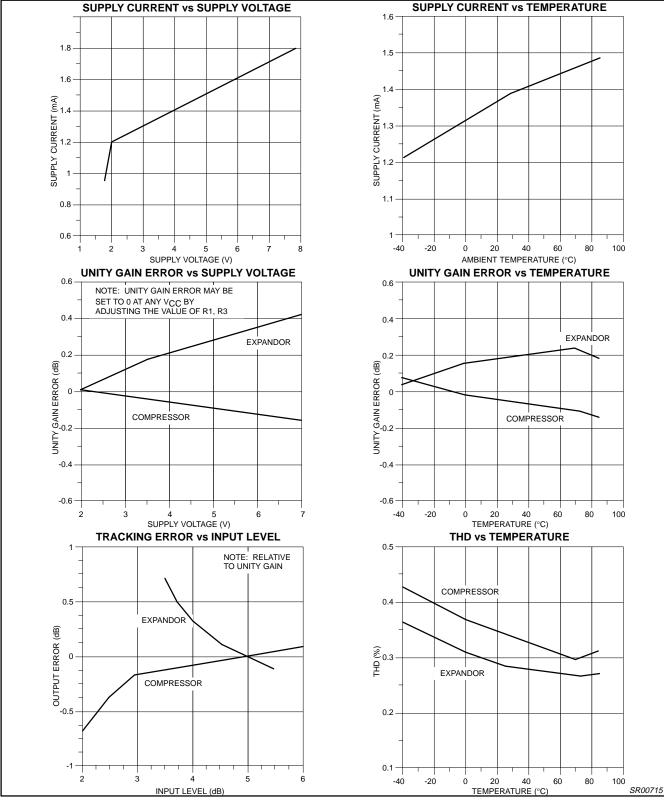
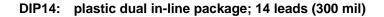
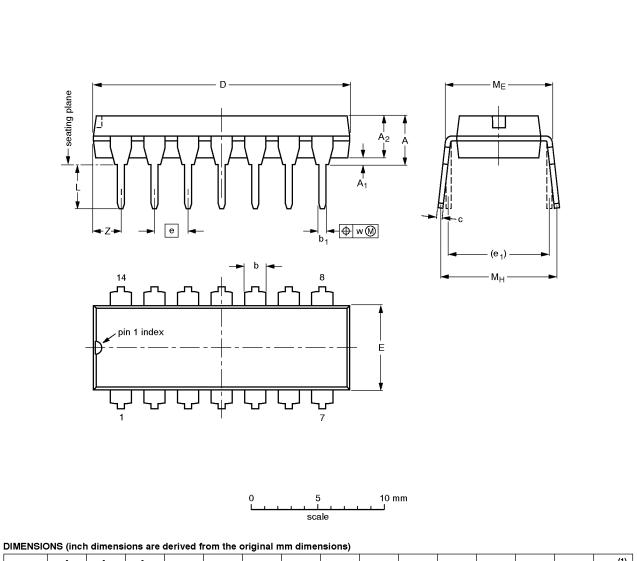


Figure 3. Typical Performance Characteristics





UNIT	A max.	A ₁ min.	A ₂ max.	ь	b ₁	c	D ⁽¹⁾	E ⁽¹⁾	e	e ₁	L	ME	м _н	w	Z ⁽¹⁾ max.
mm	4.2	0.51	3.2	1.73 1.13	0.53 0.38	0.36 0.23	19.50 18.55	6.48 6.20	2.54	7.62	3.60 3.05	8.25 7.80	10.0 8.3	0.254	2.2
inches	0.17	0.020	0.13	0.068 0.044	0.021 0.015	0.014 0.009	0.77 0.73	0.26 0.24	0.10	0.30	0.14 0.12	0.32 0.31	0.39 0.33	0.01	0.087

Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT27-1	050G04	MO-001AA				-92-11-17 95-03-11

Product specification

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SOT27-1

SO14: plastic small outline package; 14 leads; body width 3.9 mm

DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	Α ₁	A ₂	Α3	b _p	с	D ⁽¹⁾	E ⁽¹⁾	е	Η _E	L	Lp	Q	v	w	у	Z ⁽¹⁾	θ
mm	1.75	0.25 0.10	1.45 1.25	0.25	0.49 0.36	0.25 0.19	8.75 8.55	4.0 3.8	1.27	6.2 5.8	1.05	1.0 0.4	0.7 0.6	0.25	0.25	0.1	0.7 0.3	8°
inches		0.0098 0.0039		0.01		0.0098 0.0075		0.16 0.15	0.050	0.24 0.23	0.041	0.039 0.016	0.028 0.024	0.01	0.01	0.004	0.028 0.012	0°

Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT108-1	076E06S	MS-012AB				91-08-13- 95-01-23

0 2.5 5 mm L L

SOT108-1

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	DEFINITIONS								
Data Sheet Identification	Product Status	Definition							
Objective Specification	Formative or in Design	This data sheet contains the design target or goal specifications for product development. Specifications may change in any manner without notice.							
Preliminary Specification	Preproduction Product	This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.							
Product Specification	Full Production	This data sheet contains Final Specifications. Philips Semiconductors reserves the right to make changes at any time without notice, in order to improve design and supply the best possible product.							

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