Schmitt Inverter

The NL17SG14 MiniGate[™] is an advanced high-speed CMOS Schmitt Inverter in ultra-small footprint.

The NL17SG14 input structure provides protection when voltages up to 4.6 V are applied.

Features

- Wide Operating V_{CC} Range: 0.9 V to 3.6 V
- High Speed: $t_{PD} = 3.7 \text{ ns}$ (Typ) at $V_{CC} = 3.0 \text{ V}$, $C_L = 15 \text{ pF}$
- Low Power Dissipation: $I_{CC} = 0.5 \mu A$ (Max) at $T_A = 25^{\circ}C$
- 4.6 V Overvoltage Tolerant (OVT) Input Pins
- Ultra-Small Packages
- These are Pb-Free and Halide-Free Devices

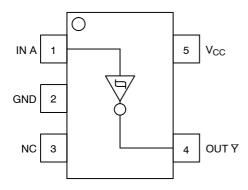


Figure 1. Pinout

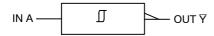


Figure 2. Logic Symbol



ON Semiconductor®

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MARKING DIAGRAM



SOT-953 CASE 527AE



2 = Specific Device Code

M = Month Code

PIN ASSIGNMENT				
1	IN A			
2	GND			
3	NC			
4	OUT \overline{Y}			
5	V _{CC}			

FUNCTION TABLE

A Input	▼ Output
L	Н
Н	L

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

1

MAXIMUM RATINGS

Symbol	Parameter		Value	Unit
V _{CC}	DC Supply Voltage		-0.5 to +5.5	V
V _{IN}	DC Input Voltage		-0.5 to +4.6	V
V _{OUT}	DC Output Voltage	Output at High or Low State Power-Down Mode (V _{CC} = 0 V)	-0.5 to V _{CC} + 0.5 -0.5 to +4.6	V
I _{IK}	DC Input Diode Current	V _{IN} < GND	-20	mA
I _{OK}	DC Output Diode Current	V _{OUT} < GND	-20	mA
lout	DC Output Source/Sink Current		±20	mA
I _{CC}	DC Supply Current per Supply Pin		±20	mA
I _{GND}	DC Ground Current per Ground Pin		±20	mA
T _{STG}	Storage Temperature Range		−65 to +150	°C
TL	Lead Temperature, 1 mm from Case for 10 Seconds		260	°C
TJ	Junction Temperature Under Bias		+150	°C
MSL	Moisture Sensitivity		Level 1	
F _R	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	
V _{ESD}	ESD Withstand Voltage	Human Body Model (Note 2) Machine Model (Note 3)	>2000 >100	V
I _{LATCHUP}	Latchup Performance Above V _{CC} an	d Below GND at 125°C (Note 4)	±100	mA

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2-ounce copper trace with no air flow.

2. Tested to EIA/JESD22-A114-A.

- 3. Tested to EIA/JESD22-A115-A.
- 4. Tested to EIA/JESD78.

RECOMMENDED OPERATING CONDITIONS

Symbol	Characteristics	Min	Max	Unit	
V _{CC}	Positive DC Supply Voltage		0.9	3.6	V
V _{IN}	Digital Input Voltage		0.0	3.6	V
V _{OUT}	Output Voltage Output at High or Low Power–Down Mode (V _{CC} =	State 0 V)	0.0 0.0	V _{CC} 3.6	V
T _A	Operating Temperature Range		-55	+125	°C
Δt / ΔV	Input Transition Rise or Fail Rate V_{CC} = 3.3 V \pm	0.3 V	0	10	ns/V

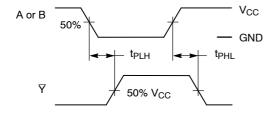
DC ELECTRICAL CHARACTERISTICS

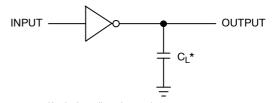
			Vcc		T _A = 25°C	, = 25°C		-55°C ≤ T _A ≤ 125°C		
Symbol Parameter	c	ondition	(V)	Min	Тур	Max	Min	Max	Unit	
V _{T+} Positive–Going			0.9		0.7	0.86		0.87		
	Input Threshold Voltage			1.1		0.81	0.95		1	
				1.4		0.94	1.16		1.2	1
				1.65		1.06	1.25		1.3	V
				2.3		1.36	1.6		1.65	
				3.0		1.8	2.05		2.1	
V_{T-}	Negative-			0.9	0.09	0.23		0.08		
	Going Input Threshold			1.1	0.15	0.33		0.12		
	Voltage			1.4	0.3	0.47		0.25		1
				1.65	0.35	0.6		0.3		٧
				2.3	0.55	0.85		0.5		-
				3.0	0.95	1.13	1	0.9		
V_{H}	Hysteresis			0.9	0.15	0.5	0.75	0.2	0.8	
	Voltage			1.1	0.15	0.5	0.75	0.2	0.8	
				1.4	0.15	0.5	0.75	0.2	0.8	١.,
				1.65	0.15	0.5	0.75	0.2	0.8	V
				2.3	0.15	0.5	0.75	0.2	0.8	
			3.0	0.25	0.65	0.85	0.3	0.9		
V _{OH}	High-Level	V _{IN} =	I _{OH} = -20 μA	0.9	0.75			0.75		V
	Output Voltage	V _{IH} or V _{IL}	$I_{OH} = -0.3 \text{ mA}$	1.1 to 1.3	0.75 x V _{CC}			0.75 x V _{CC}		
			I _{OH} = −1.7 mA	1.4 to 1.6	0.75 x V _{CC}			0.75 x V _{CC}		
			$I_{OH} = -3.0 \text{ mA}$	1.65 to 1.95	V _{CC} - 0.45			V _{CC} - 0.45		
			$I_{OH} = -4.0 \text{ mA}$	2.3 to 2.7	2.0			2.0		
			$I_{OH} = -8.0 \text{ mA}$	3.0 to 3.6	2.48			2.48		
V_{OL}	Low-Level	V _{IN} =	I_{OL} = 20 μ A	0.9			0.1		0.1	V
	Output Voltage	V _{IH} or V _{IL}	I _{OL} = 0.3 mA	1.1 to 1.3			0.25 x V _{CC}		0.25 x V _{CC}	
		I _{OL} = 1.7 mA	1.4 to 1.6			0.25xV cc		0.25 x V _{CC}		
			I _{OL} = 3.0 mA	1.65 to 1.95			0.45		0.45	
			I _{OL} = 4.0 mA	2.3 to 2.7			0.4		0.4	
			I _{OL} = 8.0 mA	3.0 to 3.6			0.4		0.4	
I _{IN}	Input Leakage Current	0 ≤ V _{IN} ≤ 3.6 V		0 to 3.6			±0.1		±1.0	μΑ
I _{CC}	Quiescent Supply Current	V _{IN} =	V _{IN} = V _{CC} or GND				0.5		10.0	μΑ

AC ELECTRICAL CHARACTERISTICS Input $t_r = t_f = 3.0 \text{ ns}$

				-	T _A = 25 °C	:		= 0 +125°C	
Symbol	Parameter	Test Condition	V _{CC} (V)	Min	Тур	Max	Min	Max	Unit
t _{PLH} ,	Propagation Delay, A or ₹	C _L = 10 pF,	0.9	-	27.3	_	-	-	ns
t _{PHL}	A OF Y	$R_L = 1 M\Omega$	1.1 to 1.3	-	13.0	22.6	1.0	35.9	
			1.4 to 1.6	-	7.5	10.5	1.0	11.3	
			1.65 to 1.95	-	6.0	7.8	1.0	8.2	
			2.3 to 2.7	-	4.3	5.4	1.0	5.8	
			3.0 to 3.6	-	3.5	4.4	1.0	4.6	
		C _L = 15 pF,	0.9	-	29.5	-	_	-	ns
		$R_L = 1 M\Omega$	1.1 to 1.3	-	14.3	25.1	1.0	41.6	
			1.4 to 1.6	-	8.0	11.5	1.0	12.6	
			1.65 to 1.95	-	6.3	8.4	1.0	8.7	
			2.3 to 2.7	-	4.6	5.7	1.0	6.1	
			3.0 to 3.6	-	3.7	4.6	1.0	5.0	
		C _L = 30 pF,	0.9	-	40.5	_	_	-	ns
		$R_L = 1 M\Omega$	1.1 to 1.3	-	19.6	35.7	1.0	58.1	
			1.4 to 1.6	-	10.7	15.8	1.0	17.6	
			1.65 to 1.95	-	7.8	10.7	1.0	11.7	
			2.3 to 2.7	-	5.4	6.9	1.0	8.1	
			3.0 to 3.6	=	4.3	5.2	1.0	6.1	
C _{IN}	Input Capacitance		0 to 3.6		3	_	-	-	pF
C _O	Output Capacitance	V _O = GND	0		3	_	-	-	pF
C _{PD}	Power Dissipation Capacitance (Note 5)	f = 10 MHz	0.9 to 3.6	-	4	-	-	-	pF

^{5.} C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the dynamic operating current consumption without load. Average operating current can be obtained by the equation: $I_{CC(OPR)} = C_{PD} \bullet V_{CC} \bullet f_{in} + I_{CC}$. C_{PD} is used to determine the no–load dynamic power consumption; $P_D = C_{PD} \bullet V_{CC}^2 \bullet f_{in} + I_{CC} \bullet V_{CC}$.





*Includes all probe and jig capacitance.

A 1 MHz square input wave is recommended for propagation delay tests.

Figure 3. Switching Waveform

Figure 4. Test Circuit

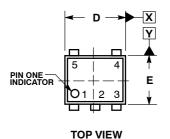
ORDERING INFORMATION

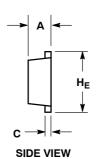
Device	Package	Shipping [†]		
NL17SG14P5T5G	SOT-953 (Pb-Free)	8000 / Tape & Reel		

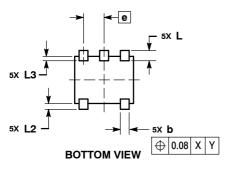
[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

SOT-953 CASE 527AE **ISSUE E**





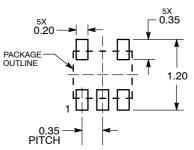


NOTES

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD
- FLASH, PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS					
DIM	MIN	NOM	MAX			
Α	0.34	0.37	0.40			
b	0.10	0.15	0.20			
C	0.07	0.12	0.17			
D	0.95	1.00	1.05			
E	0.75	0.80	0.85			
е		0.35 BS	С			
HE	0.95	1.00	1.05			
L	0.175 REF					
L2	0.05	0.10	0.15			
L3			0.15			

SOLDERING FOOTPRINT*



DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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