Ultra-Low 0.4 Ω SPDT **Analog Switch**

The NLAS5157 is Single Pole Double Throw (SPDT) switch designed for audio systems in portable applications. The NLAS5157 features Ultra-Low R_{ON} of 0.4 Ω typical at V_{CC} = 3.0 V and 0.15 Ω R_{ON} Flatness for +3.0 V supply across temperature. This device also has a broad V_{CC} operating range of 1.65 V to 4.3 V, ideal for battery-powered devices.

The NLAS5157 is protected on all pins with 8 kV Human Body Model ESD protection. This allows the device to be placed in a variety of locations, including near the interface, without risk of damage.

Features

- $R_{ON} = 0.4 \Omega$ Typical @ $V_{CC} = 3.0 V$
- V_{CC} Range: 1.65 V to 4.3 V
- 8 kV Human Body Model ESD on All Pins
- These are Pb-Free Devices

Typical Applications

- Mobile Phones
- Portable Devices



ON Semiconductor®

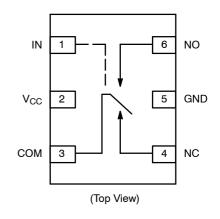
http://onsemi.com



Х = Specific Device Code М



PIN ASSIGNMENTS



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 7 of this data sheet.

PIN DESCRIPTION

PIN #	Name	Direction	Description
1	IN	Input	Control Input Select Line
2	V _{CC}	Power	Voltage Supply
3	COM	I/O	Common Signal Line
4	NC	I/O	Normally Closed Signal Line
5	GND	Power	Ground
6	NO	I/O	Normally Open Signal Line

TRUTH TABLE

Control Input	Function
L	NC Connected to COM
Н	NO Connected to COM

MAXIMUM RATINGS

Symbol	Pins	Rating	Value	Condition	Unit
V _{CC}	V _{CC}	Positive DC Supply Voltage	0.5 to +5.5		V
V _{IS}	NO, NC, or COM	Analog Signal Voltage	-0.5 to V _{CC} + 0.5		V
V _{IN}	IN	Control Input Voltage	-0.5 to +5.5		V
IIS_CON	NO, NC, or COM	Analog Signal Continuous Current	± 300	Closed Switch	mA
I _{IS_PK}	NO, NC, or COM	Analog Signal Peak Current	± 500	10% Duty Cycle	mA
I _{IN}	IN	Control Input Current	±20		mA
T _{STG}		Storage Temperature Range	-65 to 150		°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

RECOMMENDED OPERATING CONDITIONS

Symbol	Pins	Parameter	Value	Condition	Unit
V _{CC}	V _{CC}	Positive DC Supply Voltage	1.65 to 4.3		V
V _{IS}	NO, NC, or COM	Analog Signal Voltage	GND to V _{CC}		V
V _{IN}	IN	Control Input Voltage (OVT) Overvoltage Tolerance	GND to 4.3		V
T _A		Operating Temperature Range	-40 to +85		°C
t _r , t _f		Input Rise or Fall Time	20	V _{CC} = 1.6 V – 2.7 V	ns/V
			10	$V_{CC} = 3.0 \text{ V} - 4.5 \text{ V}$	

Minimum and maximum values are guaranteed through test or design across the **Recommended Operating Conditions**, where applicable. Typical values are listed for guidance only and are based on the particular conditions listed for each section, where applicable. These conditions are valid for all values found in the characteristics tables unless otherwise specified in the test conditions.

ESD PROTECTION

Pins	Description	Minimum Voltage
All Pins	Human Body Model	7 kV

DC ELECTRICAL CHARACTERISTICS

CONTROL INPUT (Typical: T = 25° C; V_{CC} = 3.0 V)

				V _{CC}	–40°C to +85°C				
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit	
V _{IH}	IN	Control Input High		1.8 – 2.7 3.0 4.3	1.0 1.3 1.6			V	
V _{IL}	IN	Control Input Low		1.8 – 2.7 3.0 4.3			0.4 0.5 0.6	V	
I _{IN}	IN	Control Input Leakage	$0 \le V_{IN} \le V_{CC}$	4.3		±0.1	±0.5	μΑ	

SUPPLY CURRENT AND LEAKAGE (Typical: T = 25° C; V_{CC} = 3.0 V)

				V _{cc}	-4	0°C to +85	5°C	
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit
I _{NO/NC} (OFF)	NC, NO	OFF State Leakage		4.3		±10	±100	nA
I _{СОМ} (ON)	СОМ	ON State Leakage	$\label{eq:VIN} \begin{array}{l} V_{IN} = V_{IL} \mbox{ or } V_{IH} \\ V_{NO} = 0.3 \mbox{ V or } 4.0 \mbox{ V with} \\ V_{NC} \mbox{ Floating or } \\ V_{NC} = 0.3 \mbox{ V or } 4.0 \mbox{ V with} \\ V_{NO} \mbox{ Floating} \\ V_{COM} = 0.3 \mbox{ V or } 4.0 \mbox{ V} \end{array}$	4.3		±20	±100	nA
I _{CC}	V _{CC}	Quiescent Supply	V_{IN} and V_{IS} = V_{CC} or GND I_{D} = 0 A	1.65 – 4.3		±0.1	±1.0	μΑ
I _{OFF}	IN	Power Off Leakage	$V_{IN} = 4.3 \text{ V or GND}$	0		±0.5	±1.0	μA

ON RESISTANCE (Typical: T = 25°C; V_{CC} = 3.0 V)

				V _{CC}	-40	0°C to +85	i°C	
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit
R _{ON}	NO, NC COM	ON Resistance	$I_{ON} = -100 \text{ mA}$ $V_{IS} = 0 \text{ to } V_{CC}$	3.0 4.3		0.4 0.35	0.6 0.5	Ω
R _{FLAT}	NO, NC COM	R _{ON} Flatness	$I_{ON} = -100 \text{ mA}$ $V_{IS} = 0 \text{ to } V_{CC}$	3.0 4.3		0.12 0.15	0.16 0.17	Ω
ΔR_{ON}	NO, NC COM	R _{ON} Matching	$I_{ON} = -100 \text{ mA}$ $V_{IS} = 0 \text{ to } V_{CC}$	3.0 4.3		0.08 0.08	0.1 0.1	Ω

AC ELECTRICAL CHARACTERISTICS

				V _{CC}	–40°C to +85°C			
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit
t _{ON}	IN to NC or NO	Turn On Time		2.3 – 4.3		30	40	ns
t _{OFF}	IN to NC or NO	Turn Off Time		2.3 - 4.53		18	25	ns
t _{BBM}	IN to NC or NO	Break Before Make		3.0	2	15		ns
BW		-3dB Bandwidth	C _L = 5 pF	1.65 – 4.3		36		MHz

TIMING/FREQUENCY (Typical: T = 25°C; V_{CC} = 3.0 V, R_L = 50 Ω , C_L = 35 pF, f = 1 MHz)

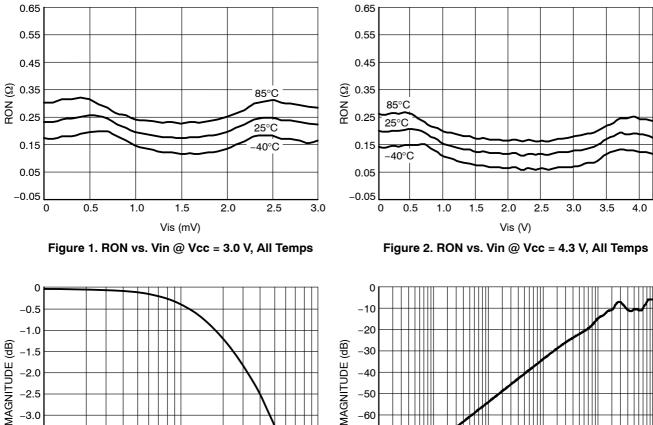
ISOLATION AND THD (Typical: T = 25°C; V_{CC} = 3.0 V, RL = 50 Ω , CL = 5 pF, f = 1 MHz)

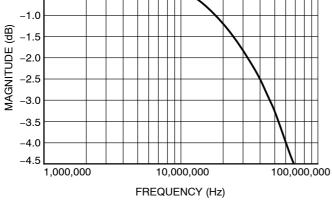
				V _{cc}	–40°C to +85°C			_
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit
Q		Charge Injection		1.65 – 4.3		38		рС
THD		Total Harmonic Distortion		3.0		0.02		%
O _{IRR}	NO	Off Isolation	$V_{IN} = 0$ V_{NO} or V_{NC} (pk-pk) = 1.0 V	1.65 – 4.3		-54		dB
Xtalk	COM to COMy	Non-Adjacent Channel	$V_{NO} \text{ or } V_{NC} \text{ (pk-pk)} = 1.0 \text{ V}$	1.65 – 4.3		-54		dB

$\textbf{CAPACITANCE} \text{ (Typical: } T = 25^{\circ}\text{C}\text{; } \text{V}_{\text{CC}} = 3.0 \text{ V}\text{, } \text{R}_{\text{L}} = 50 \ \Omega\text{, } \text{C}_{\text{L}} = 5 \text{ pF}\text{, } \text{f} = 1 \text{ MHz}\text{)}$

				V _{CC}	–40°C to +85°C			
Symbol	Pins	Parameter	Test Conditions	(V)	Min	Тур	Max	Unit
C _{IN}	IN	Control Input		0 V		3.5		pF
C _{ON}	NC to COM	Through Switch	V _{IN} = 0V	3.0 V		95		pF
C _{OFF}	NC, NC	Unselected Port	V_{IS} = 3.0V, V_{IN} = 3.0 V	3.0 V		47		pF







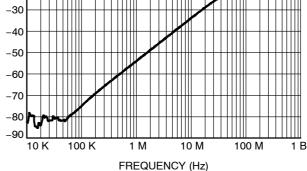


Figure 3. Bandwidth vs. Frequency

Figure 4. Cross-Talk vs. Frequency @ 25°C

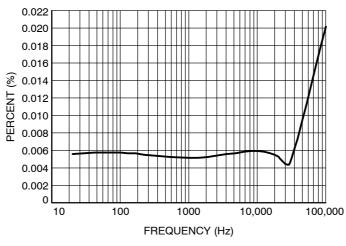
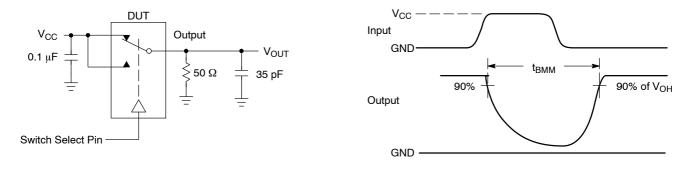


Figure 5. THD vs. Frequency @ Vin = 1 Vpp





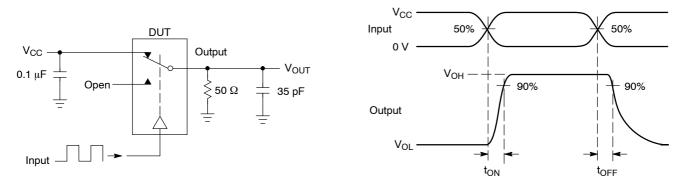
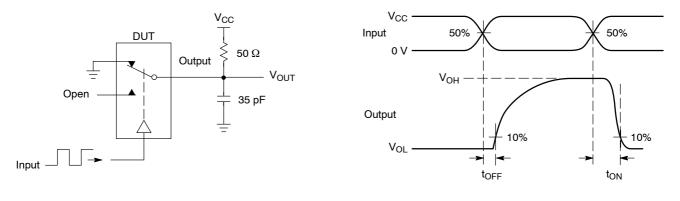
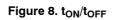
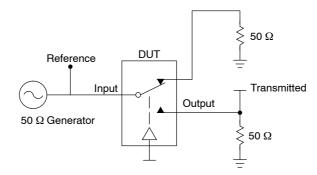


Figure 7. t_{ON}/t_{OFF}





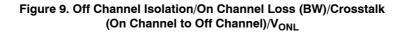


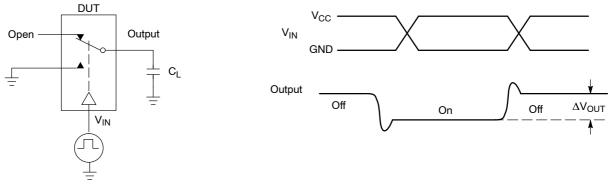
Channel switch control/s test socket is normalized. Off isolation is measured across an off channel. On loss is the bandwidth of an On switch. V_{ISO} , Bandwidth and V_{ONL} are independent of the input signal direction.

$$\begin{split} V_{ISO} &= \text{Off Channel Isolation} = 20 \text{ Log } \left(\frac{V_{OUT}}{V_{IN}} \right) \text{ for } V_{IN} \text{ at } 100 \text{ kHz} \\ V_{ONL} &= \text{On Channel Loss} = 20 \text{ Log } \left(\frac{V_{OUT}}{V_{IN}} \right) \text{ for } V_{IN} \text{ at } 100 \text{ kHz to } 50 \text{ MHz} \end{split}$$

Bandwidth (BW) = the frequency 3 dB below V_{ONL}

 V_{CT} = Use V_{ISO} setup and test to all other switch analog input/outputs terminated with 50 Ω







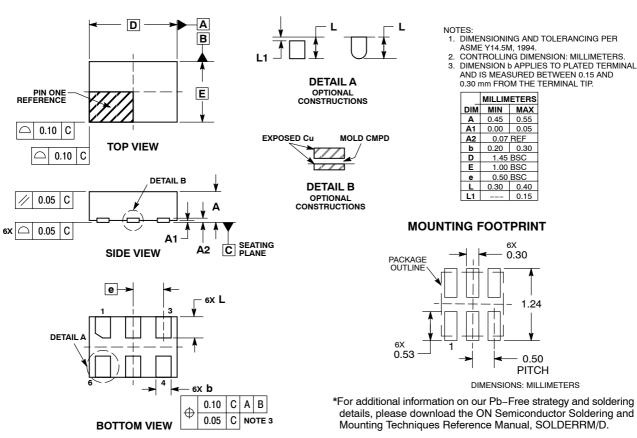
DEVICE ORDERING INFORMATION

Device Order Number	Package Type	Tape & Reel Size [†]
NLAS5157MUTCG	UDFN6 (Pb-Free)	3000/Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

UDFN6, 1.45x1.0, 0.5P CASE 517AQ-01 **ISSUE O**



ON Semiconductor and 💷 are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All or operating parameters, including "Typical" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Ce Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com

6X

0.30

1.24

0.50 PITCH

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative