Dual Complementary Transistors

General Purpose PNP Transistor and NPN Transistors with Monolithic Bias Network

NSM21156DW6T1G contains a single PNP transistor and a monolithic bias network NPN transistor with two resistors; a series base resistor and a base-emitter resistor. This device is designed to replace multiple transistors and resistors on customer boards by integrating these components into a single device. NSM21156DW6T1G is housed in a SC-88/SOT-363 package which is ideal for low power surface mount applications in space constrained applications.

Features

- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- Q1: NPN BRT, R1 = R2 = 10 k
- Q2: PNP
- This is a Pb-Free Device

Applications

- Logic Switching
- Amplification
- Driver Circuits
- Interface Circuits

MAXIMUM RATINGS

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

Rating - Q1 (NPN BRT)	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	50	Vdc
Collector-Emitter Voltage	V_{CEO}	50	Vdc
Collector Current	Ic	100	mAdc
Rating - Q2 (PNP)	Symbol	Value	Unit
Collector - Base Voltage	V _{(BR)CBO}	-80	Vdc
Collector - Emitter Voltage	V _{(BR)CEO}	-65	Vdc
Emitter - Base Voltage	V _{(BR)EBO}	-5.0	Vdc
Collector Current - Continuous	I _C	-100	mAdc

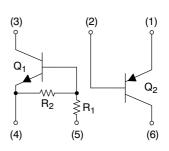
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

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SC-88/SOT-363 CASE 419B STYLE 1

MARKING DIAGRAM



N4 = Device Code
M = Date Code*

Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or position may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
NSM21156DW6T1G	SC-88 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

THERMAL CHARACTERISTICS

Characteristic (One Junction Heated)	Symbol	Max	Unit
Total Device Dissipation $T_A = 25^{\circ}C$	P _D	180 (Note 1)	mW
Derate above 25°C		1.44 (Note 1)	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	692 (Note 1)	°C/W
Characteristic (Both Junctions Heated)	Symbol	Max	Unit
Total Device Dissipation, T _A = 25°C Derate above 25°C	P _D	230 1.83	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{ heta JA}$	544	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

^{1.} FR-4 @ Minimum Pad of 1.45 mm², 1 oz Cu.

ELECTRICAL CHARACTERISTICS – Q1 NPN BRT ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector-Base Cutoff Current (V _{CB} = 50 V, I _E = 0)	I _{CBO}	-	-	100	nAdc
Collector-Emitter Cutoff Current (V _{CE} = 50 V, I _B = 0)	I _{CEO}	-	-	500	nAdc
Emitter-Base Cutoff Current $(V_{EB} = 6.0 \text{ V}, I_{C} = 0)$	I _{EBO}	-	-	0.5	mAdc
Collector-Base Breakdown Voltage ($I_C = 10 \mu A, I_E = 0$)	V _{(BR)CBO}	50	-	-	Vdc
Collector-Emitter Breakdown Voltage (Note 2) (I _C = 2.0 mA, I _B = 0)	V _{(BR)CEO}	50	-	-	Vdc
ON CHARACTERISTICS (Note 2)					
DC Current Gain (V _{CE} = 10 V, I _C = 5.0 mA)	h _{FE}	35	60	-	
Collector-Emitter Saturation Voltage ($I_C = 10 \text{ mA}, I_B = 0.3 \text{ mA}$)	V _{CE(sat)}	-	-	0.25	Vdc
Output Voltage (on) (V _{CC} = 5.0 V, V _B = 2.5 V, R _L = 1.0 k Ω)	V _{OL}	-	-	0.2	Vdc
Output Voltage (off) $(V_{CC} = 5.0 \text{ V}, V_B = 0.5 \text{ V}, R_L = 1.0 \text{ k}\Omega)$	V _{OH}	4.9	-	-	Vdc
Input Resistor	R1	7.0	10	13	kΩ
Resistor Ratio	R1/R2	0.8	1.0	1.2	

^{2.} Pulse Test: Pulse Width < 300 μs, Duty Cycle < 2.0%

ELECTRICAL CHARACTERISTICS - Q2 PNP (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit		
OFF CHARACTERISTICS							
Collector - Emitter Breakdown Voltage (I _C = -10 mA)	V _{(BR)CEO}	-65	-	-	V		
Collector - Emitter Breakdown Voltage (I _C = -10 μA, V _{EB} = 0)	V _{(BR)CES}	-80	-	-	V		
Collector - Base Breakdown Voltage (I _C = -10 μA)	V _{(BR)CBO}	-80	-	-	V		
Emitter – Base Breakdown Voltage ($I_E = -1.0 \mu A$)	V _{(BR)EBO}	-5.0	-	-	V		
Collector Cutoff Current ($V_{CB} = -30 \text{ V}$) ($V_{CB} = -30 \text{ V}$, $T_A = 150^{\circ}\text{C}$)	Ісво		- -	-15 -4.0	nA μA		
ON CHARACTERISTICS							
DC Current Gain $ (I_C = -10 \; \mu\text{A, V}_{CE} = -5.0 \; \text{V}) $ $ (I_C = -2.0 \; \text{mA, V}_{CE} = -5.0 \; \text{V}) $	h _{FE}	- 220	150 290	- 475	-		
Collector – Emitter Saturation Voltage ($I_C = -10$ mA, $I_B = -0.5$ mA) ($I_C = -100$ mA, $I_B = -5.0$ mA)	V _{CE(sat)}	- -	- -	-0.3 -0.65	V		
Base – Emitter Saturation Voltage ($I_C = -10$ mA, $I_B = -0.5$ mA) ($I_C = -100$ mA, $I_B = -5.0$ mA)	V _{BE(sat)}	- -	-0.7 -0.9	-	V		
Base – Emitter On Voltage ($I_C = -2.0 \text{ mA}, V_{CE} = -5.0 \text{ V}$) ($I_C = -10 \text{ mA}, V_{CE} = -5.0 \text{ V}$)	V _{BE(on)}	-0.6 -		-0.75 -0.82	V		

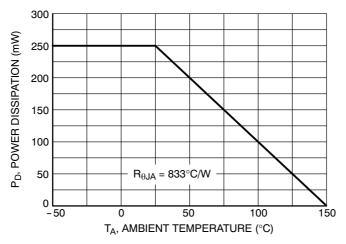
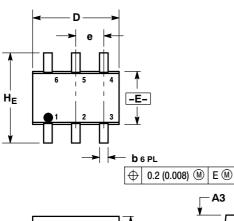
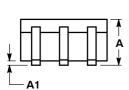


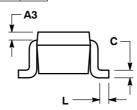
Figure 1. Derating Curve

PACKAGE DIMENSIONS

SC-88 (SOT-363) CASE 419B-02 **ISSUE V**







NOTES

- 1. DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- 3. 419B-01 OBSOLETE, NEW STANDARD 419B-02.

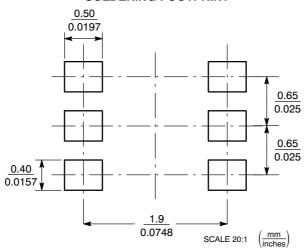
	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.80	0.95	1.10	0.031	0.037	0.043	
A1	0.00	0.05	0.10	0.000	0.002	0.004	
А3		0.20 REF			0.008 REF		
b	0.10	0.21	0.30	0.004	0.008	0.012	
С	0.10	0.14	0.25	0.004	0.005	0.010	
D	1.80	2.00	2.20	0.070	0.078	0.086	
Е	1.15	1.25	1.35	0.045	0.049	0.053	
е	(0.65 BSC			0.026 BSC		
L	0.10	0.20	0.30	0.004	0.008	0.012	
HE	2.00	2.10	2.20	0.078	0.082	0.086	

STYLE 1:

PIN 1. EMITTER 2

- BASE 2 COLLECTOR 1
- 3. EMITTER 1
- BASE 1
- COLLECTOR 2

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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