NPN Transistor with Zener Diode

Features

• These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Typical Applications

- Driving Circuit
- Switching Applications

MAXIMUM RATINGS – NPN TRANSISTOR

Symbol	Value	Unit
V _{CEO}	40	V
V _{CBO}	60	V
V _{EBO}	6.0	V
Ι _C	600	mA
I _{CM}	900	mA
	V _{CEO} V _{CBO} V _{EBO} I _C	$\begin{array}{c c} V_{CEO} & 40 \\ V_{CBO} & 60 \\ V_{EBO} & 6.0 \\ I_{C} & 600 \\ \end{array}$

MAXIMUM RATINGS - ZENER DIODE

Rating	Symbol	Value	Unit
Forward Voltage @ I _F = 10 mA	V _F	0.9	V

THERMAL CHARACTERISTICS

Rating	Symbol	Max	Unit
Total Device Dissipation FR-5 Board, (Note 1) @ T _A = 25°C	PD	380	mW
Thermal Resistance from Junction-to-Ambient	$R_{\theta JA}$	328	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

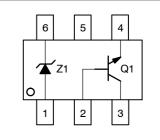
1. FR-4 Minimum Pad.



ON Semiconductor®

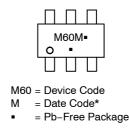
http://onsemi.com

NPN Transistor with Zener Diode





MARKING DIAGRAM



(Note: Microdot may be in either location) *Date Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
NSM6056MT1G	SC–74 (Pb–Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

NPN TRANSISTOR – ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

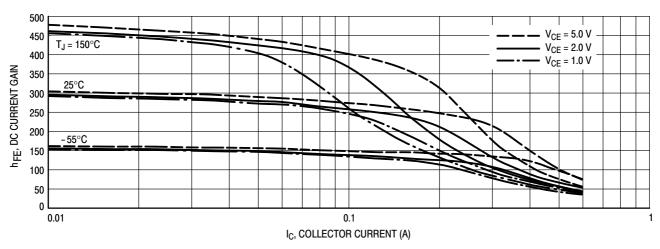
Cha	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS		•			•
Collector – Emitter Breakdown Voltage	V _{(BR)CEO}	40	-	Vdc	
Collector – Base Breakdown Voltage	V _{(BR)CBO}	60	-	Vdc	
Emitter-Base Breakdown Voltage	$(I_E = 0.1 \text{ mAdc}, I_C = 0)$	V _{(BR)EBO}	6.0	-	Vdc
Base Cutoff Current	$(V_{CE}$ = 35 Vdc, V_{EB} = 0.4 Vdc)	I _{BEV}	-	0.1	μAdc
Collector Cutoff Current	$(V_{CE}$ = 35 Vdc, V_{EB} = 0.4 Vdc)	I _{CEX}	-	0.1	μAdc
ON CHARACTERISTICS (Note 3)					
DC Current Gain		h _{FE}	20 40 80 100 40	- - 300 -	-
Collector – Emitter Saturation Voltage	$(I_{C} = 150 \text{ mAdc}, I_{B} = 15 \text{ mAdc})$ $(I_{C} = 500 \text{ mAdc}, I_{B} = 50 \text{ mAdc})$	V _{CE(sat)}		0.4 0.75	Vdc
Base – Emitter Saturation Voltage	V _{BE(sat)}	0.75	0.95 1.2	Vdc	
SMALL-SIGNAL CHARACTERISTIC	S	•			·
Current-Gain - Bandwidth Product	(I _C = 20 mAdc, V _{CE} = 10 Vdc, f = 100 MHz)	f _T	250	-	MHz
Collector-Base Capacitance	$(V_{CB} = 5.0 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$	C _{cb}	-	6.5	pF
Emitter-Base Capacitance	$(V_{EB} = 0.5 \text{ Vdc}, I_C = 0, f = 1.0 \text{ MHz})$	C _{eb}	-	30	pF
Input Impedance	(I _C = 1.0 mAdc, V _{CE} = 10 Vdc, f = 1.0 kHz)	h _{ie}	1.0	15	kΩ
Voltage Feedback Ratio	(I _C = 1.0 mAdc, V _{CE} = 10 Vdc, f = 1.0 kHz)	h _{re}	0.1	8.0	X 10 ⁻⁴
Small-Signal Current Gain	(I _C = 1.0 mAdc, V _{CE} = 10 Vdc, f = 1.0 kHz)	h _{fe}	40	500	-
Output Admittance	$(I_{C} = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h _{oe}	1.0	30	μmhos
SWITCHING CHARACTERISTICS					
Delay Time	(V _{CC} = 30 Vdc, V _{EB} = 2.0 Vdc,	t _d	-	15	
Rise Time	$I_{\rm C}$ = 150 mAdc, $I_{\rm B1}$ = 15 mAdc)	t _r	-	20	ns
Storage Time	(V _{CC} = 30 Vdc, I _C = 150 mAdc,	t _s	-	225	
Fall Time	I _{B1} = I _{B2} = 15 mAdc)	t _f	-	30	ns

2. Pulse Test: Pulse Width \leq 300 $\mu s,$ Duty Cycle \leq 2.0%.

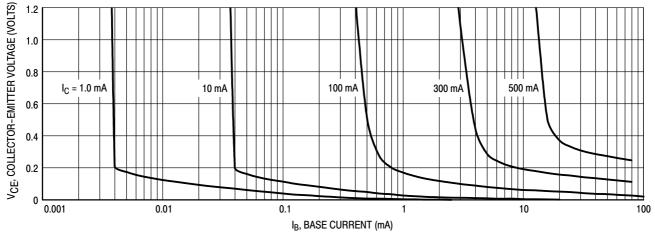
ZENER DIODE – ELECTRICAL CHARACTERISTICS (V_F = 0.9 Max @ I_F = 10 mA for all types)

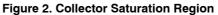
	Test	Zener Vo	ltage VZ	Z _{ZK} I _Z = 0.5	Z _{ZT} I _Z = IZT @ 10%	Ma IR @		d _{VZ} /dt @ I _{ZT1}		C pF Max @
Device	Current Izt mA	Min	Max	mA Ω Max	Mod Ω Max	μA	v	Min	Max	V _R = 0 f = 1 MHz
NSM6056MT1G	5.0	5.49	5.73	200	40	1.0	2.0	-2.0	2.5	200

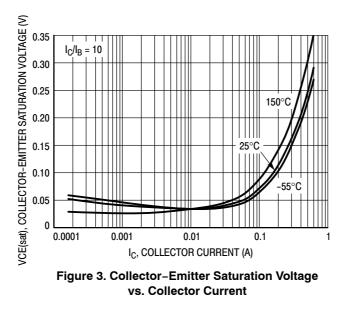
TYPICAL ELECTRICAL CHARACTERISTICS - NPN TRANSISTOR

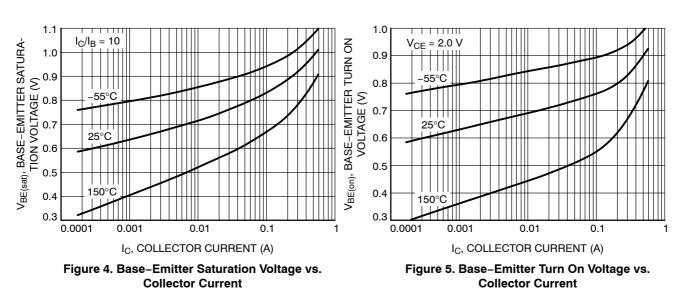












TYPICAL ELECTRICAL CHARACTERISTICS – NPN TRANSISTOR

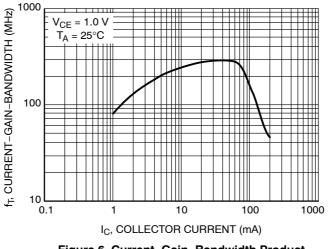
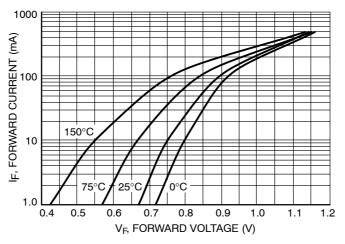


Figure 6. Current–Gain–Bandwidth Product

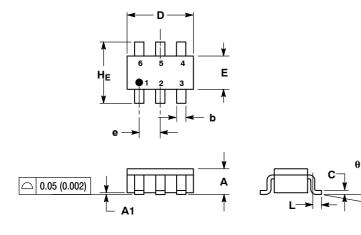






PACKAGE DIMENSIONS

SC-74 CASE 318F-05 ISSUE M



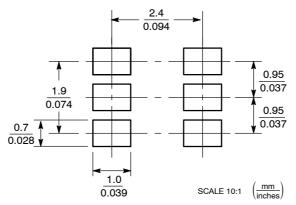
NOTES:

- I. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: INCH
- CONTROLLING DIMENSION: INCH.
 MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH
- THICKNESS, MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

4.	318F-0	01, -02, -	03 OBSOL	ETE. NEV	V STANDARE) 318F–04.

	м	ILLIMETE	RS		INCHES	
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.90	1.00	1.10	0.035	0.039	0.043
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.25	0.37	0.50	0.010	0.015	0.020
С	0.10	0.18	0.26	0.004	0.007	0.010
D	2.90	3.00	3.10	0.114	0.118	0.122
E	1.30	1.50	1.70	0.051	0.059	0.067
е	0.85	0.95	1.05	0.034	0.037	0.041
L	0.20	0.40	0.60	0.008	0.016	0.024
HE	2.50	2.75	3.00	0.099	0.108	0.118
θ	0°	-	10°	0°	-	10°

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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