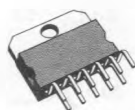# 10 + 10 W HIGH QUALITY STEREO AMPLIFIER

- HIGH OUTPUT POWER (10 + 10 W min. @ d = 0.5 %)
- HIGH CURRENT CAPABILITY (up to 3.5 A)
- THERMAL OVERLOAD PROTECTION
- SPACE AND COST SAVING : VERY LOW NUMBER OF EXTERNAL COMPONENTS AND SIMPLE MOUNTING THANKS TO THE MULTIWATT ® PACKAGE.
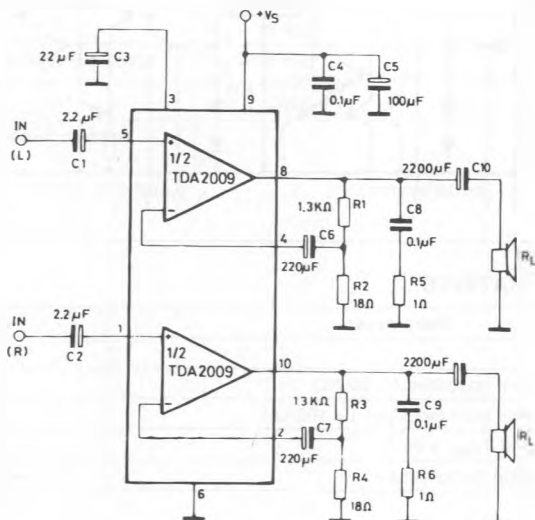

MULTIWATT-11
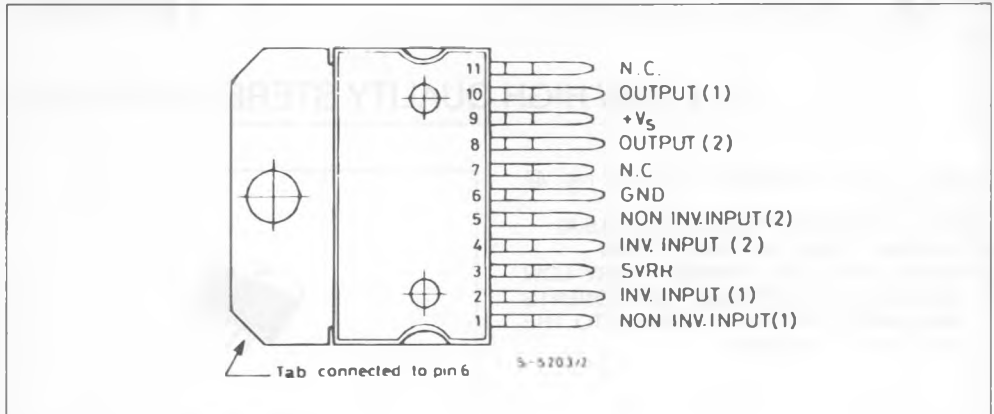
ORDER CODE : TDA2009

DESCRIPTION

The TDA2009 is class AB dual Hi-Fi Audio power amplifier assembled in Multiwatt ® package, specially designed for high quality stereo application as Hi-Fi and music centers.
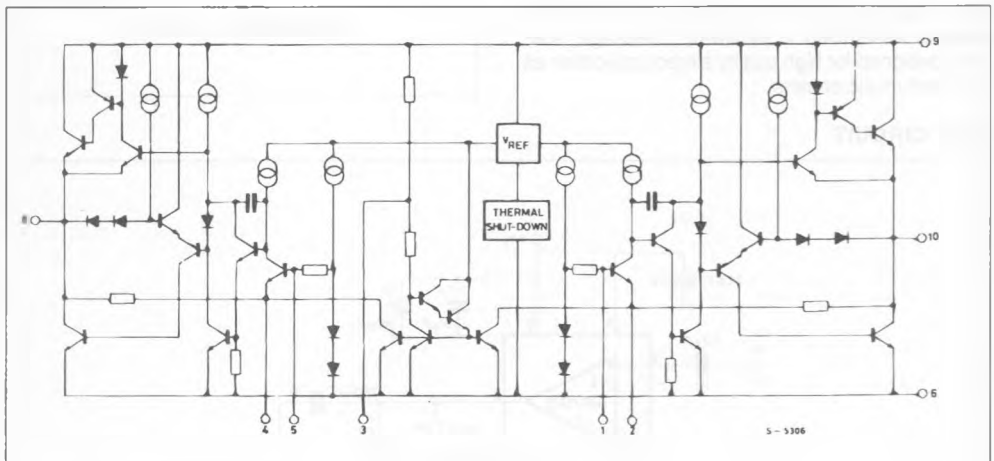
TEST CIRCUIT

S - 5189/1

PIN CONNECTION (top view)

11	N.C.
10	OUTPUT (1)
9	+V_S
8	OUTPUT (2)
7	N.C
6	GND
5	NON INV.INPUT (2)
4	INV. INPUT (2)
3	SVRK
2	INV. INPUT (1)
1	NON INV.INPUT(1)

Tab connected to pin 6

S-5203/2

SCHEMATIC DIAGRAM

S – 5306

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_s	Supply Voltage	28	V
I_o	Output Peak Current (repetitive $f \geq 20$ Hz)	3.5	A
I_o	Output Peak Current (non repetitive, $t = 100\ \mu s$)	4.5	A
P_{tot}	Power Dissipation at $T_{case} = 90\ °C$	20	W
$T_{stg},\ T_j$	Storage and Junction Temperature	-40 to 150	°C

THERMAL DATA

$R_{th\ j-case}$	Thermal Resistance Junction-case	Max	3	°C/W

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ELECTRICAL CHARACTERISTICS (refer to the stereo application circuit, $T_{amb} = 25\ °C$, $V_s = 23\ V$, $G_v = 36\ dB$, unless otherwise specified)

Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Unit
V_s	Supply Voltage			8		28	V
V_o	Quiescent Output Voltage	$V_s = 23\ V$			11		V
I_d	Total Quiescent Drain Current	$V_s = 23\ V$			55	120	mA
P_o	Output Power (each channel)	$f = 50\ Hz$ to $16\ KHz$ $d = 0.5\ \%$ $V_s = 23\ V$ $\qquad\qquad R_L = 4\ \Omega$ $\qquad\qquad R_L = 8\ \Omega$ $V_s = 18\ V \qquad R_L = 4\ \Omega$ $\qquad\qquad R_L = 8\ \Omega$		10 5.5	11 6.5 6.5 4		W W W W
d	Distortion (each channel)	$f = 1\ KHz$ $V_s = 23\ V \qquad R_L = 4\ \Omega$ $P_o = 100\ mW$ to $8\ W$ $V_s = 23\ V \qquad R_L = 8\ \Omega$ $P_o = 100\ mW$ to $3\ W$			0.05 0.05		%
CT	Cross Talk (°°°)	$R_L = \infty$ $R_g = 10\ K\Omega$	$f = 1\ KHz$	50	65		dB
			$f = 10\ KHz$	40	50		dB
V_i	Input Saturation Voltage (rms)			300			mV
R_i	Input Resistance	$f = 1\ KHz \qquad$ Non Inverting Input		70	200		$K\Omega$
f_L	Low Frequency Roll off ($-3\ dB$)	$R_L = 4\ \Omega$			20		Hz
f_H	High Frenquency Roll off ($-3dB$)				80		KHz
G_v	Voltage Gain (closed loop)	$f = 1\ KHz$		35.5	36	36.5	dB
ΔG_v	Closed Loop Gain Matching				0.5		dB
e_N	Total Input Noise Voltage	$R_g = 10\ K\Omega$ (°)			1.5		μV
		$R_g = 10\ K\Omega$ (°°)			2.5	8	μV
SVR	Supply Voltage Rejection (each channel)	$R_g = 10\ K\Omega$ $f_{ripple} = 100\ Hz$ $V_{ripple} = 0.5\ V$		43	55		dB
T_J	Thermal Shut-down Junction Temperature				145		°C

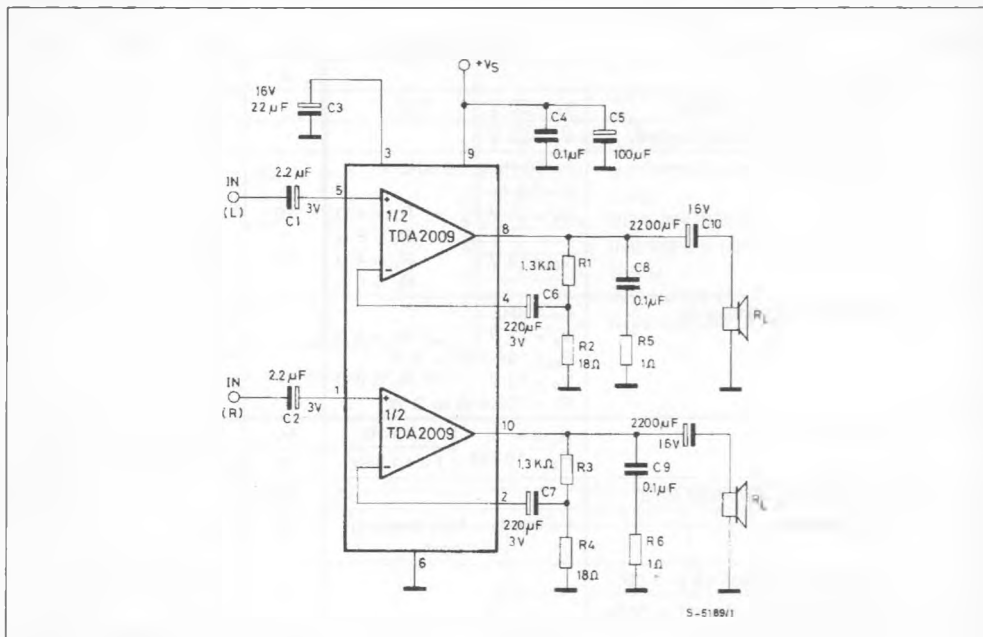(°) Curve A   (°°) 22 Hz to 22 KHz.   (°°°) Optimized test box

Figure 1 : Test and Application Circuit (Gv = 36 dB).

Figure 2 : P.C. Board and Components Layout of the Circuit of Fig. 1 (1 : 1 scale).

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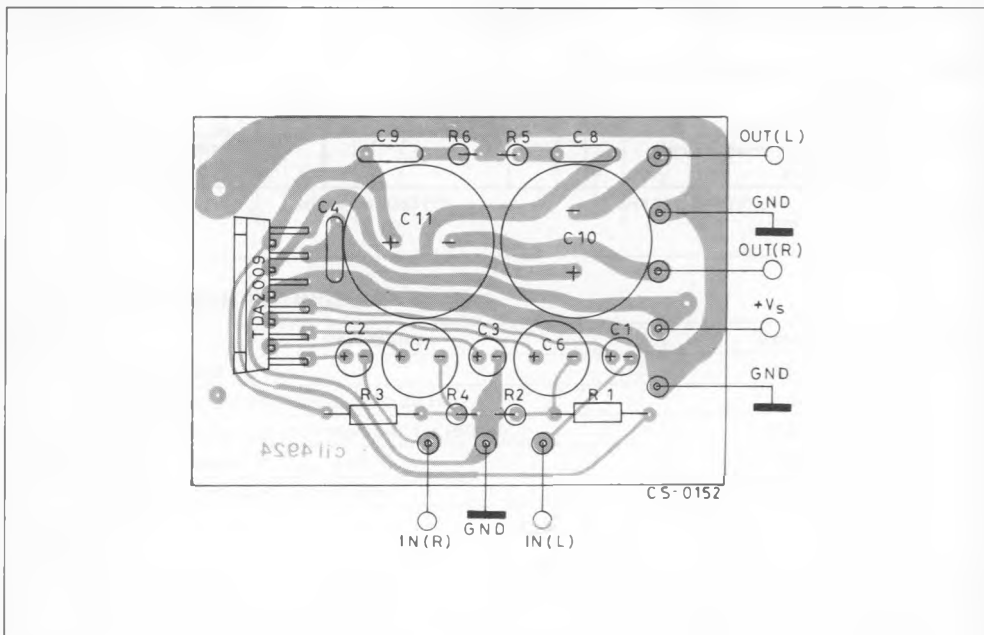MICROELECTRONICS

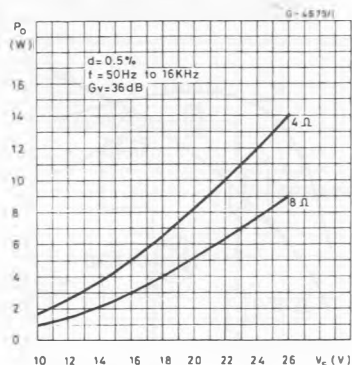**Figure 3** : Output Power vs. Supply Voltage.

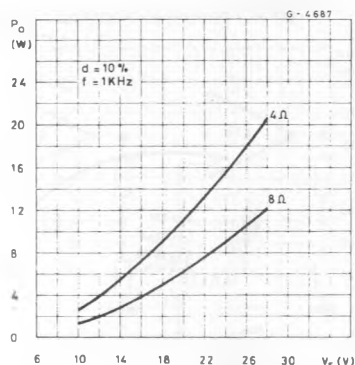**Figure 4** : Output Power vs. Supply Voltage.
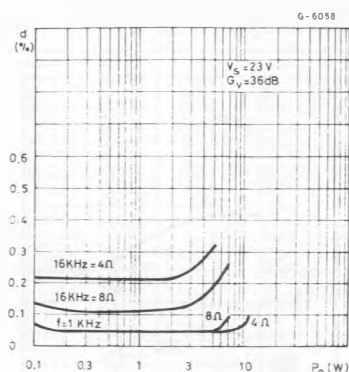

Figure 5 : Distortion vs. Output Power.
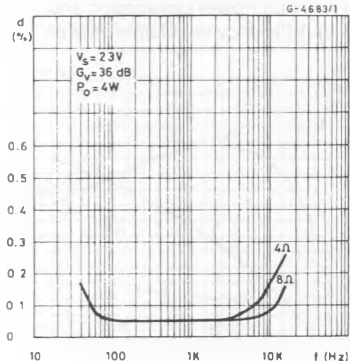

Figure 6 : Distortion vs. Frequency.

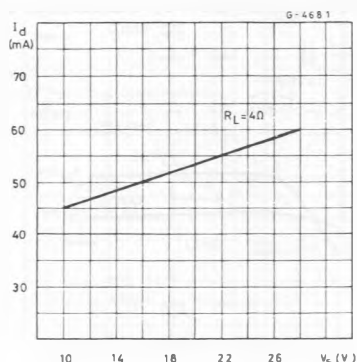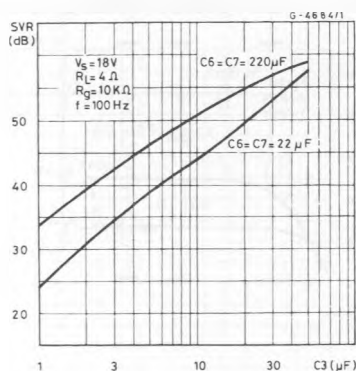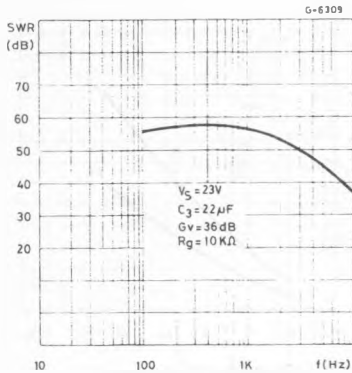**Figure 7** : Quiescent Current vs. Supply Voltage.

Figure 8 : Supply Voltage Rejection vs. Value of Capacitor C3.
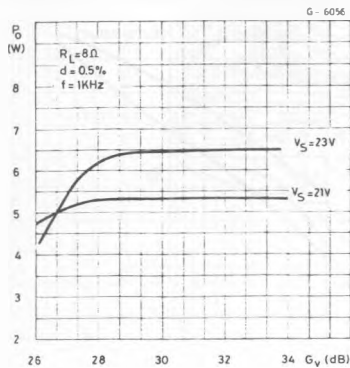

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Figure 9 : Supply Voltage Rejection vs. Frequency.

Figure 10 : Total Power Dissipation and Efficiency vs. Output Power.

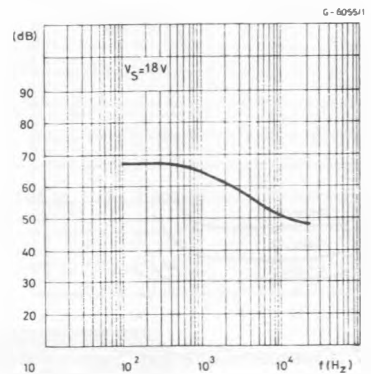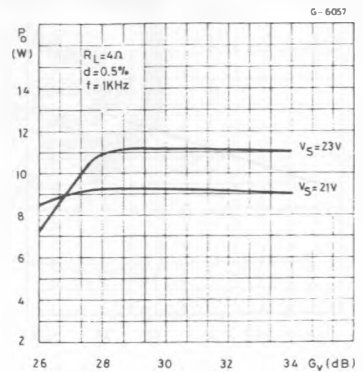**Figure 11 :** Total Power Dissipation and Efficiency vs. Output Power.
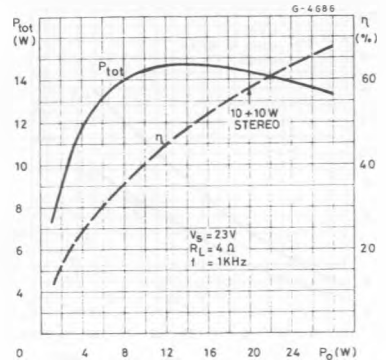
Figure 12 : Cross-talk vs. Frequency.

Figure 13 : Output Power vs. Closed Loop Gain.

Figure 14 : Output Power vs. Closed Loop Gain.

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APPLICATION INFORMATION

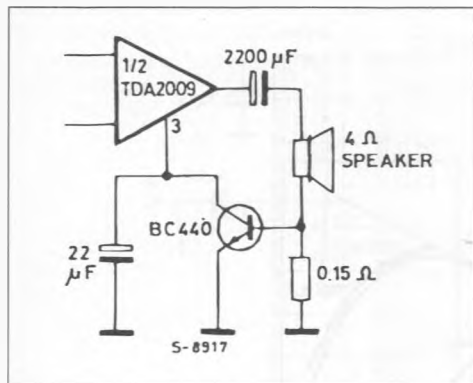**Figure 15 :** Simple Short-circuit Protection.

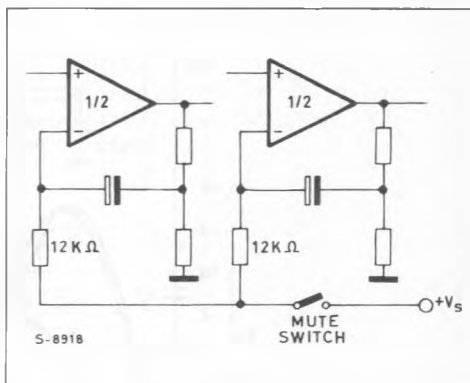**Figure 16 :** Example of Muting Circuit.

Figure 17 : 10 + 10 W Stereo Amplifier with Tone Balance and Loudness Control.
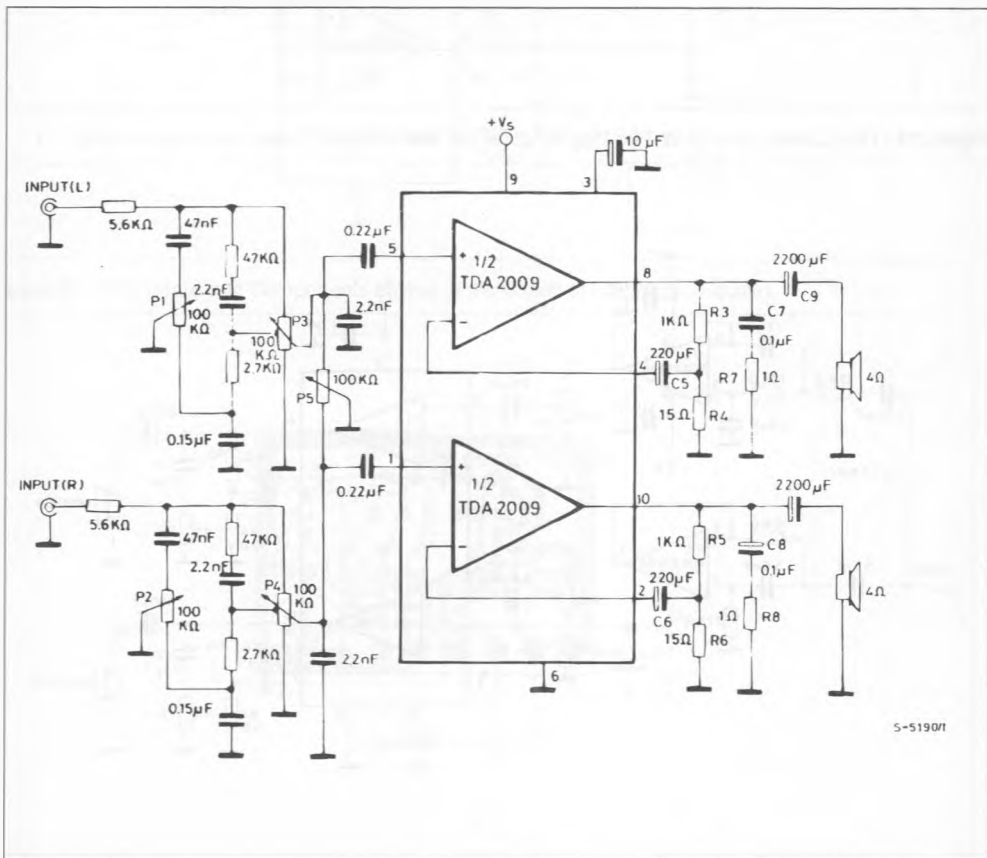
APPLICATION INFORMATION

Figure 18 : Tone Control Response (circuit of fig. 17).
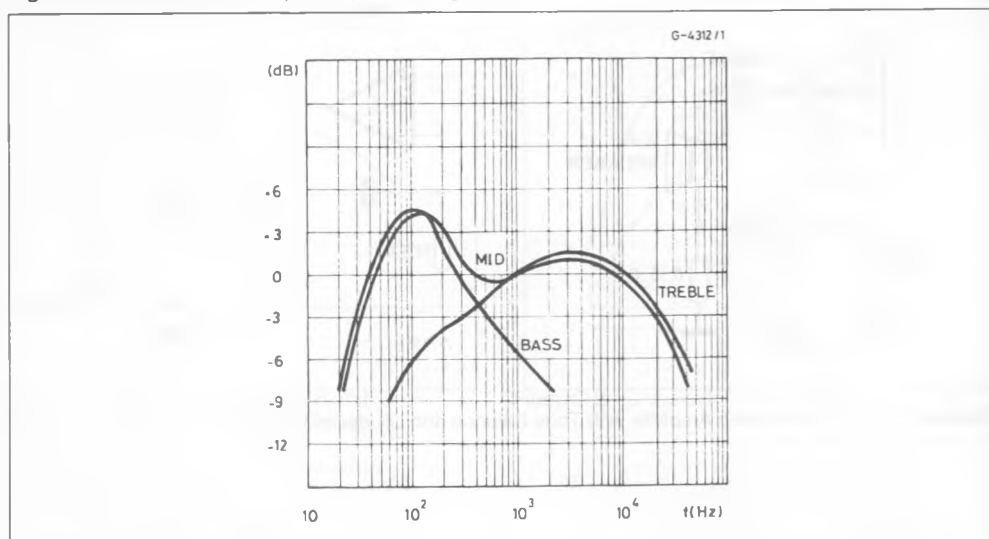
Figure 19 : High Quality 10 + 20 W Two Way Amplifier for Stereo Music Center (one channel only).

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APPLICATION INFORMATION (continued)

Figure 20 : 18 W Bridge Amplifier (d = 0.5 %, G_v = 40 dB).

Figure 21 : P.C. Board and Components Layout of the Circuit of Fig. 20 (1 : 1 scale).

APPLICATION SUGGESTION

The recommended values of the components are those shown on application circuit of fig. 1. Different values can be used ; the following table can help the designer.
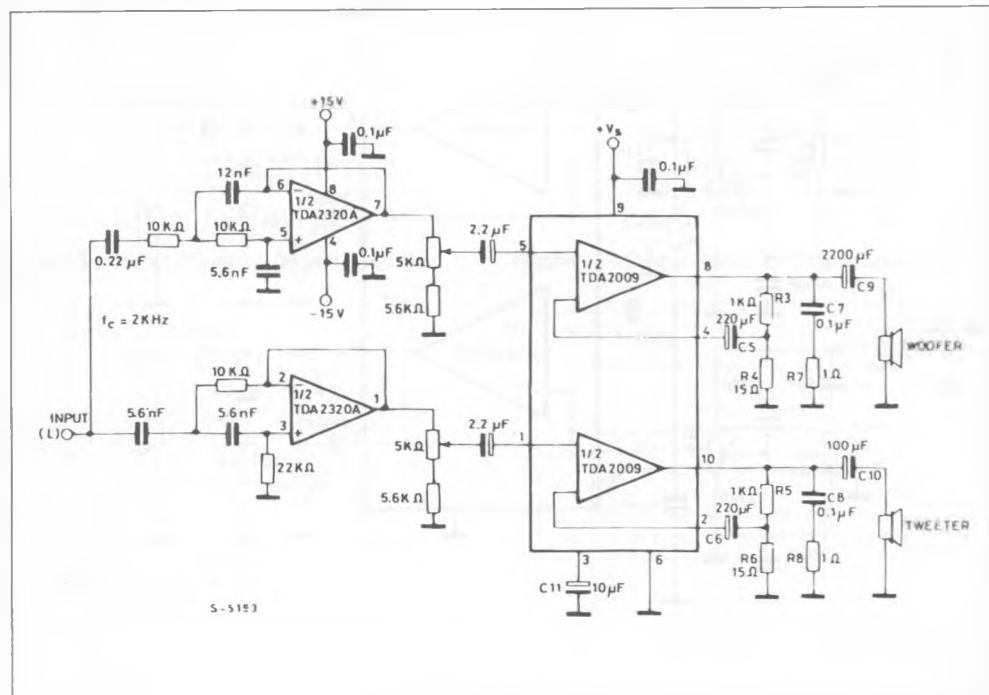
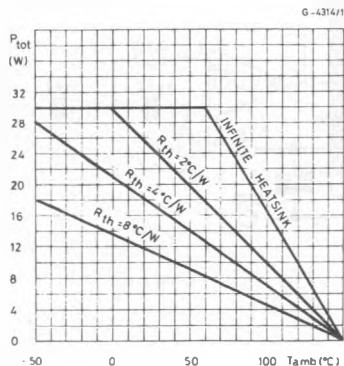| Component	Recomm. Value	Purpose	Larger Than	Smaller Than
R1 and R3	1.2 KΩ	Close Loop Gain Setting (*)	Increase of Gain	Decrease of Gain
R2 and R4	18 Ω		Decrease of Gain	Increase of Gain
R5 and R6	1 Ω	Frequency Stability	Danger of Oscillation at High Frequency with Inductive Load	
C1 and C2	2.2 μF	Input DC Decoupling	High Turn-on Delay	High Turn-on Pop Higher Low Frequency Cutoff. Increase of Noise
C3	22 μF	Ripple Rejection	Better SVR. Increase of the Switch-on Time	Degradation of SVR
C6 and C7	220 μF	Feedback Input DC Decoupling		
C8 and C9	0.1 μF	Frenquency Stability		Danger of Oscillation
C10 and C11	1000 μF to 2200 μF	Output DC Decoupling		Higher Low-frequency Cut-off

(*) The closed loop gain must be higher than 26 dB

BUILT-IN PROTECTION SYSTEMS

THERMAL SHUT-DOWN

The presence of a thermal limiting circuit offers the following advantages :

1) an overload on the output (even it is permanent), or an excessive ambient temperature can be easily withstood.

2) the heatsink can have a smaller factor of safety compared with that of a conventional circuits.

There is no device damage in the case of excessive junction temperature : all that happens is that P_o (and therefore P_{tot}) and I_d are reduced.

The maximum allowable power dissipation depends upon the size of the external heatsink (i.e. its thermal resistance) ; fig. 22 shows this dissipable power as a function of ambient temperature for different thermal resistance.

Figure 22 : Maximum Allowable Power Dissipation vs. Ambient Temperature.

Figure 23 : Output Power vs. Case Temperature.

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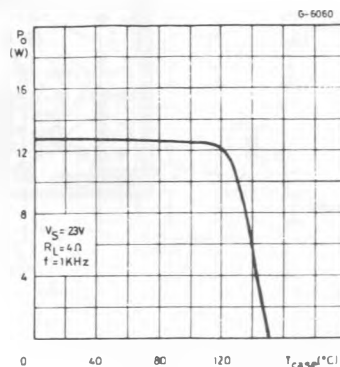
MOUNTING INSTRUCTIONS

The power dissipated in the circuit must be removed by adding an external heatsink.

Thanks to the MULTIWATT ® package attaching the heatsink is very simple, a screw or a compression spring (clip) being sufficient. Between the heatsink and the package it is better to insert a layer of silicon grease, to optimize the thermal contact ; no electrical isolation is needed between the two surfaces.