CMOS 8-Bit Microcontroller

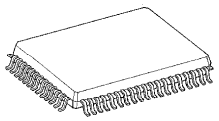
TMP87PS39N/F

The TMP87PS39 is a One-Time PROM microcontroller with low-power 543K bits (a 60K bytes program memory and a 256 characters OSD font memory) electrically programmable read only memory for the TMP87CS39 system evaluation. The TMP87PS39 is pin compatible with the TMP87CS39. The operations possible with the TMP87CS39 can be performed by writing programs and OSD character data to PROM. The TMP87PS39 can write and verify in the same way as the TC571000 using an adaptor socket BM11118/BM11138 and an EPROM programmer.

Part No.	OTP	RAM	Package	Adaptor Socket
TMP87PS39N	60 Kbytes + 14 × 18 × 256 bits	2 Kbytes	P-SDIP64-750-1.78	BM11118
TMP87PS39F			P-QFP64-1420-1.00A	BM11138

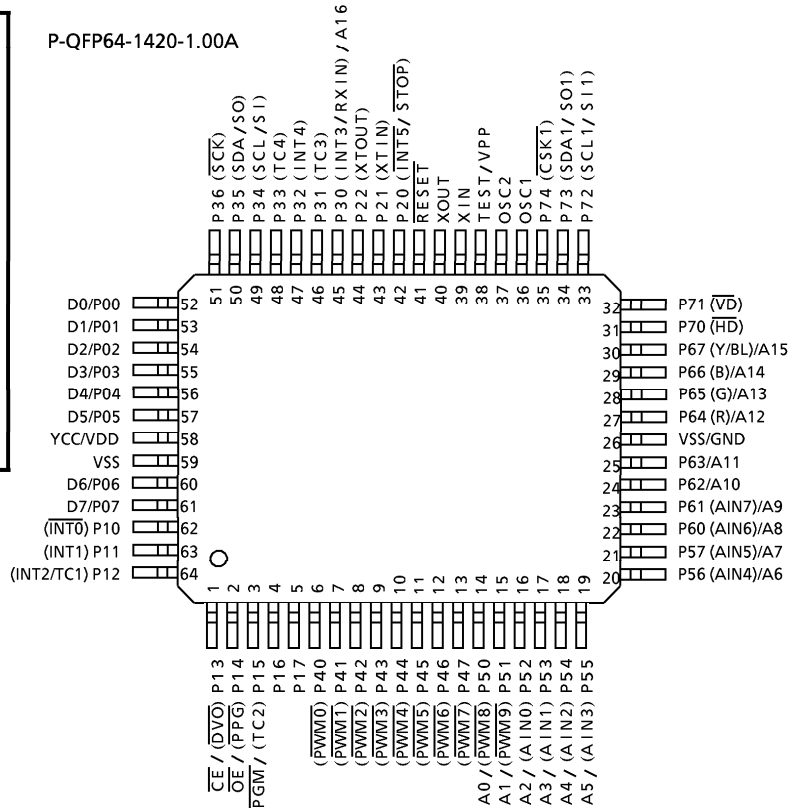
Pin Assignment (Top View)

P-QFP1420-1.00A



TMP87PS39F

P-QFP64-1420-1.00A



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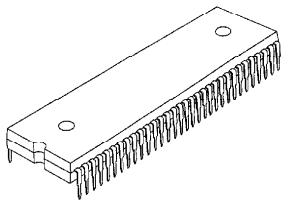

TMP87PS39N

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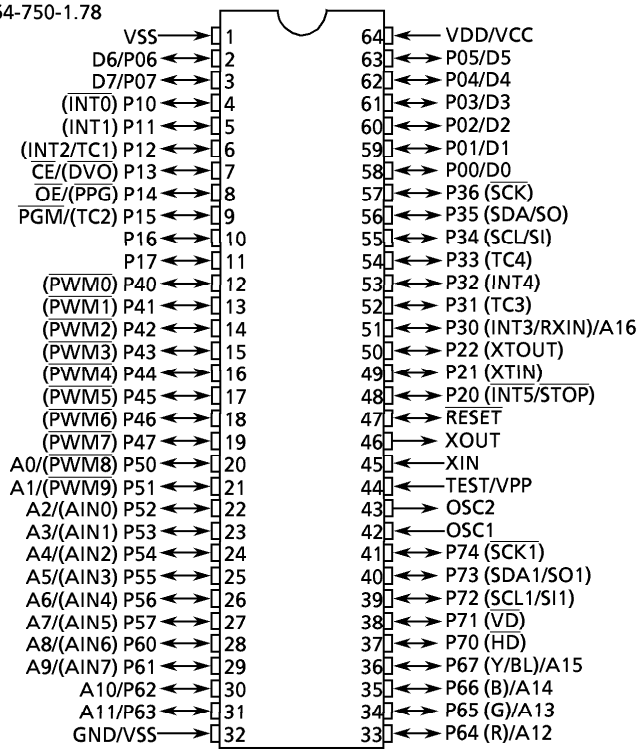


Pin Function

The TMP87PS39 has two modes: MCU and PROM.

(1) MCU mode

In this mode, the TMP87PS39 is pin compatible with the TMP87CS39 (fix the TEST pin at low level).

(2) PROM mode

Pin Name (PROM mode)	Input/Output	Functions	Pin Name (MCU mode)
A16	Input	PROM address inputs	P30
A15 to A8			P67 to P60
A7 to A0			P57 to P50
D7 to D0	I/O	PROM data input/outputs	P07 to P00
$\overline{CE}$	Input	Chip enable signal input (active low)	P13
$\overline{OE}$		Output enable signal input (active low)	P14
$\overline{PGM}$	Input	Program mode signal input (active low)	P15
VPP	Power supply	+ 12.5V/5V (Program supply voltage)	TEST
VCC		+ 5V	VDD
GND		0V	VSS
P47 to 40	Input	Pull-up with resistance for input processing	
P12			
P74 to P70			
P36 to P32			
P11		PROM mode setting pin. Be fixed at high level.	
P21			
P31			
P17, P16, P10		PROM mode setting pin. Be fixed at low level.	
P22, P20			
$\overline{RESET}$			
XIN	Input	Connect an 8 MHz oscillator to stabilize the internal state.	
XOUT	Output		
OSC1	Input	Non connection	
OSC2	Output		

Operational Description

The following explains the TMP87PS39 hardware configuration and operation. The configuration and functions of the TMP87PS39 are the same as those of the TMP87CS39, except in that a one-time PROM is used instead of an on-chip mask ROM.

The TMP87PS39 is placed in the *single-clock* mode during reset. To use the dual-clock mode, the low-frequency oscillator should be turned on by executing [SET (SYSCR2). XTEN] instruction at the beginning of the program.

1. Operating Mode

The TMP87PS39 has two modes: MCU and PROM.

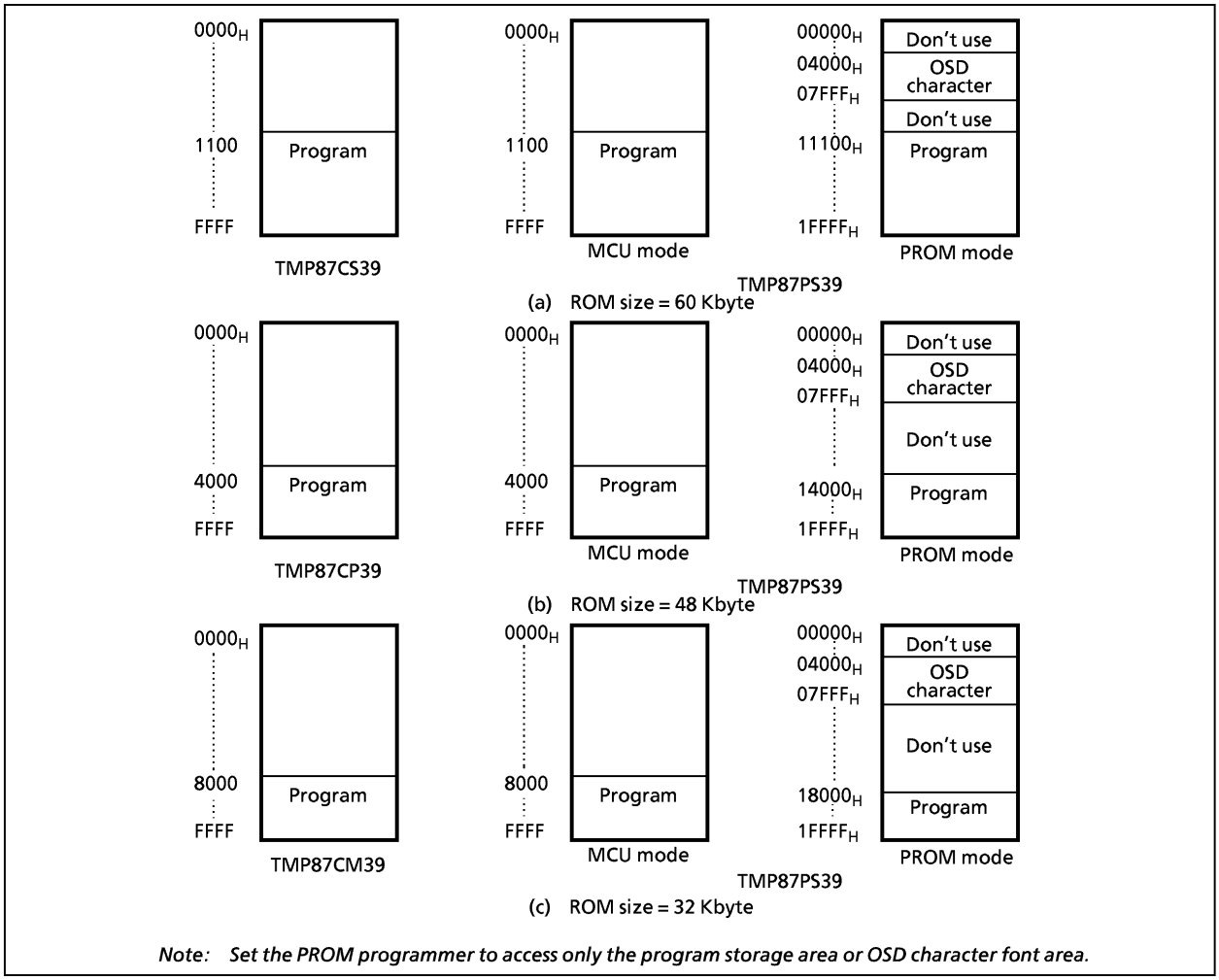
1.1 MCU Mode

The MCU mode is activated by fixing the TEST/VPP pin at low level.

In the MCU mode, operation is the same as with the TMP87CS39 (the TEST/VPP pin cannot be used open because it has no built-in pull-down resistance).

1.1.1 Program Memory and OSD Character Font Memory

The TMP87PS39 has a 60K × 8-bit (addresses 1100_H to $FFFF_H$ in the MCU mode, addresses 11100_H to $1FFFF_H$ in the PROM mode) of program memory and a 14 × 18 × 256 bits (addresses 04000_H to $07FFF_H$ in the PROM mode) of OSD character font memory.



Note: Set the PROM programmer to access only the program storage area or OSD character font area.

Figure 1-1. Program Memory Area

Electrical Characteristics

Absolute Maximum Ratings (V_{SS} = 0 V)

Parameter	Symbol	Pins	Ratings	Unit
Supply Voltage	V_{DD}		– 0.3 to 6.5	V
Program Voltage	V_{PP}	TEST/VPP	– 0.3 to 13.0	
Input Voltage	V_{IN}		– 0.3 to V_{DD} + 0.3	
Output Voltage	V_{OUT1}		– 0.3 to V_{DD} + 0.3	
Output Current (Per 1 pin)	I_{OUT1}	Ports P0, P1, P2, P3, P4, P5, P64 to P67, P7	3.2	mA
	I_{OUT2}	Ports P60 to P63	30	
Output Current (Total)	ΣI_{OUT1}	Ports P0, P1, P2, P3, P4, P5, P64 to P67, P7	120	
	ΣI_{OUT2}	Ports P60 to P63	120	
Power Dissipation [Topr = 70°C]	PD		600	mW
Soldering Temperature (time)	Tsld		260 (10 s)	°C
Storage Temperature	Tstg		– 55 to 125	
Operating Temperature	Topr		– 30 to 70	

Note: The absolute maximum ratings are rated values which must not be exceeded during operation, even for an instant. Any one of the ratings must not be exceeded. If any absolute maximum rating is exceeded, a device may break down or its performance may be degraded, causing it to catch fire or explode resulting in injury to the user. Thus, when designing products which include this device, ensure that no absolute maximum rating value will ever be exceeded.

Recommended Operating Conditions (V_{SS} = 0 V, Topr = – 30 to 70°C)

Parameter	Symbol	Pins	Conditions		Min	Max	Unit
Supply Voltage	V_{DD}		fc = 8 MHz	NORMAL1	4.5	5.5	V
				IDLE1, 2 modes			
			fc = 32.768 kHz	SLOW mode	2.7		
				SLEEP mode			
				STOP mode	2.0		
Input High Voltage	V_{IH1}	Except hysteresis input	$V_{DD} \geqq 4.5$ V		$V_{DD} \times 0.70$	V_{DD}	
	V_{IH2}	Hysteresis input			$V_{DD} \times 0.75$		
	V_{IH3}		$V_{DD} < 4.5$ V		$V_{DD} \times 0.90$		
Input Low Voltage	V_{IL1}	Except hysteresis input	$V_{DD} \geqq 4.5$ V		0	$V_{DD} \times 0.30$	
	V_{IL2}	Hysteresis input				$V_{DD} \times 0.25$	
	V_{IL3}		$V_{DD} < 4.5$ V			$V_{DD} \times 0.10$	
Clock Frequency	fc	XIN, XOUT	V_{DD} = 4.5 to 5.5 V		4.0	8.0	MHz
	f_{OSC}	OSC1, OSC2	Normal frequency mode (FORS = 0, V_{DD} = 4.5 to 5.5 V)		4.0	$f_{OSC} \leqq$ fc × 1.2 $\leqq$ 8.0	
			Doublel frequency mode (FORS = 1, V_{DD} = 4.5 to 5.5 V)		2.0	$f_{OSC} \leqq$ fc × 0.6 $\leqq$ 4.0	
	fs	XTIN, XTOUT			30.0	34.0	kHz

Note 1: The recommended operating conditions for a device are operating conditions under which it can be guaranteed that the device will operate as specified. If the device is used under operating conditions other than the recommended operating conditions (supply voltage, operating temperature range, specified AC/DC values etc.), malfunction may occur. Thus, when designing products which include this device, ensure that the recommended operating conditions for the device are always adhered to.

Note 2: Clock frequency fc ; Supply voltage range is specified in NORMAL 1/2 mode and IDLE 1/2 mode.

Note 3: When using test video signal circuit, high frequency must be 8 MHz.

Note 4: When the OSD circuit is used, the supply voltage must be from 4.5 V to 5.5 V.

DC Characteristics

(V_{SS} = 0 V, Topr = – 30 to 70°C)

Parameter	Symbol	Pins	Conditions	Min	Typ.	Max	Unit
Hysteresis Voltage	V_{HS}	Hysteresis inputs		–	0.9	–	V
Input Current	I_{IN1}	TEST	V_{DD} = 5.5 V, V_{IN} = 5.5 V/0 V	–	–	± 2	μA
	I_{IN2}	Open drain ports	V_{DD} = 5.5 V, V_{IN} = 5.5 V/0 V	–	–	± 2	
	I_{IN3}	Tri-state ports	V_{DD} = 5.5 V, V_{IN} = 5.5 V/0 V	–	–	± 2	
	I_{IN4}	$\overline{RESET}$, $\overline{STOP}$	V_{DD} = 5.5 V, V_{IN} = 5.5 V/0 V	–	–	± 2	
Input Resistance	R_{IN2}	$\overline{RESET}$		100	220	450	kΩ
Output Leakage Current	I_{LO1}	Sink open drain ports	V_{DD} = 5.5 V, V_{OUT} = 5.5 V	–	–	2	μA
	I_{LO2}	Tri-state ports	V_{DD} = 5.5 V, V_{OUT} = 5.5 V/0 V	–	–	± 2	
Output High Voltage	V_{OH2}	Tri-state ports	V_{DD} = 4.5 V, I_{OH} = – 0.7 mA	4.1	–	–	V
Output Low Voltage	V_{OL}	Except XOUT, OSC2 and ports P63 to P60	V_{DD} = 4.5 V, I_{OL} = 1.6 mA	–	–	0.4	
Output Low current	I_{OL3}	Ports P63 to P60	V_{DD} = 4.5 V, V_{OL} = 1.0 V	–	20	–	mA
Supply Current in NORMAL 1, 2 modes	I_{DD}		V_{DD} = 5.5 V fc = 8 MHz	–	13	20	
Supply Current in IDLE 1, 2 modes			fs = 32.768 kHz V_{IN} = 5.3 V/0.2 V	–	6.5	10	
Supply Current in SLOW mode			V_{DD} = 3.0 V fs = 32.768 kHz	–	30	70	μA
Supply Current in SLEEP mode			V_{IN} = 2.8 V/0.2 V	–	15	35	
Supply Current in STOP mode			V_{DD} = 5.5 V V_{IN} = 5.3 V/0.2 V	–	0.5	10	

Note 1: Typical values show those at Topr = 25°C, V_{DD} = 5 V.

Note 2: Input Current I_{IN3}; The current through pull-up resistor is not included.

Note 3: Supply Current I_{DD}; The current (Typ. 0.5 mA) through ladder resistors of ADC is included in NORMAL mode and IDLE mode.

AD Conversion Characteristics

(V_{SS} = 0 V, V_{DD} = 4.5 to 5.5 V, Topr = – 30 to 70°C)

Parameter	Symbol	Conditions	Min	Typ.	Max	Unit
Analog Reference Voltage	V_{DD}	supplied from V_{DD} pin	—	V_{DD}	—	V
	V_{SS}	supplied from V_{SS} pin	—	0	0	
Analog Reference Voltage Range	$_{\Delta}V_{AREF}$	= V_{DD} – V_{SS}	—	V_{DD}	—	
Analog Input Voltage	V_{AIN}		V_{SS}	—	V_{DD}	
Nonlinearity Error		V_{DD} = 4.5 to 5.5V	—	—	± 1	LSB
Zero Point Error			—	—	± 2	
Full Scale Error			—	—	± 2	
Total Error			—	—	± 3	

AC Characteristics

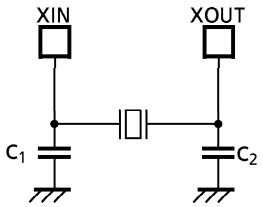
(V_{SS} = 0 V, V_{DD} = 4.5 to 5.5 V, Topr = – 30 to 70°C)

Parameter	Symbol	Conditions	Min	Typ.	Max	Unit
Machine Cycle Time	t_{cy}	In NORMAL1, 2 modes	0.5	–	1.0	μs
		In IDLE1, 2 modes				
		In SLOW mode	117.6	–	133.3	
		In SLEEP mode				
High Level Clock Pulse Width	t_{WCH}	For external clock operation (XIN input), fc = 8MHz	50	–	–	ns
Low Level Clock Pulse Width	t_{WCL}					
High Level Clock Pulse Width	t_{WSH}	For external clock operation (XTIN input), fs = 32.768kHz	14.7	–	–	μs
Low Level Clock Pulse Width	t_{WSL}					

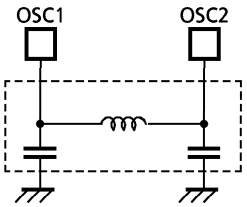
Recommended Oscillating Conditions

(V_{SS} = 0 V, V_{DD} = 4.5 to 5.5 V, Topr = – 30 to 70°C)

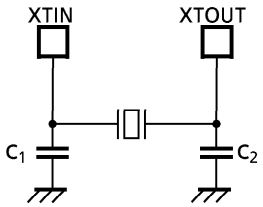
Parameter	Oscillator	Oscillation Frequency	Recommended Oscillator	Recommended Constant C_1	Recommended Constant C_2
High-frequency Oscillation	Ceramic Resonator	8 MHz	KYOCERA KBR8.0M	30 pF	30 pF
		4 MHz	KYOCERA KBR4.0MS		
			MURATA CSA4.00MG		
	Crystal Oscillator	8 MHz	TOYOCOM 210B 8.0000	20 pF	20 pF
		4 MHz	TOYOCOM 204B 4.0000		
OSD	LC Resonator	8 MHz	TOKO A285TNIS-11695	–	–
		7 MHz	TOKO TBEKSES-30375FBY		
Low-frequency Oscillation	Crystal Oscillator	32.768 kHz	NDK MX-38T	15 pF	15 pF



(1) High-frequency Oscillation



(2) LC Resonator for OSD



(3) Low-frequency Oscillation

Note: On our OSD circuit, the horizontal display start position is determined by counting the clock from LC oscillator. So, the unstable start of oscillation after the rising edge of Horizontal Sync. Signal will be cause the OSD distortion. Generally, smaller C and larger L make clearer wave form at the beginning of oscillation. We recommend that the value of LC oscillator should be equal and bigger than 33μH.

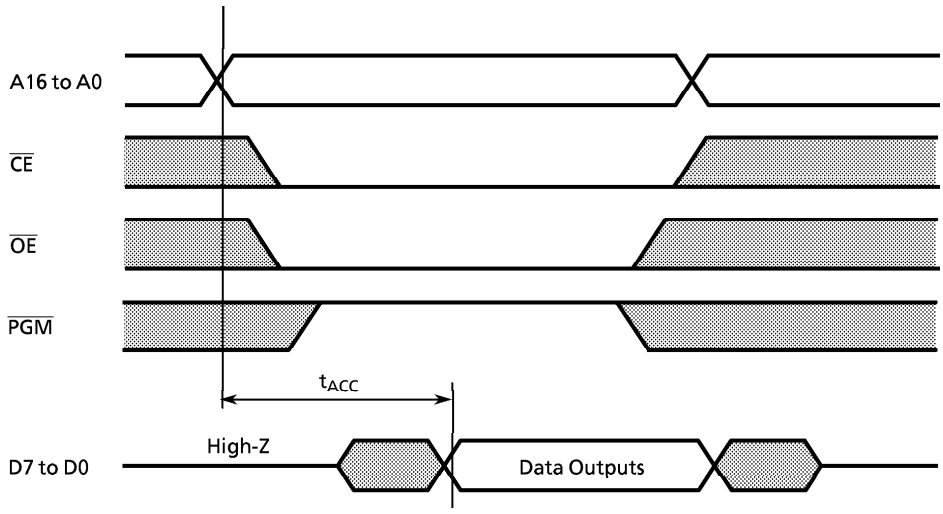
Note: To keep reliable operation, shield the device electrically with the metal plate on its package mold surface against the high electric field, for example, be CRT (Cathode Ray Tube).

DC/AC Characteristics (PROM mode) (V_{SS} = 0 V)

(1) Read Operation

Parameter	Symbol	Conditions	Min	Typ.	Max	Unit
Input High Voltage	V_{IH4}		$V_{CC} \times 0.7$	–	V_{CC}	V
Input Low Voltage	V_{IL4}		0	–	$V_{CC} \times 0.12$	
Power Supply Voltage	V_{CC}		4.75	5.0	5.25	
Program Power Supply Voltage	V_{PP}					
Address Access Time	t_{ACC}	V_{CC} = 5.0 ± 0.25 V	–	1.5tcyc + 300	–	ns

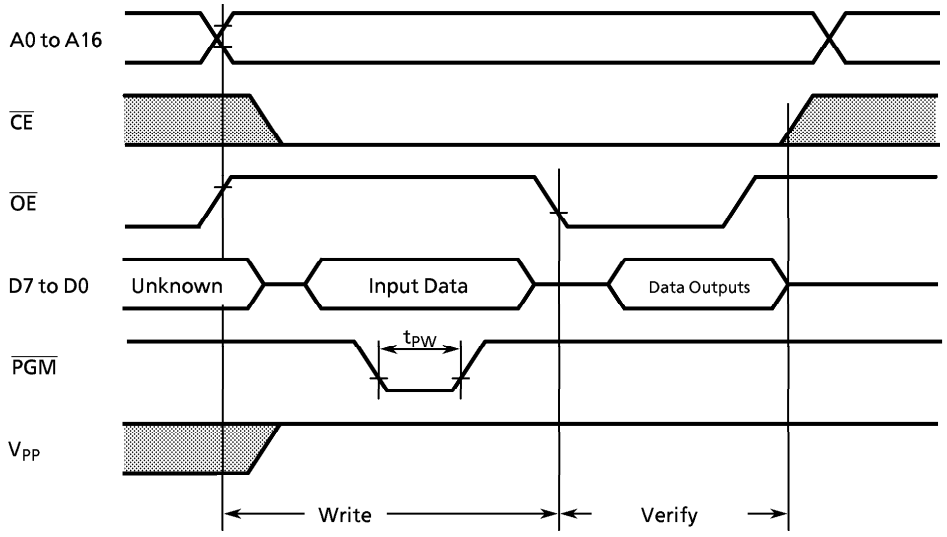
tcyc = 500 ns at 8 MHz



(2) High-Speed Programming Operation

Parameter	Symbol	Conditions	Min	Typ.	Max	Unit
Input High Voltage	V_{IH4}		$V_{CC} \times 0.7$	–	V_{CC}	V
Input Low Voltage	V_{IL4}		0	–	$V_{CC} \times 0.12$	
Power Supply Voltage	V_{CC}		6.0	6.25	6.5	
Program Power Supply Voltage	V_{PP}		12.5	12.75	13.0	
Initial Program Pulse Width	t_{PW}	V_{CC} = 6.0 V	0.095	0.1	0.105	ms

High-speed Programing Timing



Note1: When V_{cc} power supply is turned on or after, V_{pp} must be increased.
When V_{cc} power supply is turned off or before, V_{pp} must be increased.
Note2: The device must not be set to the EPROM programmer or picked op from it under applying the program voltage (12.75 V ± 0.25 V = V) to the V_{pp} pin as the device is damaged.
Note3: Be sure to execute the recommended programing mode with the recommended programing adaptor. If a mode or an adaptor except the above, the misoperation sometimes occurs.