BAW56WT1G, SBAW56WT1G

Dual Switching Diode, Common Anode

Features

- AEC-Q101 Qualified and PPAP Capable
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant*

MAXIMUM RATINGS $(T_A = 25^{\circ}C)$

| Rating | Symbol | Max | Unit |
|----------------------------|------------------------|-----|------|
| Reverse Voltage | V _R | 70 | V |
| Forward Current | IF | 200 | mA |
| Peak Forward Surge Current | I _{FM(surge)} | 500 | mA |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS (T_A = 25°C)

| Characteristic | Symbol | Max | Unit |
|--|-----------------------------------|----------------|-------|
| Total Device Dissipation FR-5 Board (Note 1) T _A = 25°C Derate above 25°C | P _D | 200 | mW |
| Derate above 25 C | | 1.0 | mW/°C |
| Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | 625 | °C/W |
| Total Device Dissipation Alumina Substrate (Note 2) T _A = 25°C | P _D | 300 | mW |
| Derate above 25°C | | 2.4 | mW/°C |
| Thermal Resistance, Junction-to-Ambient | $R_{\theta JA}$ | 417 | °C/W |
| Junction and Storage Temperature | T _J , T _{stg} | -55 to +150 | °C |

- 1. FR-5 = $1.0 \times 0.75 \times 0.062$ in.
- 2. Alumina = $0.4 \times 0.3 \times 0.024$ in. 99.5% alumina.

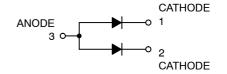


ON Semiconductor®

http://onsemi.com



SC-70 CASE 419 STYLE 4



MARKING DIAGRAM



A1 = Device Code M = Date Code* • = Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

| Device | Package | Shipping [†] |
|------------|--------------------|-----------------------|
| BAW56WT1G | SC-70 (Pb-Free) | 3,000 / Tape & Reel |
| SBAW56WT1G | SC-70 (Pb-Free) | 3,000 / Tape & Reel |

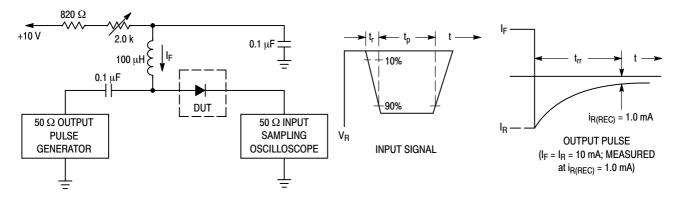
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

BAW56WT1G, SBAW56WT1G

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

| Characteristic | Symbol | Min | Max | Unit |
|--|-------------------|------------------|----------------------------|------|
| OFF CHARACTERISTICS | | | 1 | |
| Reverse Breakdown Voltage (I _(BR) = 100 μA) | V _(BR) | 70 | - | V |
| Reverse Voltage Leakage Current $(V_R = 25 \text{ V}, T_J = 150^{\circ}\text{C})$ $(V_R = 70 \text{ V})$ $(V_R = 70 \text{ V}, T_J = 150^{\circ}\text{C})$ | I _R | - - - | 30 2.5 50 | μΑ |
| Diode Capacitance (V _R = 0, f = 1.0 MHz) | C _D | - | 2.0 | pF |
| Forward Voltage (I _F = 1.0 mA) (I _F = 10 mA) (I _F = 60 mA) (I _F = 150 mA) | V _F | - - - - | 715 855 1000 1250 | mV |
| Reverse Recovery Time (I _F = I _R = 10 mA, R _L = 100 Ω , I _{R(REC)} = 1.0 mA) (Figure 1) | t _{rr} | - | 6.0 | ns |

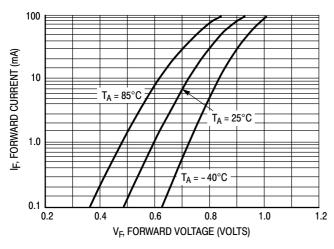


Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (IF) of 10 mA.

- 2. Input pulse is adjusted so $I_{R(peak)}$ is equal to 10 mA.
- 3. t_p » t_{rr}

Figure 1. Recovery Time Equivalent Test Circuit

BAW56WT1G, SBAW56WT1G



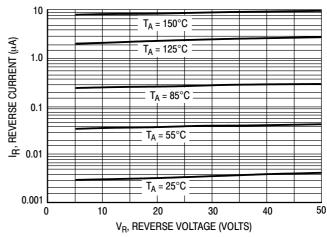


Figure 2. Forward Voltage

Figure 3. Leakage Current

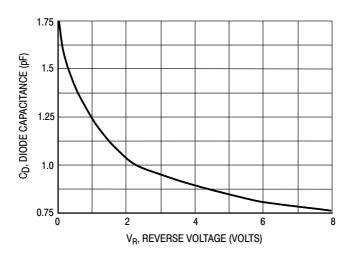
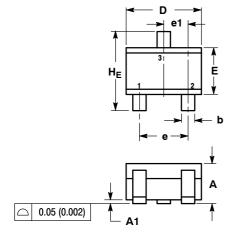


Figure 4. Capacitance

BAW56WT1G, SBAW56WT1G

PACKAGE DIMENSIONS

SC-70 (SOT-323) CASE 419-04 ISSUE N





- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.

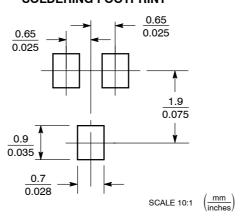
| | MILLIMETERS | | | INCHES | | |
|--------|-------------|------|-----------|--------|-------|-------|
| DIM | MIN | NOM | MAX | MIN | NOM | MAX |
| Α | 0.80 | 0.90 | 1.00 | 0.032 | 0.035 | 0.040 |
| A1 | 0.00 | 0.05 | 0.10 | 0.000 | 0.002 | 0.004 |
| A2 | 0.70 REF | | 0.028 REF | | | |
| b | 0.30 | 0.35 | 0.40 | 0.012 | 0.014 | 0.016 |
| С | 0.10 | 0.18 | 0.25 | 0.004 | 0.007 | 0.010 |
| D | 1.80 | 2.10 | 2.20 | 0.071 | 0.083 | 0.087 |
| Е | 1.15 | 1.24 | 1.35 | 0.045 | 0.049 | 0.053 |
| е | 1.20 | 1.30 | 1.40 | 0.047 | 0.051 | 0.055 |
| e1 | 0.65 BSC | | 0.026 BSC | | ; | |
| ا ا | 0.20 | 0.38 | 0.56 | 0.008 | 0.015 | 0.022 |
| HE | 2.00 | 2.10 | 2.40 | 0.079 | 0.083 | 0.095 |

STYLE 4: PIN 1.

IN 1. CATHODE

ANODE

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center

Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative