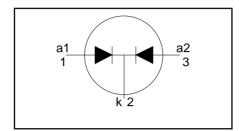
# BYV118F, BYV118X series

## **FEATURES**

- Low forward volt drop
- Fast switching
- Reverse surge capability
- High thermal cycling performance
- Isolated package

## **SYMBOL**



## QUICK REFERENCE DATA

$$V_R = 35 \text{ V}/ 40 \text{ V}/ 45 \text{ V}$$
 $I_{O(AV)} = 10 \text{ A}$ 
 $V_F \le 0.6 \text{ V}$ 

#### **GENERAL DESCRIPTION**

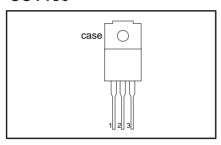
Dual, common cathode schottky rectifier diodes in a plastic envelope with electrically isolated mounting tab. Intended for use as output rectifiers in low voltage, high frequency switched mode power supplies.

The BYV118F series is supplied in the SOT186 package. The BYV118X series is supplied in the SOT186A package.

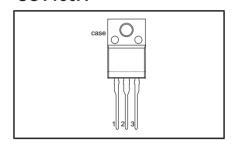
#### **PINNING**

PIN	DESCRIPTION		
1	anode 1 (a)		
2	cathode (k)		
3	anode 2 (a)		
tab	isolated		

## **SOT186**



## **SOT186A**



# **LIMITING VALUES**

Limiting values in accordance with the Absolute Maximum System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.			UNIT
		BYV118F- BYV118X-		35 35	40 40	45 45	
$V_{RRM}$	Peak repetitive reverse voltage		-	35	40	45	V
$V_{RWM}$	Working peak reverse voltage		-	35	40	45	V
$V_R$	Continuous reverse voltage	T <sub>hs</sub> ≤ 97 °C	-	35	40	45	V
I <sub>O(AV)</sub>	Average rectified output current (both diodes conducting)	square wave; $\delta = 0.5$ ; $T_{hs} \le 107 ^{\circ}C$	-		10		А
I <sub>FRM</sub>	Repetitive peak forward current per diode	square wave; $\delta = 0.5$ ; $T_{hs} \le 107  ^{\circ}C$	-		10		A
I <sub>FSM</sub>	Non-repetitive peak forward	t = 10 ms	-		100		Α
	current per diode	t = 8.3  ms sinusoidal; $T_j = 125 ^{\circ}\text{C}$ prior to surge; with reapplied $V_{\text{RRM}(\text{max})}$	-		110		A
I <sub>RRM</sub>	Peak repetitive reverse surge current per diode	pulse width and repetition rate limited by T <sub>i max</sub>	-		1		Α
T <sub>j</sub>	Operating junction temperature	Jillax	-		150		°C
$T_{stg}$	Storage temperature		- 65		175		°C

BYV118F, BYV118X series

# **ISOLATION LIMITING VALUE & CHARACTERISTIC**

 $T_{hs}$  = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V <sub>isol</sub>	Peak isolation voltage from all terminals to external heatsink	SOT186 package; R.H. ≤ 65%; clean and dustfree	1	-	1500	<b>V</b>
V <sub>isol</sub>	R.M.S. isolation voltage from all terminals to external heatsink	SOT186A package; f = 50-60 Hz; sinusoidal waveform; R.H. ≤ 65%; clean and dustfree	-	-	2500	V
C <sub>isol</sub>	Capacitance from pin 2 to external heatsink	f = 1 MHz	-	10	-	pF

# THERMAL RESISTANCES

SYM	IBOL PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R <sub>th j-hs</sub>	Thermal resistance junction to heatsink	per diode both diodes	-	-	6.5 5.5	K/W K/W
R <sub>th j-a</sub>	Thermal resistance junction to ambient	(with heatsink compound) in free air	-	55	-	K/W

# **ELECTRICAL CHARACTERISTICS**

 $T_i = 25$  °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{F}$	Forward voltage	$I_F = 5 \text{ A}; T_i = 125^{\circ}\text{C}$	-	0.52	0.6	٧
	_	$I_{\rm F} = 10  {\rm A}^{\circ}$	-	0.72	0.87	V
I <sub>R</sub>	Reverse current	$\dot{V}_{R} = V_{RWM}$	-	0.06	0.5	mΑ
		$V_R = V_{RWM}$ ; $T_i = 100$ °C	-	6	15	mΑ
$C_d$	Junction capacitance	$V_R = 5 \text{ V}$ ; f = '1 MHz, $T_j = 25 ^{\circ}\text{C}$ to 125 $^{\circ}\text{C}$	-	155	-	pF

# BYV118F, BYV118X series

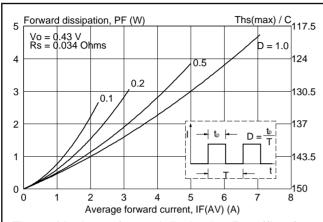


Fig.1. Maximum forward dissipation  $P_F = f(I_{F(AV)})$  per diode; square current waveform where  $I_{F(AV)} = I_{F(RMS)} x \sqrt{D}$ .

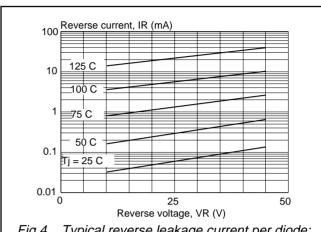


Fig.4. Typical reverse leakage current per diode;  $I_R = f(V_R)$ ; parameter  $T_j$ 

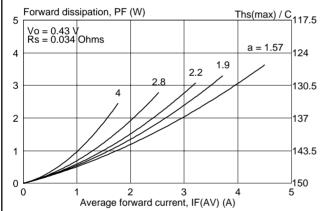


Fig.2. Maximum forward dissipation  $P_F = f(I_{F(AV)})$  per diode; sinusoidal current waveform where a = f form factor  $= I_{F(RMS)} / I_{F(AV)}$ .

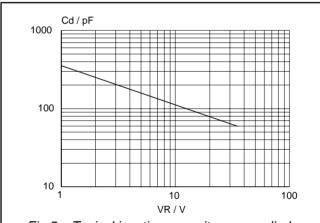


Fig.5. Typical junction capacitance per diode;  $C_d = f(V_R)$ ; f = 1 MHz;  $T_j = 25$ °C to 125°C.

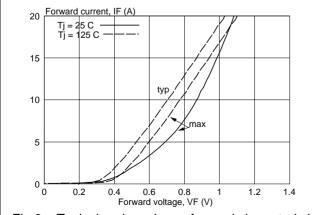


Fig.3. Typical and maximum forward characteristic  $I_F = f(V_F)$ ; parameter  $T_i$ 

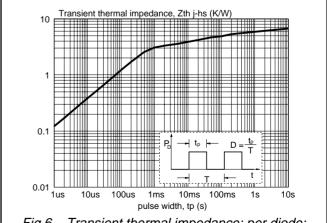
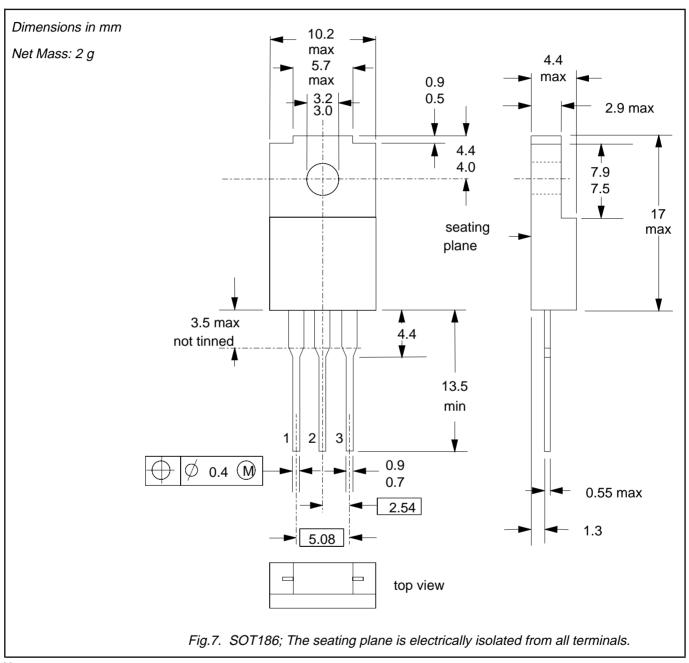


Fig.6. Transient thermal impedance; per diode;  $Z_{th j ext{-}hs} = f(t_p)$ .

BYV118F, BYV118X series

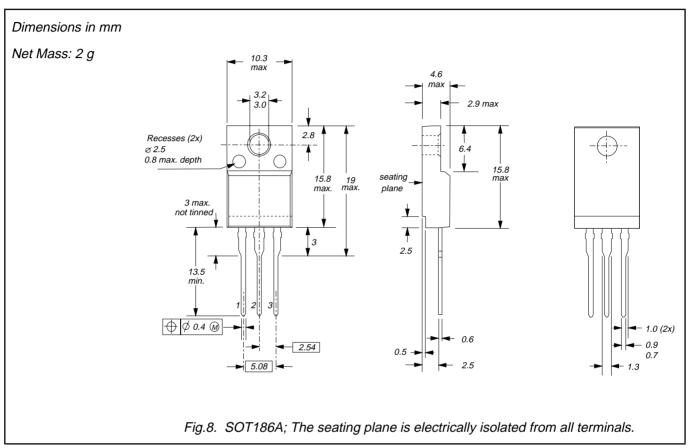
## **MECHANICAL DATA**



- Refer to mounting instructions for F-pack envelopes.
   Epoxy meets UL94 V0 at 1/8".

BYV118F, BYV118X series

# **MECHANICAL DATA**



- Notes
  1. Refer to mounting instructions for F-pack envelopes.
  2. Epoxy meets UL94 V0 at 1/8".

Philips Semiconductors Product specification

# Rectifier diodes Schottky barrier

BYV118F, BYV118X series

#### **DEFINITIONS**

Data sheet status					
Objective specification	This data sheet contains target or goal specifications for product development.				
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.				
Product specification This data sheet contains final product specifications.					
Limiting values					

Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

#### **Application information**

Where application information is given, it is advisory and does not form part of the specification.

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