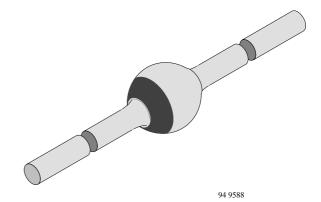


### **Silicon Mesa Rectifiers**

#### Features

- Glass passivated junction
- Hermetically sealed package
- Controlled avalanche characteristics
- Low reverse current
- High surge current loading
- Electrically equivalent diodes: BYW82 – 1N5624 BYW83 – 1N5625 BYW84 – 1N5626 BYW85 – 1N5627



#### Applications

Rectifier, general purpose

#### **Absolute Maximum Ratings**

 $T_i = 25^{\circ}C$ 

Parameter	Test Conditions	Туре	Symbol	Value	Unit
Reverse voltage, repetitive peak		BYW82	V <sub>R</sub> =V <sub>RRM</sub>	200	V
reverse voltage		BYW83	V <sub>R</sub> =V <sub>RRM</sub>	400	V
		BYW84	V <sub>R</sub> =V <sub>RRM</sub>	600	V
		BYW85	V <sub>R</sub> =V <sub>RRM</sub>	800	V
		BYW86	V <sub>R</sub> =V <sub>RRM</sub>	1000	V
Peak forward surge current	t <sub>p</sub> =10ms		I <sub>FSM</sub>	100	А
Repetitive peak forward current			I <sub>FRM</sub>	18	Α
Average forward current	$T_{amb} \leq 65^{\circ}C$		I <sub>FAV</sub>	3	Α
Pulse avalanche peak power	t <sub>p</sub> =20µs, half sine wave, T <sub>j</sub> =175 °C		P <sub>R</sub>	1000	W
Max. pulse energy in the avalanche mode, non repetitive (inductive load switch off)	$I_{(BR)R}=1A, T_{j}=175^{\circ}C$		E <sub>R</sub>	20	mWs
i <sup>2</sup> *t-rating			i <sup>2</sup> *t	40	A <sup>2</sup> *s
Junction temperature			Тj	175	°C
Storage temperature range			T <sub>stg</sub>	-65+175	°C

#### **Maximum Thermal Resistance**

 $T_i = 25^{\circ}C$ 

Parameter	Test Conditions	Symbol	Value	Unit
Junction ambient	l=25mm, T <sub>L</sub> =constant	R <sub>thJA</sub>	30	K/W
	on PC board with spacing 37.5mm		70	K/W

#### Characteristics

 $T_j = 25^{\circ}C$ 

Parameter	Test Conditions	Туре	Symbol	Min	Тур	Max	Unit
Forward voltage	I <sub>F</sub> =3A		V <sub>F</sub>			1.0	V
Reverse current	V <sub>R</sub> =V <sub>RRM</sub>		I <sub>R</sub>		0.1	1	μΑ
	V <sub>R</sub> =V <sub>RRM</sub> , T <sub>j</sub> =100°C		I <sub>R</sub>		5	10	μΑ
Breakdown voltage	$I_R=100\mu A, t_p/T=0.01, t_p=0.3ms$		V <sub>(BR)</sub>			1600	V
Diode capacitance	V <sub>R</sub> =0, f=0.47MHz		CD		65	100	pF
Reverse recovery time	$I_{F}=0.5A, I_{R}=1A, i_{R}=0.25A$		t <sub>rr</sub>		2	4	μs
	$I_{F}=1A, d_{i}/d_{t}=5A/\mu s, \\ V_{R}=50V$		t <sub>rr</sub>		3	6	μs
Reverse recovery charge	$I_F=1A$ , $d_i/d_t=5A/\mu s$		Qrr		6	10	μC

**Typical Characteristics** ( $T_j = 25^{\circ}C$  unless otherwise specified)

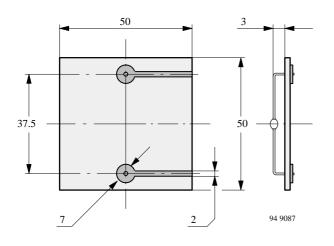


Figure 1. Epoxy glass hard tissue, board thickness 1.5 mm,  $R_{thJA} \! \leq \! 70 \; \text{K/W}$ 

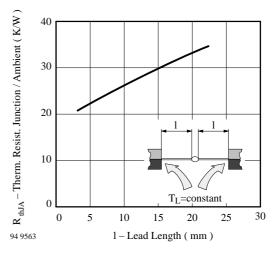


Figure 2. Thermal Resistance vs. Lead Length



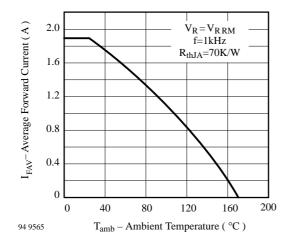


Figure 3. Average Forward Current vs. Ambient Temperature

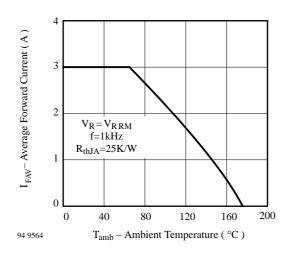


Figure 4. Average Forward Current vs. Ambient Temperature

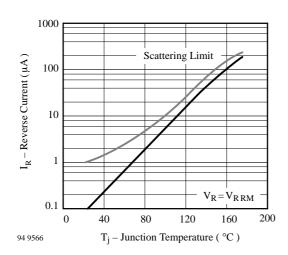
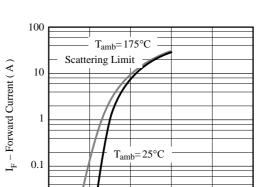


Figure 5. Reverse Current vs. Junction Temperature



 $0.01 \begin{array}{|c|c|c|c|c|c|c|} \hline 0.01 & \hline 0 & 0.6 & 1.2 & 1.8 & 2.4 & 3.0 \\ \hline 0 & 0.6 & 1.2 & 1.8 & 2.4 & 3.0 \\ \hline 94 \, 9567 & V_F - Forward Voltage (V) \end{array}$ 

Figure 6. Forward Current vs. Forward Voltage

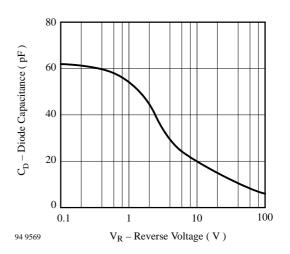


Figure 7. Diode Capacitance vs. Reverse Voltage

## BYW82...BYW86

# BYW82...BYW86

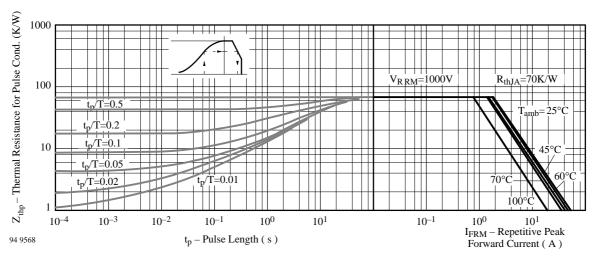
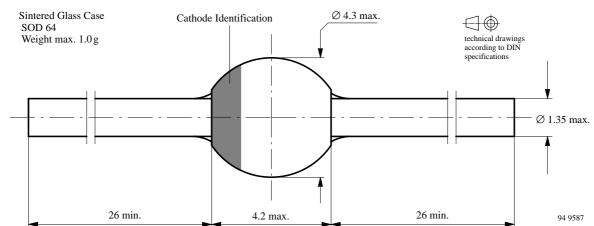


Figure 8. Thermal Response



#### **Dimensions in mm**

**EMIC** 

Semiconductors

#### **Ozone Depleting Substances Policy Statement**

It is the policy of **TEMIC TELEFUNKEN microelectronic GmbH** to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

**TEMIC TELEFUNKEN microelectronic GmbH** semiconductor division has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

**TEMIC** can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice. Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use TEMIC products for any unintended or unauthorized application, the buyer shall indemnify TEMIC against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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