

# **PNP Multi-Chip General Purpose Amplifier**

This device is designed for general purpose amplifier and switching applications at collector currents of 10 µA to 100 mA. Sourced from Process 66.

#### Absolute Maximum Ratings\* $T_A = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CEO</sub>	Collector-Emitter Voltage	40	V
V <sub>CBO</sub>	Collector-Base Voltage	40	V
V <sub>EBO</sub>	Emitter-Base Voltage	5.0	V
I <sub>C</sub>	Collector Current - Continuous	200	mA
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Junction Temperature Range	-55 to +150	°C

\*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

#### NOTES:

1) These ratings are based on a maximum junction temperature of 150 degrees C.

These rating the steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
All voltages (V) and currents (A) are negative polarity for PNP transistors.

#### **Thermal Characteristics** $T_A = 25^{\circ}C$ unless otherwise noted

Symbol	Characteristic	Max			Units
		FFB3906	FMB3906	MMPQ3906	
P <sub>D</sub>	Total Device Dissipation Derate above 25°C	300 2.4	700 5.6	1,000 8.0	mW mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient Effective 4 Die Each Die	415	180	125 240	°C/W °C/W °C/W

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## (continued)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
OFF CHAP	RACTERISTICS			•	•	•
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage*	$I_{\rm C} = 1.0 \text{ mA}, I_{\rm B} = 0$	40			V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	$I_{\rm C} = 10 \ \mu A, \ I_{\rm E} = 0$	40			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_{\rm E} = 10 \ \mu A, \ I_{\rm C} = 0$	5.0			V
I <sub>BL</sub>	Base Cutoff Current	$V_{CE} = 30 \text{ V}, \text{ V}_{BE} = 3.0 \text{ V}$			50	nA
I <sub>CEX</sub>	Collector Cutoff Current	$V_{CE} = 30 \text{ V}, \text{ V}_{BE} = 3.0 \text{ V}$			50	nA
	ACTERISTICS					
			<u> </u>			
h <sub>FE</sub>	DC Current Gain *	$I_{\rm C} = 0.1 \text{ mA}, V_{\rm CE} = 1.0 \text{ V}$	60 40			

FFB3906 / FMB3906 / MMPQ3906

### ONCH

h <sub>FE</sub>	DC Current Gain *	$I_{C} = 0.1 \text{ mA}, V_{CE} = 1.0 \text{ V}$	60		
		MMPQ3906	40		
		I <sub>C</sub> = 1.0 mA, V <sub>CE</sub> = 1.0 V	80		
		MMPQ3906	60		
		$I_{C} = 10 \text{ mA}, V_{CE} = 1.0 \text{ V}$	100	300	
		MMPQ3906	75		
		$I_{C} = 50 \text{ mA}, V_{CE} = 1.0 \text{ V}$	60		
		$I_{C} = 100 \text{ mA}, V_{CE} = 1.0 \text{ V}$	30		
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 1.0 \text{ mA}$		0.25	V
		$I_{C} = 50 \text{ mA}, I_{B} = 5.0 \text{ mA}$		0.4	V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 1.0 \text{ mA}$	0.65	0.85	V
		$I_{C} = 50 \text{ mA}, I_{B} = 5.0 \text{ mA}$		0.95	V

### SMALL SIGNAL CHARACTERISTICS (MMPQ3906 only)

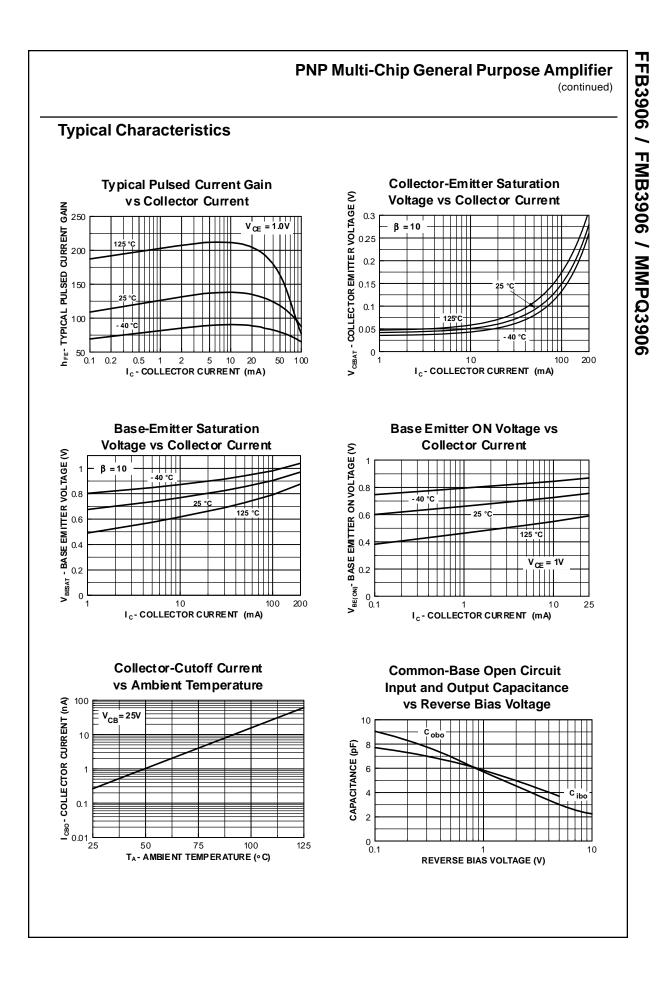
f <sub>T</sub>	Current Gain - Bandwidth Product	$I_{C} = 10 \text{ mA}, V_{CE} = 20 \text{ V},$ f = 100 MHz	200	MHz
C <sub>obo</sub>	Output Capacitance	$V_{CB} = 5.0 \text{ V}, I_E = 0,$ f = 140 kHz	4.5	pF
Cibo	Input Capacitance	$V_{EB} = 0.5 V, I_C = 0,$ f = 140 kHz	10	pF

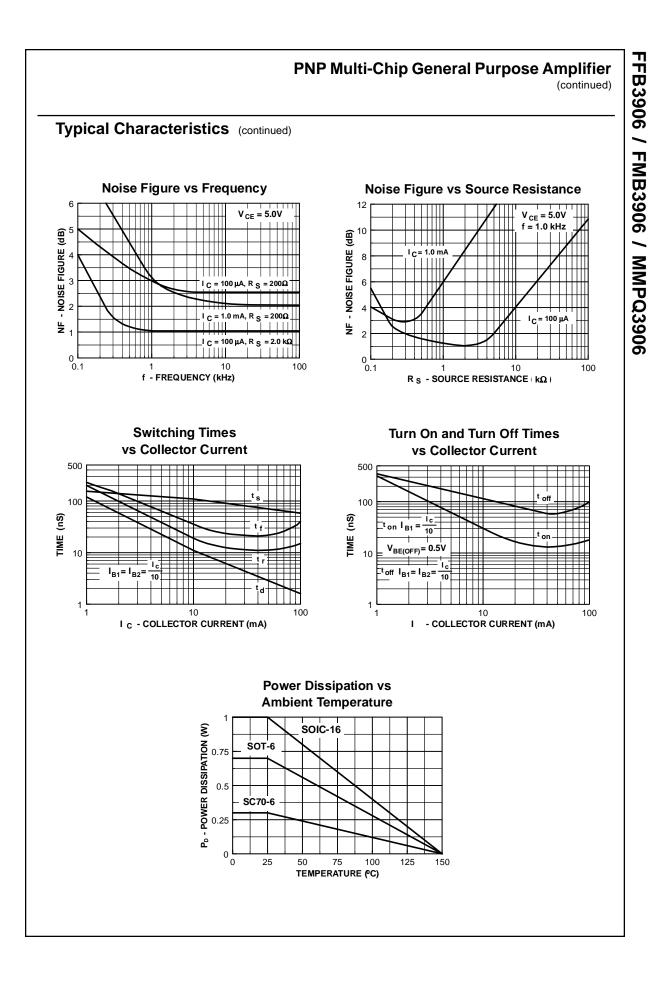
\*Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%

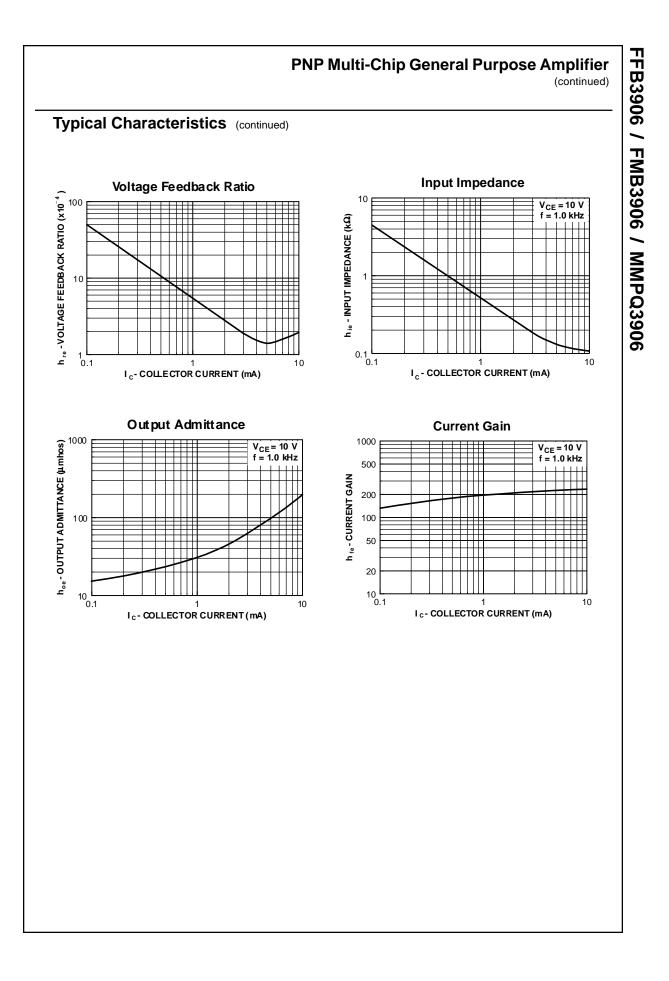
NOTE: All voltages (V) and currents (A) are negative polarity for PNP transistors.

# **Spice Model**

PNP (Is=1.41f Xti=3 Eg=1.11 Vaf=18.7 Bf=180.7 Ne=1.5 Ise=0 Ikf=80m Xtb=1.5 Br=4.977 Nc=2 Isc=0 Ikr=0 Rc=2.5 Cjc=9.728p Mjc=.5776 Vjc=.75 Fc=.5 Cje=8.063p Mje=.3677 Vje=.75 Tr=33.42n Tf=179.3p Itf=.4 Vtf=4 Xtf=6 Rb=10)







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