International **IOR** Rectifier

HFA04TB60

HEXFRED™

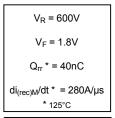
Ultrafast, Soft Recovery Diode

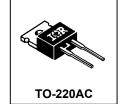
Features

- · Ultrafast Recovery
- · Ultrasoft Recovery
- Very Low I_{RRM}
- · Very Low Qrr
- · Specified at Operating Conditions

Benefits

- · Reduced RFI and FMI
- Reduced Power Loss in Diode and Switching Transistor
- · Higher Frequency Operation
- Reduced Snubbing
- · Reduced Parts Count





Description

International Rectifier's HFA04TB60 is a state of the art ultra fast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 600 volts and 8 amps per Leg continuous current, the HFA04TB60 is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultra fast recovery time, the HEXFRED product line features extremely low values of peak recovery current (I_{RRM}) and does not exhibit any tendency to "snap-off" during the t_b portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED HFA04TB60 is ideally suited for applications in power supplies and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.

Absolute Maximum Ratings

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	Parameter	Max	Units				
V _R	Cathode-to-Anode Voltage	600	V				
I _F @ T _C = 100°C	Continuous Forward Current	4.0					
I _{FSM}	Single Pulse Forward Current	25	Α				
I _{FRM}	Maximum Repetitive Forward Current	16					
P _D @ T _C = 25°C	Maximum Power Dissipation	25	w				
P _D @ T _C = 100°C	Maximum Power Dissipation	10					
TJ	Operating Junction and	55 to 1450					
T _{STG}	Storage Temperature Range	-55 to +150	С				

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Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min	Тур	Max	Units	Test Conditions		
V_{BR}	Cathode Anode Breakdown Voltage	600			V	$I_R = 100 \mu A$		
V _{FM}	Max Forward Voltage		1.5	1.8	٧	I _F = 4.0A		
			1.8	2.2		I _F = 8.0A See Fig	g. 1	
			1.4	1.7		I _F = 4.0A, T _J = 125°C		
I _{RM}	Max Reverse Leakage Current		0.17	3.0	μΑ	V _R = V _R Rated See Fig	g. 2	
			44	300		$T_J = 125$ °C, $V_R = 0.8 \times V_R$ Rated		
CT	Junction Capacitance		4.0	8.0	pF	V _R = 200V See Fig	g. 3	
LS	Series Inductance				nH	Measured lead to lead 5mm from		
			8.0			package body		

Dynamic Recovery Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min	Тур	Max	Units	Test Conditions		
t _{rr}	Reverse Recovery Time		17			$I_F = 1.0A$, $di_f/dt = 200A/\mu s$, $V_R = 30$		
t _{rr1}	See Fig. 5, 6 & 16		28	42	ns	T _J = 25°C		
t _{rr2}	-		38	57		T _J = 125°C	I _F = 4.0A	
I _{RRM1}	Peak Recovery Current		2.9	5.2	Α	T _J = 25°C		
I _{RRM2}	See Fig. 7& 8		3.7	6.7	^	T _J = 125°C	V _R = 200V	
Q _{rr1}	Reverse Recovery Charge		40	60	nC	T _J = 25°C		
Q _{rr2}	See Fig. 9 & 10		70	105	IIC	T _J = 125°C	di _f /dt = 200A/µs	
di _{(rec)M} /dt1	Peak Rate of Fall of Recovery Current		280		A/us	T _J = 25°C		
di _{(rec)M} /dt2	During t _b See Fig. 11 & 12		235		-λ/μδ	T _J = 125°C		

Thermal - Mechanical Characteristics

	Parameter	Min	Тур	Max	Units
T _{lead} ①	Lead Temperature			300	℃
RthJC	Thermal Resistance, Junction to Case			5.0	
R _{thA} ②	Thermal Resistance, Junction to Ambient			80	K/W
R _{ths} ③	Thermal Resistance, Case to Heat Sink		0.5		
Wt	Weight		2.0		g
			0.07		(oz)
Т	Mounting Torque	6.0		12	Kg-cm
		5.0		10	lbf•in

① 0.063 in. from Case (1.6mm) for 10 sec

Typical Socket Mount
 Mounting Surface, Flat, Smooth and Greased

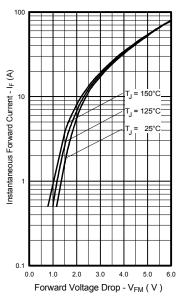


Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current,

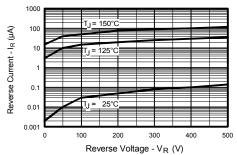


Fig. 2 - Typical Reverse Current vs. Reverse Voltage

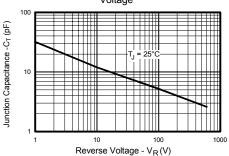


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

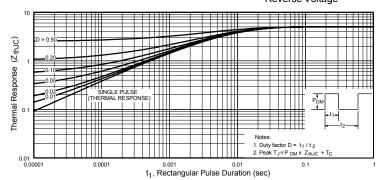


Fig. 4 - Maximum Thermal Impedance Zthic Characteristics

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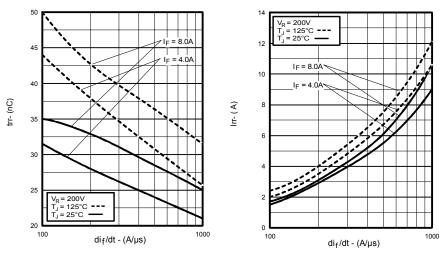
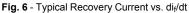


Fig. 5 - Typical Reverse Recovery vs. di_f/dt



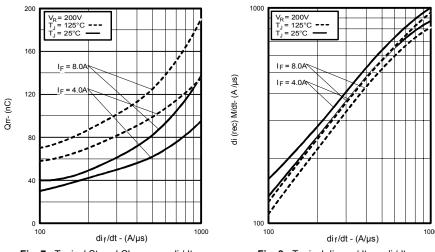


Fig. 7 - Typical Stored Charge vs. di_f/dt

 $\textbf{Fig. 8} \text{ - Typical } \text{di}_{(\text{rec})\text{M}}/\text{dt vs. } \text{di}_{\text{f}}/\text{dt},$

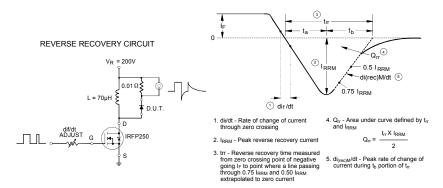
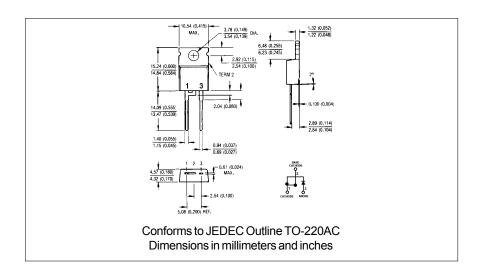


Fig. 9 - Reverse Recovery Parameter Test Circuit

Fig. 10 - Reverse Recovery Waveform and Definitions

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Data and specifications subject to change without notice.