## MURD320T4G, SURD8320T4G

# **SWITCHMODE Power Rectifier**

## **DPAK Surface Mount Package**

These state-of-the-art devices are designed for use in switching power supplies, inverters and as free wheeling diodes.

#### **Features**

- Ultrafast 35 Nanosecond Recovery Time
- Low Forward Voltage Drop
- Low Leakage
- AEC-Q101 Qualified and PPAP Capable
- SURD8 Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- All Packages are Pb–Free\*

#### **Mechanical Characteristics**

- · Case: Epoxy, Molded
- Weight: 0.4 Gram (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- ESD Ratings:
  - ♦ Machine Model = C (> 400 V)
  - ♦ Human Body Model = 3B (> 8 kV)

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	200	<b>V</b>
Average Rectified Forward Current (Rated V <sub>R</sub> , T <sub>C</sub> = 158°C)	I <sub>F(AV)</sub>	3.0	Α
Peak Repetitive Forward Current (Rated V <sub>R</sub> , Square Wave, 20 kHz, T <sub>C</sub> = 158°C)	I <sub>FRM</sub>	6.0	А
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, 60 Hz)	I <sub>FSM</sub>	75	А
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-65 to +175	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



### ON Semiconductor®

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## ULTRAFAST RECTIFIER 3.0 AMPERES, 200 VOLTS



DPAK CASE 369C



#### **MARKING DIAGRAM**



A = Assembly Location

Y = Year

WW = Work Week

G = Pb-Free Package

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MURD320T4G	DPAK (Pb-Free)	2,500/Tape & Reel 16 mm
SURD8320T4G	DPAK (Pb-Free)	2,500/Tape & Reel 16 mm

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

#### **MURD320T4G, SURD8320T4G**

#### THERMAL CHARACTERISTICS

Characteristics	Symbol	Value	Unit
Thermal Resistance – Junction-to-Case	$R_{ heta JC}$	6	°C/W
Thermal Resistance – Junction–to–Ambient (Note 1)	$R_{ heta JA}$	80	°C/W

<sup>1.</sup> Rating applies when surface mounted on the minimum pad sizes recommended.

#### **ELECTRICAL CHARACTERISTICS**

Characteristics	Symbol	Value	Unit
Maximum Instantaneous Forward Voltage Drop (Note 2) ( $i_F = 3 \text{ Amps}, T_J = 25^{\circ}\text{C}$ ) ( $i_F = 3 \text{ Amps}, T_J = 125^{\circ}\text{C}$ )	VF	0.95 0.75	Volts
Maximum Instantaneous Reverse Current (Note 2) (T <sub>J</sub> = 25°C, Rated dc Voltage) (T <sub>J</sub> = 125°C, Rated dc Voltage)	i <sub>R</sub>	5 500	μА
Maximum Reverse Recovery Time $ (I_F=1 \text{ Amp, di/dt}=50 \text{ Amps/}\mu\text{s, V}_R=30 \text{ V, T}_J=25^{\circ}\text{C}) \\ (I_F=0.5 \text{ Amp, i}_R=1 \text{ Amp, I}_{REC}=0.25 \text{ A, V}_R=30 \text{ V, T}_J=25^{\circ}\text{C}) $	t <sub>rr</sub>	35 25	ns

<sup>2.</sup> Pulse Test: Pulse Width = 300  $\mu s,$  Duty Cycle  $\leq$  2.0%.

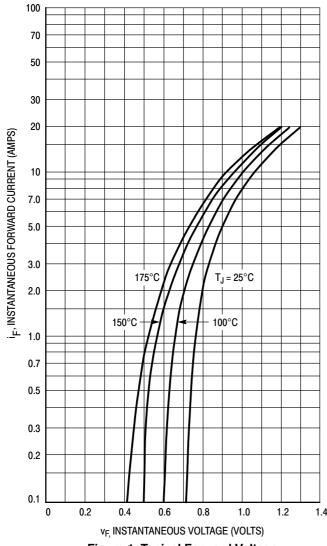


Figure 1. Typical Forward Voltage

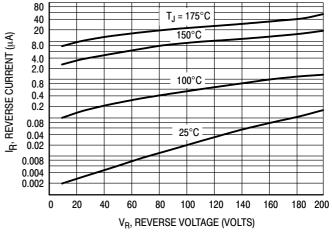


Figure 2. Typical Reverse Current\*

\* The curves shown are typical for the highest voltage device in the voltage grouping. Typical reverse current for lower voltage selections can be estimated from these curves if  $V_R$  is sufficiently below rated  $V_R$ .

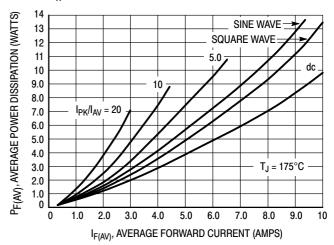


Figure 3. Average Power Dissipation

### **MURD320T4G, SURD8320T4G**

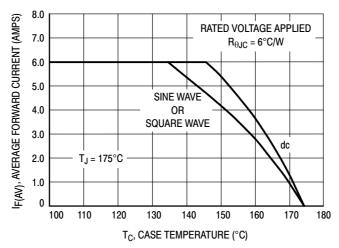


Figure 4. Current Derating, Case

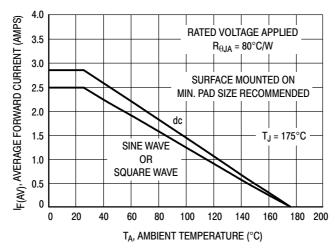


Figure 5. Current Derating, Ambient

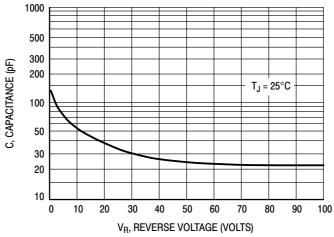


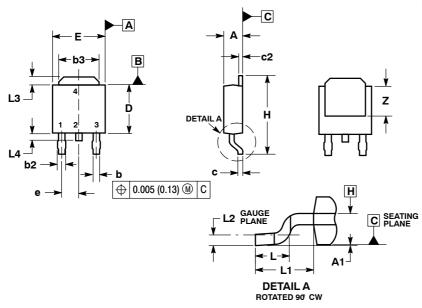
Figure 6. Typical Capacitance

#### **MURD320T4G, SURD8320T4G**

#### PACKAGE DIMENSIONS

#### **DPAK (SINGLE GAUGE)**

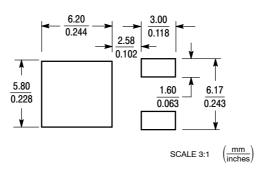
CASE 369C-01 ISSUE D



- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994
- 2. CONTROLLING DIMENSION: INCHES.
  3. THERMAL PAD CONTOUR OPTIONAL WITHIN DI-
- MENSIONS b3, L3 and Z.
  4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.006 INCHES PER SIDE.
  5. DIMENSIONS D AND E ARE DETERMINED AT THE
- OUTERMOST EXTREMES OF THE PLASTIC BODY.
- 6. DATUMS A AND B ARE DETERMINED AT DATUM

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.086	0.094	2.18	2.38	
A1	0.000	0.005	0.00	0.13	
b	0.025	0.035	0.63	0.89	
b2	0.030	0.045	0.76	1.14	
b3	0.180	0.215	4.57	5.46	
С	0.018	0.024	0.46	0.61	
c2	0.018	0.024	0.46	0.61	
D	0.235	0.245	5.97	6.22	
E	0.250	0.265	6.35	6.73	
е	0.090	BSC	2.29 BSC		
Н	0.370	0.410	9.40	10.41	
L	0.055	0.070	1.40	1.78	
L1	0.108 REF		2.74 REF		
L2	0.020 BSC		0.51 BSC		
L3	0.035	0.050	0.89	1.27	
L4		0.040		1.01	
Z	0.155		3.93		

#### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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