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40CDQ & 60CDQ SERIES AND SD241

40 and 60 Amp Dual Schottky Center Tap Rectifiers

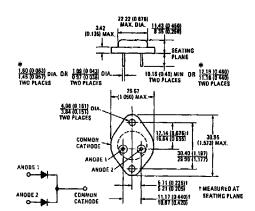
Major Ratings and Characteristics

С	haracteristics	40CDQ	SD241	60CDQ	Units	
IO Rec	tangular Waveform	40 60		60		
Sint	soidal Waveform	36	54	54	Α	
IFSM @ 50 Hz		38	30	475		
	@ 60 Hz	40	30	500	^	
12t	@ 50 Hz	730		1140	. 2.	
	@ 60 Hz	6	36	1040	A ² s	
$12\sqrt{t}$		10,325		16,130	$A^2\sqrt{s}$	
VRWM		20 - 45	35	20 - 45	٧	
C _t @ -5V		1400			рF	
TJ		-	°C			

Description/Features

The 40CDQ and 60CDQ Dual Schottky Rectifier Series and SD241 amploy the "830" process which results in a very low ratio of reverse leakage current to junction temperature. In addition to offering improvements in reliability and performance, they are rugged devices with a guaranteed repetitive peak reverse voltage capability, and excellent ability to withstend reverse energy translents. They can be used in both existing and new designs.

- 175°C T_J operation
- 100% reverse energy tested (each junction)
- 400A and 500A surge, 60 Hz, one cycle (per junction)
- Extremely low reverse leakage: 10 mA @ 25°C
- No voltage derating of V_{RWM} over temperature range
- A guaranteed repetitive peak reverse voltage capability for short pulses which is 20% above V_{RWM}
- High power supply reliability
- Minimizes problem of thermal runaway
- TO-204AE (Modified TO-3) Case Style available (60CDQ series)
- Can be supplied to meat stringent military, serospace and other high-reliability requirements.



Conforms to JEDEC Outline TO-204AA (TO-3) *Conforms to JEDEC Outline TO-204AE (Modified TO-3) All Dimensions in Millimeters and (inches)

NJ Semi-Conductors reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However, NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.

Quality Semi-Conductors

VOLTAGE RATINGS PER JUNCTION

Part Numbers			V _{RWM} — Max. Working Peak Reverse Voltage (V) ②	VRRM — Max. Repetitive Peak Reverse Voltage (V) ③ (tp = 200 ns Max.)	V _R - Mex. Direct Reverse Voltage (V)(1)	
40CDQ020	-	60CDQ020	20	24	20	
40CDQ030	-	60CDQ030	² 30	36	30	
40CDQ035	SD241	60CDQ035	35 🕦	42 ③	35	
40CDQ040	_	60CDQ040	40	48	40	
40CDQ045	-	-60CDQ045 -	45	54	45	

ELECTRICAL SPECIFICATIONS

		40CDQ	SD241	60CDQ	Units	Conditions
10	Max, average output current from center tap circuit	40	60	60	A	180° conduction, rectangular waveform, T _C = -40 to 143°C for 40CDQ, T _C = -40 to 120°C for 60CDQ.
		36	· 54	54		180° conduction, sinuscidal waveform, T _C = -40 to 141°C for 40°CDQ, T _C = -40 to 116°C for 60°CDQ.
^I FSM	Max. peak one cycle, non- repatitive surge current,	.380 47		475	Α	50 Hz half cycle sine wave or 6 ms rectangular pulse, condition and with rated
	per junction	400		500		60 Hz half cycle sine wave or 5 ms rectangular pulse,
		455 570 475 595			Α	50 Hz With V _{RWM} = 0 following surge, initial T _{.1} = 175°C.
12t	Max. 12t for fusing, per junction	730 665		1140	A ² s	t = 10 ms. Rated V _{RWM} following surge,
	Junction			1040		t = 8.3 ms. initial T _j = 175°C.
{2 _t	Max, 12t for individual function fusing, per junction	1030 940		1610	A ² s	t = 10 ms. V _{RWM} following surge = 0,
	per function			1470	, , ,	t = 8.3 ms. Initial T _j = 175°C.
ı²√t	Max. I ² √t for individual (i) junction fusing, per junction	10,325 16,130		$A^2\sqrt{s}$.t = 0.1 to 10 ms, T _J = 175 ^O C, V _{RWM} = 0 following surg	
VFM	Max. peak forward voltage per junction	0.70	0.70 0.82			T _J = 25°C IFM = 20A peak for 40CDQ, IFM = 30A peak for 60CDQ and SD241
		0.91	1.0	09	V	T _J = 25°C 180° rectangular wave. Rated I _F (AV) (40A peak for 40CDQ, 60A
	•	0.74	0.74 0.92			T _J = 175°C peak for 80CDQ and 8D241) 180° rectangular wave.
IRM	Mex. peak reverse current,	10		mA	T. = 250C	
	per junction	. 20			T _J = 125°C V _{RM} = rated V _{RWM}	
IRRM:	Max. rapetitive peak reverse current	2,0		, A	$T_C = 25^{\circ}C$, $t_p = 2 \mu s$ rectangular pulse, $f = 1 \text{ kHz.}$ (1) see fig. 11 for test circuit.	
C _t	Max. capacitance, per junction	1400		pF	$T_C = 25^{\circ}C$, $V_R = 5$ Vdc (Test signal in the range of 100 kHz to 1 MHz)	
dv/dt	Max. rate of application of reverse voltage, per junction	1000		V/µs	T _C = 25°C, V _{RM} = reted V _{RWM}	

THERMAL-MECHANICAL SPECIFICATIONS

Tj	Max. operating junction temperature range	-55 to 1	76	°C	
Tstg	Max. storage temperature range	-55 to 1	76	· •c	
R _{thJC}	Max. thermal resistance, junction-to-case, DC operation	1.4		deg. C/W	Based on power dissipated in one junction, both junctions operating.
	Mex. composite thermal resistance, junction-to-case, DC operation	. 0.7			Based on power dissipated in both junctions.
RthCs	Thermal resistance, case-to-sink	0.2		deg. C/W	Mounting surface flat, smooth and greated.
wt	Approximate weight	12.8 (0.4	6)	g (oz.)	
	Case Style	TO-204AA (TO-3)	TO-204AE (Modified TO-3)		Terminals 1 and 2: Anodes 1 and 2 Case: Common Cathodes

③ T_C ≈ 0 to 172°C, 180° conduction ④ T_C = -55 to 162°C.

① T_C = -55 to 172°C, 180° conduction ① For SD241 rated V_{RWM} and V_{RRM} = 45V @ T_J = 25°C, = 35V @ T_J = 150°C

⁽¹⁾ t^2t for time $t_{\chi} = t^2\sqrt{t} + \sqrt{t_{\chi}}$.

¹ For test circuit refer to Fig. 11.