

# New Jersey Semi-Conductor Products, Inc.

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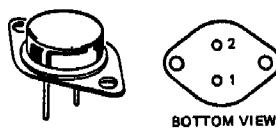
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## 2N6755 ■ 2N6756

### N-Channel Enhancement Mode MOSPOWER

#### APPLICATIONS

- Switching Regulators
- Converters
- Motor Drivers



PIN 1 – Gate  
PIN 2 – Source  
CASE – Drain

BOTTOM VIEW

(TO-3)

#### PRODUCT SUMMARY

Part Number	$V_{DSS}$ Volts	$r_{DS(ON)}$ (ohms)
2N6755	60	0.25
2N6756	100	0.18

#### ABSOLUTE MAXIMUM RATINGS ( $T_C = 25^\circ C$ unless otherwise noted)

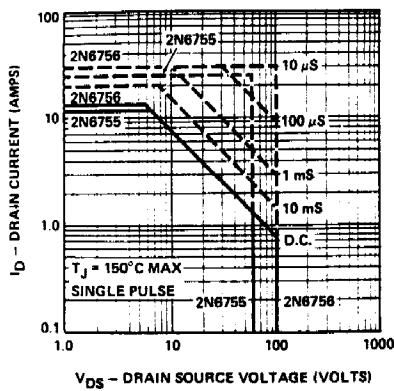
Parameter	2N6755	2N6756	Units
$V_{DS}$	Drain-Source Voltage	60°	V
$V_{DGR}$	Drain-Gate Voltage ( $R_{GS} = 1 M\Omega$ )	60°	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current	$\pm 12^\circ$	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current	$\pm 8^\circ$	A
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	$\pm 25$	A
$V_{GS}$	Gate-Source Voltage <sup>2</sup>	$\pm 40$	V
$P_D @ T_C = 25^\circ C$	Max. Power Dissipation	75°	W
$P_D @ T_C = 100^\circ C$	Max. Power Dissipation	30°	W
Junction to Case	Linear Derating Factor	0.6°	W/ $^\circ C$
Junction to Ambient	Linear Derating Factor	0.033	W/ $^\circ C$
$T_J$	Operating and		
$T_{stg}$	Storage Temperature Range	-55 To 150°	$^\circ C$
Lead Temperature (1/16" from case for 10 secs.)		300°	$^\circ C$

1 Pulse Test: Pulsewidth  $\leq 300 \mu s$ , Duty Cycle  $\leq 2\%$

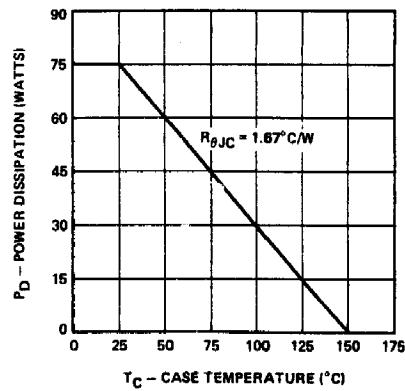
2 Exceeds Jedec Values

\* Jedec Registered Values

#### Active Region



#### Power Derating



Quality Semi-Conductors

## ELECTRICAL CHARACTERISTICS ( $T_C = 25^\circ C$ unless otherwise noted)

### STATIC

Parameter		Type	Min.	Typ.	Max.	Units	Test Conditions
$BV_{DSS}$	Drain-Source Breakdown Voltage	2N6755	60			V	$V_{GS} = 0$ $I_D = 1 \text{ mA}$
		2N6756	100			V	
$V_{GS(th)}$	Gate-Threshold Voltage	All	2.0*		4.0*	V	$V_{DS} = V_{GS}$ , $I_D = 1 \text{ mA}$
$I_{GSSF}$	Gate-Body Leakage Forward	All			100*	nA	$V_{GS} = 20V$
$I_{GSSR}$	Gate-Body Leakage Reverse	All			-100*	nA	$V_{GS} = -20V$
$I_{DSS}$	Zero Gate Voltage Drain Current	All		0.1	1.0*	mA	$V_{DS} = \text{Max. Rating}$ , $V_{GS} = 0$
		All		0.2	4.0*	mA	$V_{DS} = \text{Max. Rating}$ , $V_{GS} = 0$ $T_C = 125^\circ C$
$I_{D(on)}$	On-State Drain Current <sup>1</sup>	2N6755	12			A	$V_{GS} = 10V$ , $V_{DS} = 15V$
		2N6756	14			A	$V_{GS} = 10V$ , $V_{DS} = 15V$
$V_{DS(on)}$	Static Drain-Source On-State Voltage <sup>1</sup>	2N6755			3.0*	V	$V_{GS} = 10V$ , $I_D = 12A$
		2N6756			2.52*	V	$V_{GS} = 10V$ , $I_D = 14A$
$R_{DS(on)}$	Static Drain-Source On-State Resistance <sup>1</sup>	2N6755		0.2	0.25*	$\Omega$	$V_{GS} = 10V$ , $I_D = 8.0A$
		2N6756		0.14	0.18*	$\Omega$	$V_{GS} = 10V$ , $I_D = 9.0A$
$R_{DS(on)}$	Static Drain-Source On-State Resistance <sup>1</sup>	2N6755			0.45*	$\Omega$	$V_{GS} = 10V$ , $I_D = 8.0A$ , $T_C = 125^\circ C$
		2N6756			0.33*	$\Omega$	$V_{GS} = 10V$ , $I_D = 9.0A$ , $T_C = 125^\circ C$

### DYNAMIC

$g_{fs}$	Forward Transductance <sup>1</sup>	All	4.0*	5.5	12.0*	S (U)	$V_{DS} = 15V$ , $I_D = 9.0A$
$C_{iss}$	Input Capacitance	All	350*	600	800*	pF	$V_{GS} = 0$ , $V_{DS} = 25V$ $f = 1 \text{ MHz}$
$C_{oss}$	Output Capacitance	All	150*	300	500*	pF	
$C_{rss}$	Reverse Transfer Capacitance	All	50*	100	150*	pF	$V_{DD} = 38V$ , $I_D = 9.0A$ $R_g = 7.5\Omega$ , $R_L = 4.0\Omega$ (MOSFET switching times are essentially independent of operating temperature.)
$t_{d(on)}$	Turn-On Delay Time	All			30*	ns	
$t_r$	Rise Time	All			75*	ns	
$t_{d(off)}$	Turn-Off Delay Time	All			40*	ns	
$t_f$	Fall Time	All			45*	ns	

### THERMAL RESISTANCE

$R_{thJC}$	Junction-to-Case	All			1.67*	°C/W	
$R_{thJA}$	Junction-to-Ambient	All			30	°C/W	Free Air Operation

### BODY-DRAIN DIODE RATINGS AND CHARACTERISTICS

$I_S$	Continuous Source Current (Body Diode)	2N6755			-12*	A	Modified MOSPOWER symbol showing the integral P-N Junction rectifier
		2N6756			-14*	A	
$I_{SM}$	Source Current <sup>1</sup> (Body Diode)	2N6755			-25	A	
		2N6756			-30	A	
$V_{SD}$	Diode Forward Voltage <sup>1</sup>	2N6755	-0.85*		-1.7*	V	$T_C = 25^\circ C$ , $I_S = -12A$ , $V_{GS} = 0$
		2N6756	-0.9*		-1.8*	V	$T_C = 25^\circ C$ , $I_S = 14A$ , $V_{GS} = 0$
$t_{rr}$	Reverse Recovery Time	All		300		ns	$T_J = 150^\circ C$ , $I_F = I_S$ , $dI_F/dt = 100 \text{ A}/\mu s$

<sup>1</sup> Pulse Test: Pulse Width < 300  $\mu$ sec, Duty Cycle < 2%

\* JEDEC Registered Values

Data Sheet Curves: VNDE10