2SK1313(L)(S), 2SK1314(L)(S)

Silicon N-Channel MOS FET

HITACHI

Application

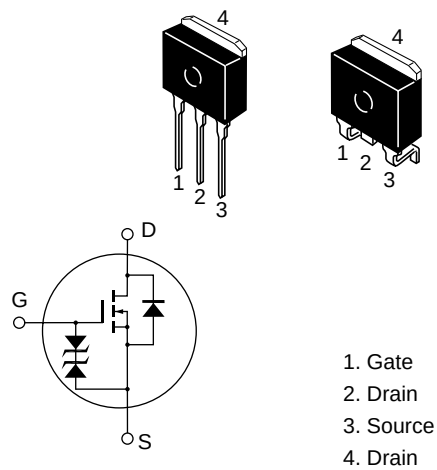
High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

Outline

LDPAK



1. Gate
2. Drain
3. Source
4. Drain

Hitachi semiconductor

Absolute Maximum Ratings (Ta = 25°C)

Item		Symbol	Ratings	Unit
Drain to source voltage	2SK1313	V_{DSS}	450	V
	2SK1314		500	
Gate to source voltage		V_{GSS}	±30	V
Drain current		I_D	5	A
Drain peak current		$I_{D(pulse)}$*[1]	20	A
Body to drain diode reverse drain current		I_{DR}	5	A
Channel dissipation		Pch*[2]	50	W
Channel temperature		Tch	150	°C
Storage temperature		Tstg	–55 to +150	°C

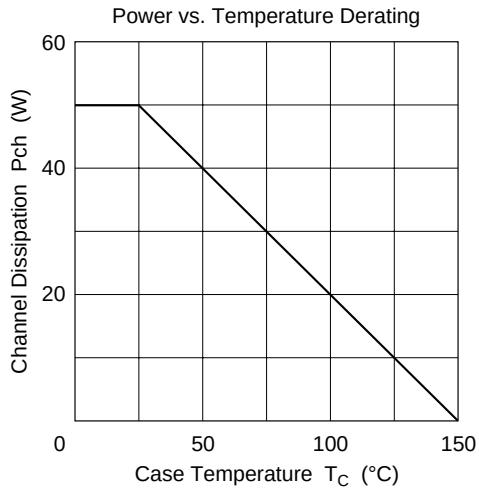
Notes: 1. PW 10 µs, duty cycle 1%
2. Value at T_C = 25°C

Electrical Characteristics (Ta = 25°C)

Item		Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	2SK1313	$V_{(BR)DSS}$	450	—	—	V	I_D = 10 mA, V_{GS} = 0
	2SK1314		500				
Gate to source breakdown voltage		$V_{(BR)GSS}$	±30	—	—	V	I_G = ±100 µA, V_{DS} = 0
Gate to source leak current		I_{GSS}	—	—	±10	µA	V_{GS} = ±25 V, V_{DS} = 0
Zero gate voltage drain current	2SK1313	I_{DSS}	—	—	250	µA	V_{DS} = 360 V, V_{GS} = 0
	2SK1314						V_{DS} = 400 V, V_{GS} = 0
Gate to source cutoff voltage		$V_{GS(off)}$	2.0	—	3.0	V	I_D = 1 mA, V_{DS} = 10 V
Static Drain to source on state resistance	2SK1313	$R_{DS(on)}$	—	1.0	1.4		I_D = 2.5 A, V_{GS} = 10 V *[1]
	2SK1314		—	1.2	1.5		
Forward transfer admittance		|yfs|	2.5	4.0	—	S	I_D = 2.5 A, V_{DS} = 10 V *[1]
Input capacitance		Ciss	—	640	—	pF	V_{DS} = 10 V, V_{GS} = 0, f = 1 MHz
Output capacitance		Coss	—	160	—	pF	
Reverse transfer capacitance		Crss	—	20	—	pF	
Turn-on delay time		$t_{d(on)}$	—	10	—	ns	I_D = 2.5 A, V_{GS} = 10 V, R_L = 12
Rise time		t_r	—	25	—	ns	
Turn-off delay time		$t_{d(off)}$	—	50	—	ns	
Fall time		t_f	—	30	—	ns	
Body to drain diode forward voltage		V_{DF}	—	0.95	—	V	I_F = 5 A, V_{GS} = 0
Body to drain diode reverse recovery time		t_{rr}	—	300	—	ns	I_F = 5 A, V_{GS} = 0, di_F/dt = 100 A/µs

Note: 1. Pulse test

See characteristic curves of 2SK1155, 2SK1156.


Power vs. Temperature Derating
Channel Dissipation Pch (W)
60
40
20
0
50
100
150
Case Temperature T_C (°C)

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