

# 2SK1519, 2SK1520

Silicon N-Channel MOS FET

## HITACHI

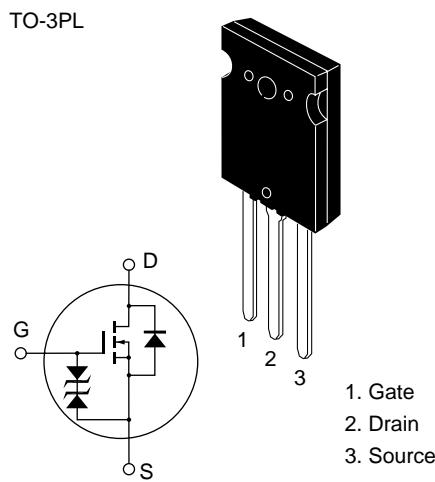
### Application

High speed power switching

### Features

- Low on-resistance
- High speed switching
- Low drive current
- Built-in fast recovery diode ( $t_{rr} = 120$  ns)
- Suitable for motor control, switching regulator, DC-DC converter

### Outline



## **2SK1519, 2SK1520**

### **Absolute Maximum Ratings (Ta = 25°C)**

<b>Item</b>		<b>Symbol</b>	<b>Ratings</b>	<b>Unit</b>
Drain to source voltage	2SK1519	V <sub>DSS</sub>	450	V
	2SK1520		500	
Gate to source voltage		V <sub>GSS</sub>	±30	V
Drain current		I <sub>D</sub>	30	A
Drain peak current		I <sub>D(pulse)</sub> <sup>*1</sup>	120	A
Body to drain diode reverse drain current		I <sub>DR</sub>	30	A
Channel dissipation		Pch <sup>*2</sup>	200	W
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature		T <sub>stg</sub>	-55 to +150	°C

Notes: 1. PW 10 µs, duty cycle 1%

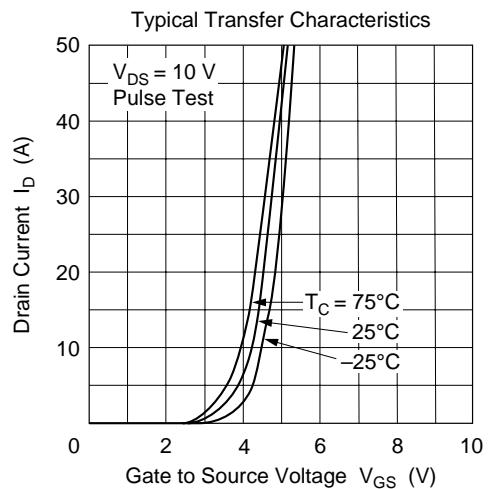
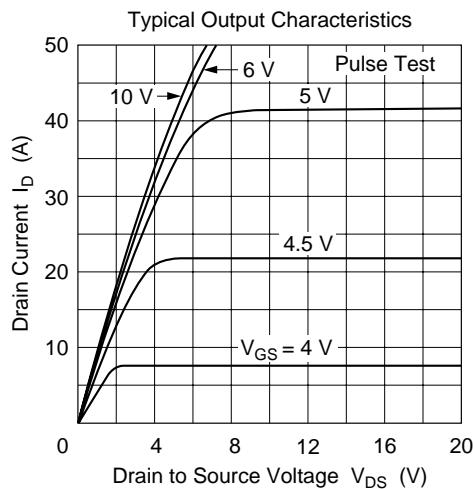
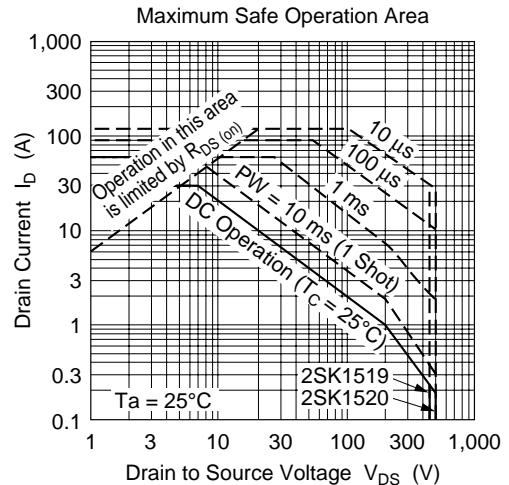
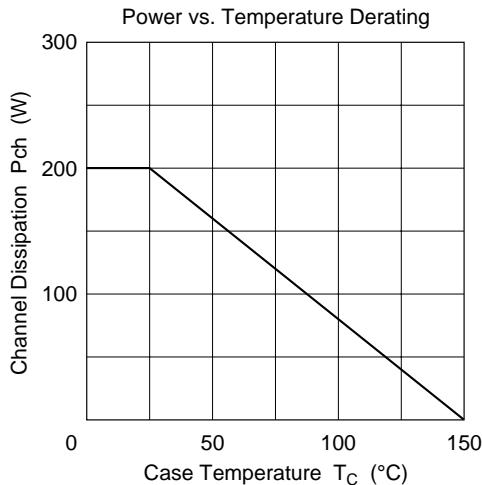
2. Value at T<sub>c</sub> = 25°C

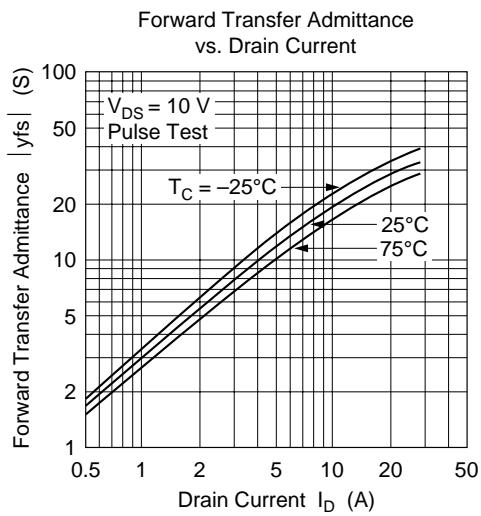
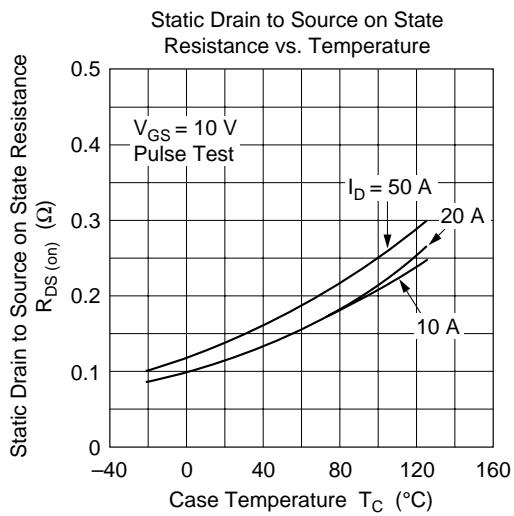
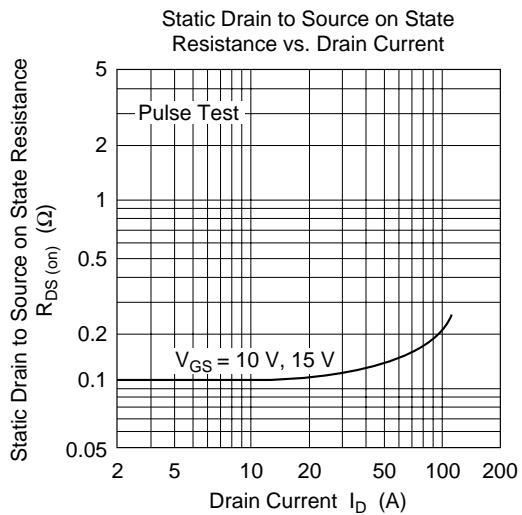
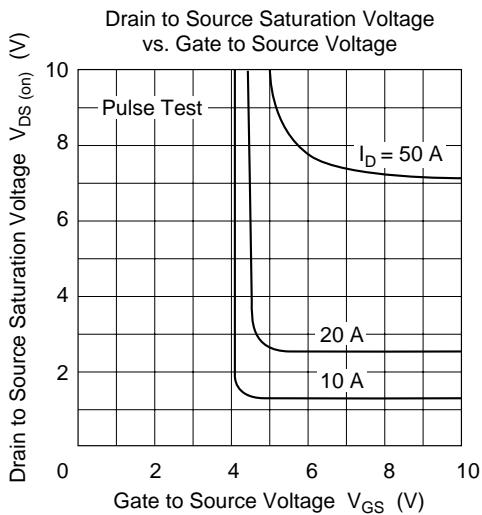
**Electrical Characteristics (Ta = 25°C)**

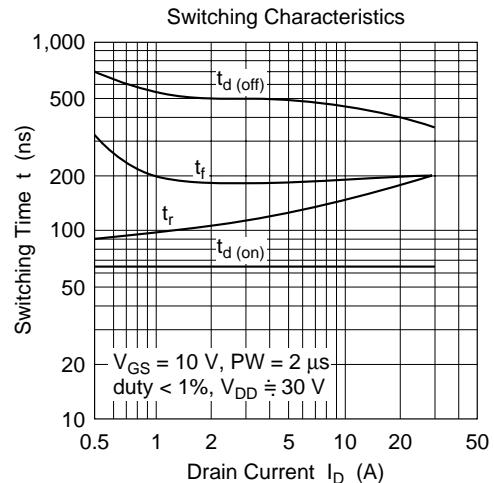
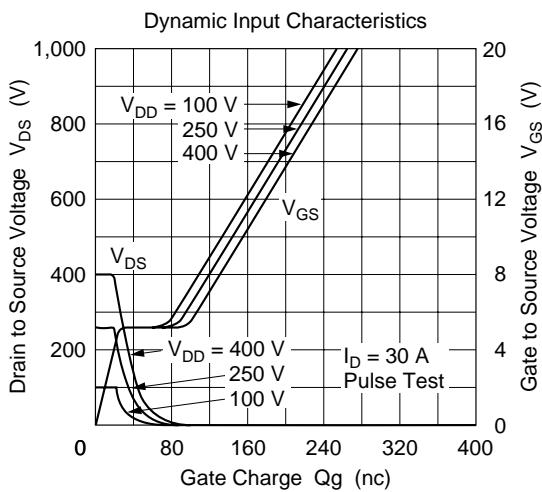
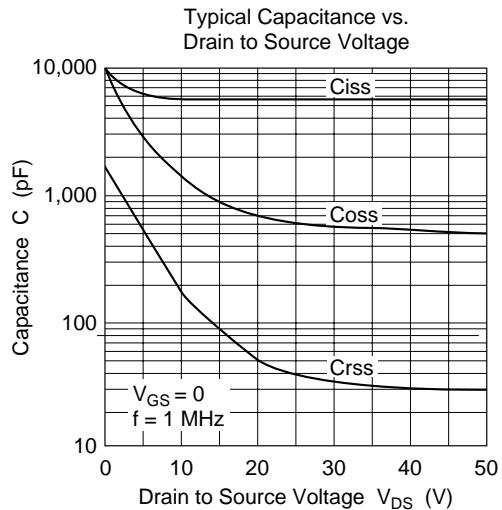
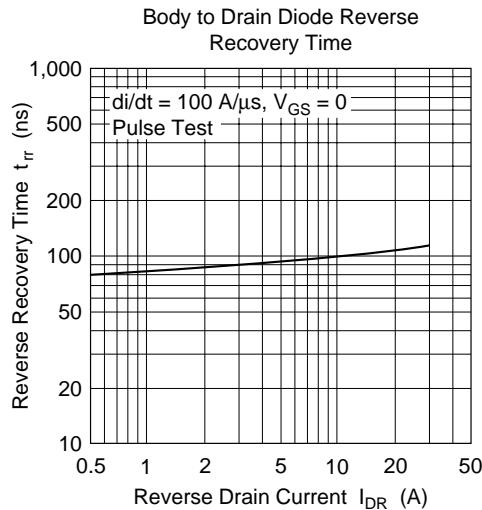
<b>Item</b>		<b>Symbol</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Unit</b>	<b>Test conditions</b>
Drain to source breakdown voltage	2SK1519 2SK1520	V <sub>(BR)DSS</sub>	450 500	—	—	V	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0
Gate to source breakdown voltage		V <sub>(BR)GSS</sub>	±30	—	—	V	I <sub>G</sub> = ±100 µA, V <sub>DS</sub> = 0
Gate to source leak current		I <sub>GSS</sub>	—	—	±10	µA	V <sub>GS</sub> = ±25 V, V <sub>DS</sub> = 0
Zero gate voltage drain current	2SK1519 2SK1520	I <sub>DSS</sub>	—	—	250	µA	V <sub>DS</sub> = 360 V, V <sub>GS</sub> = 0 V <sub>DS</sub> = 400 V, V <sub>GS</sub> = 0
Gate to source cutoff voltage		V <sub>GS(off)</sub>	2.0	—	3.0	V	I <sub>D</sub> = 1 mA, V <sub>DS</sub> = 10 V
Static Drain to source on state resistance	2SK1519 2SK1520	R <sub>DS(on)</sub>	—	0.11 0.12	0.15 0.16		I <sub>D</sub> = 15 A, V <sub>GS</sub> = 10 V * <sup>1</sup>
Forward transfer admittance		y <sub>fs</sub>	15	25	—	S	I <sub>D</sub> = 15 A, V <sub>DS</sub> = 10 V * <sup>1</sup>
Input capacitance		C <sub>iss</sub>	—	5800	—	pF	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0,
Output capacitance		C <sub>oss</sub>	—	1550	—	pF	f = 1 MHz
Reverse transfer capacitance		C <sub>rss</sub>	—	170	—	pF	
Turn-on delay time		t <sub>d(on)</sub>	—	65	—	ns	I <sub>D</sub> = 15 A, V <sub>GS</sub> = 10 V,
Rise time		t <sub>r</sub>	—	170	—	ns	R <sub>L</sub> = 2
Turn-off delay time		t <sub>d(off)</sub>	—	415	—	ns	
Fall time		t <sub>f</sub>	—	200	—	ns	
Body to drain diode forward voltage		V <sub>DF</sub>	—	1.1	—	V	I <sub>F</sub> = 30 A, V <sub>GS</sub> = 0
Body to drain diode reverse recovery time		t <sub>rr</sub>	—	120	—	ns	I <sub>F</sub> = 30 A, V <sub>GS</sub> = 0, di <sub>F</sub> /dt = 100 A/µs

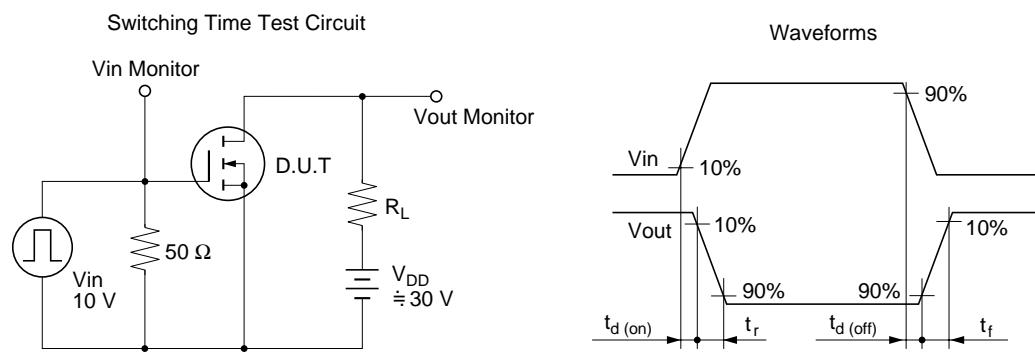
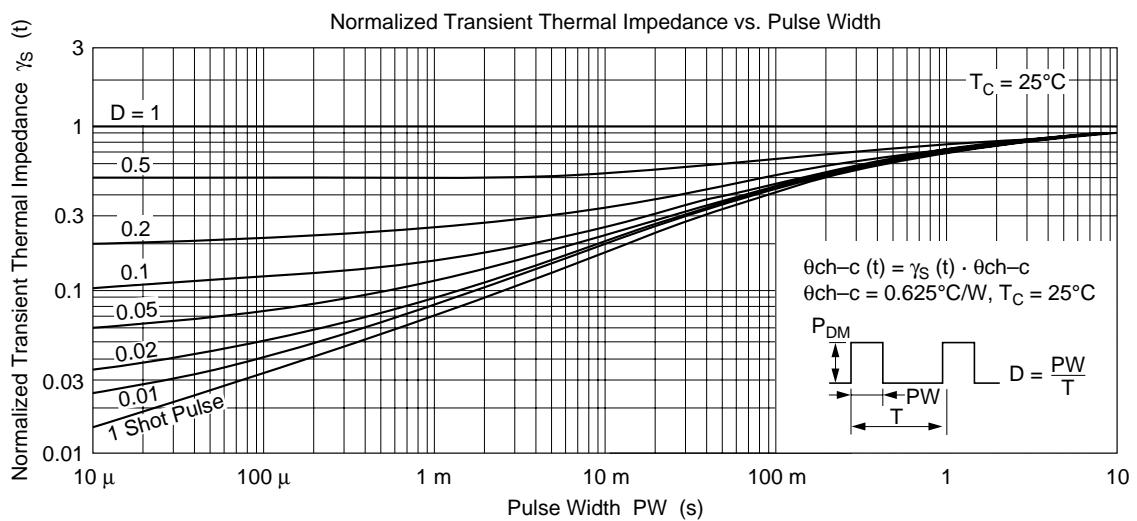
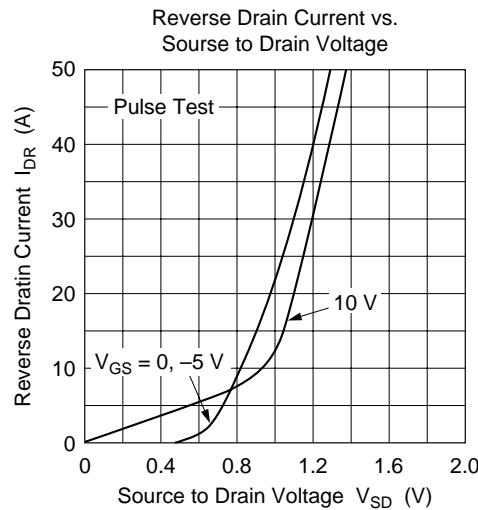
Note: 1. Pulse test

# 2SK1519, 2SK1520









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