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# 2SK2788

Silicon N Channel MOS FET  
High Speed Power Switching

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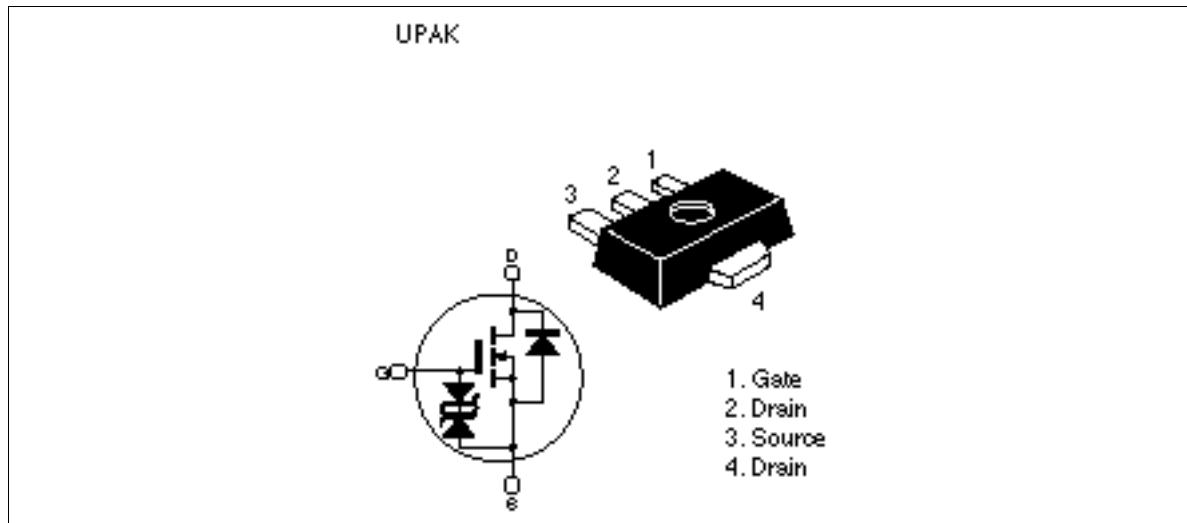
ADE-208-538  
1st. Edition

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## Features

- Low on-resistance  
 $R_{DS(on)} = 0.12\Omega$  typ ( $V_{GS} = 10$  V,  $I_D = 1$  A)
- Low drive current
- High speed switching
- 4V gate drive devices.

## Outline



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### Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	60	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	2	A
Drain peak current	I <sub>D(pulse)</sub> <sup>*1</sup>	4	A
Body to drain diode reverse drain current	I <sub>DR</sub>	2	A
Channel dissipation	P <sub>ch</sub> <sup>*2</sup>	1	W
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

Notes: 1. PW ≤ 10μs, duty cycle ≤ 1 %  
2. When using the alumina ceramic board (12.5 x 20 x 0.7 mm)

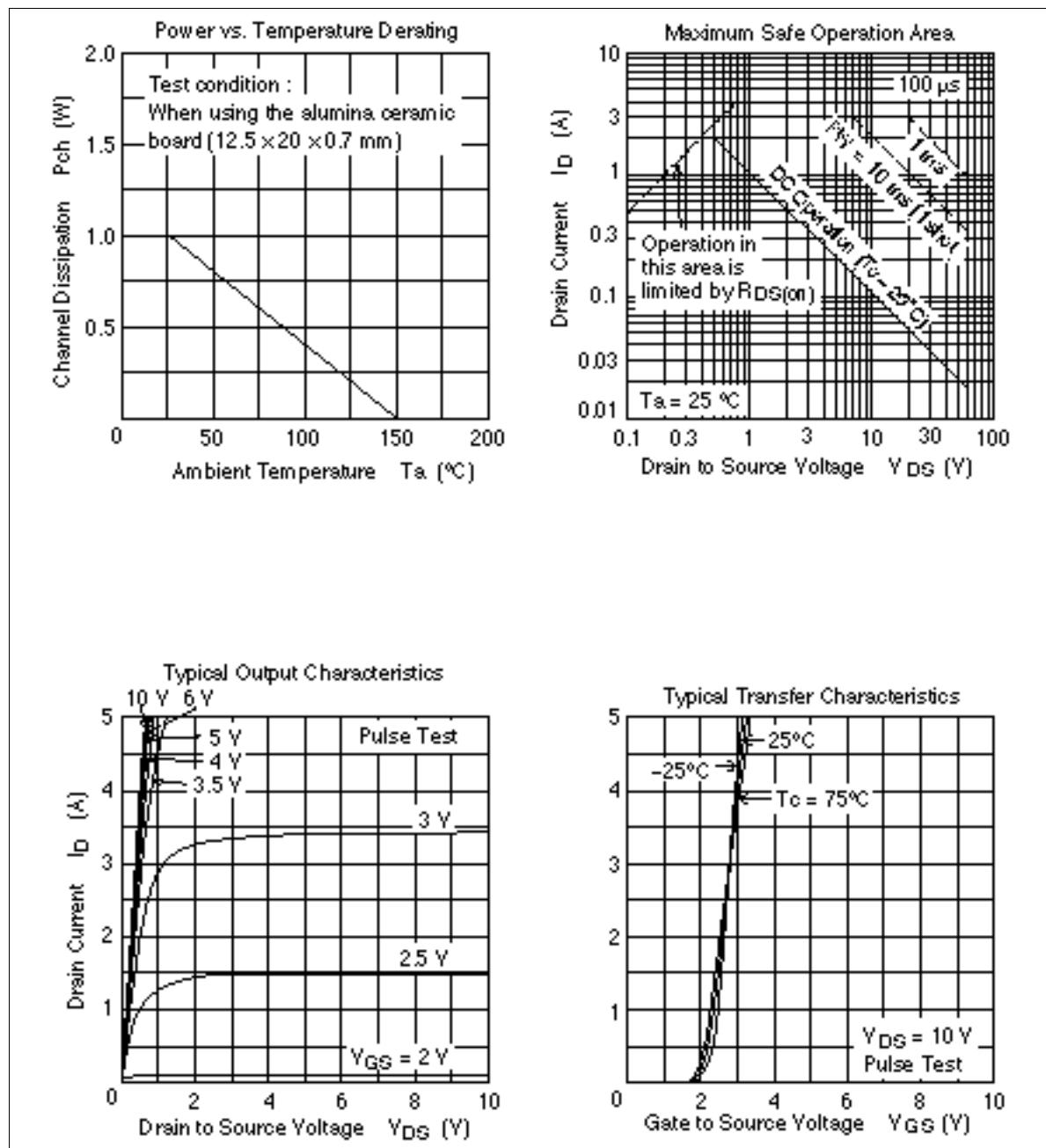
**Electrical Characteristics (Ta = 25°C)**

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	60	—	—	V	I <sub>D</sub> = 10mA, V <sub>GS</sub> = 0
Gate to source breakdown voltage	V <sub>(BR)GSS</sub>	±20	—	—	V	I <sub>G</sub> = ±100μA, V <sub>DS</sub> = 0
Zero gate voltage drain current	I <sub>DSS</sub>	—	—	10	μA	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0
Gate to source leak current	I <sub>GSS</sub>	—	—	±10	μA	V <sub>GS</sub> = ±16V, V <sub>DS</sub> = 0
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.0	—	2.0	V	I <sub>D</sub> = 1mA, V <sub>DS</sub> = 10V
Static drain to source on state resistance	R <sub>DS(on)</sub>	—	0.12	0.16	Ω	I <sub>D</sub> = 1 A, V <sub>GS</sub> = 10V <sup>*1</sup>
	R <sub>DS(on)</sub>	—	0.16	0.25	Ω	I <sub>D</sub> = 1A, V <sub>GS</sub> = 4V <sup>*1</sup>
Forward transfer admittance	y <sub>fs</sub>	1.6	2.8	—	S	I <sub>D</sub> = 1A, V <sub>DS</sub> = 10V <sup>*1</sup>
Input capacitance	C <sub>iss</sub>	—	180	—	pF	V <sub>DS</sub> = 10V
Output capacitance	C <sub>oss</sub>	—	90	—	pF	V <sub>GS</sub> = 0
Reverse transfer capacitance	C <sub>rss</sub>	—	30	—	pF	f = 1MHz
Turn-on delay time	t <sub>d(on)</sub>	—	9	—	ns	V <sub>GS</sub> = 10V, I <sub>D</sub> = 1A
Rise time	t <sub>r</sub>	—	15	—	ns	R <sub>L</sub> = 30Ω
Turn-off delay time	t <sub>d(off)</sub>	—	40	—	ns	
Fall time	t <sub>f</sub>	—	35	—	ns	
Body to drain diode forward voltage	V <sub>DF</sub>	—	0.9	—	V	I <sub>D</sub> = 2A, V <sub>GS</sub> = 0
Body to drain diode reverse recovery time	t <sub>rr</sub>	—	35	—	ns	I <sub>F</sub> = 2A, V <sub>GS</sub> = 0 di <sub>F</sub> / dt = 50A/μs

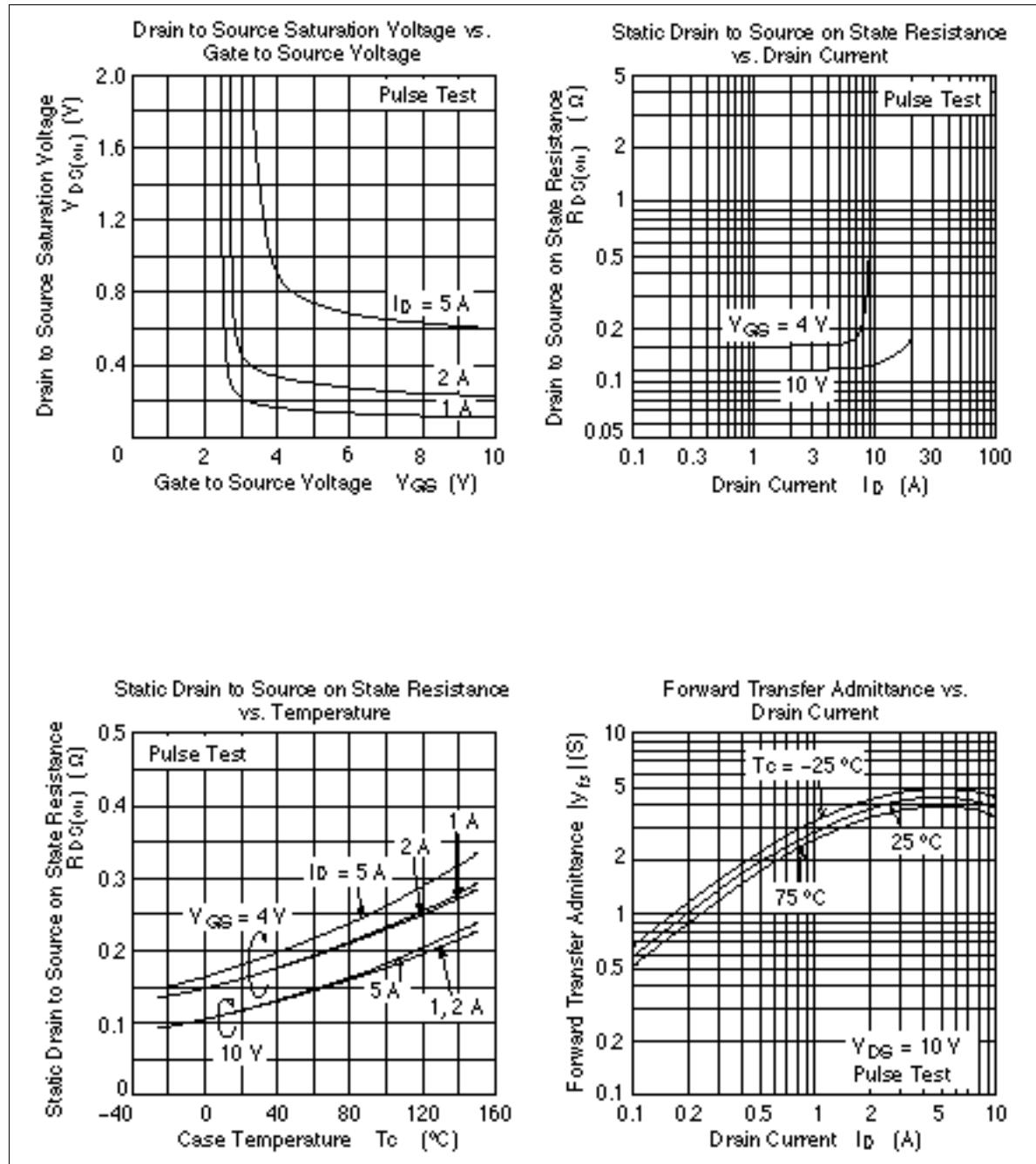
Notes: 1. Pulse test  
       2. Marking is "VY"

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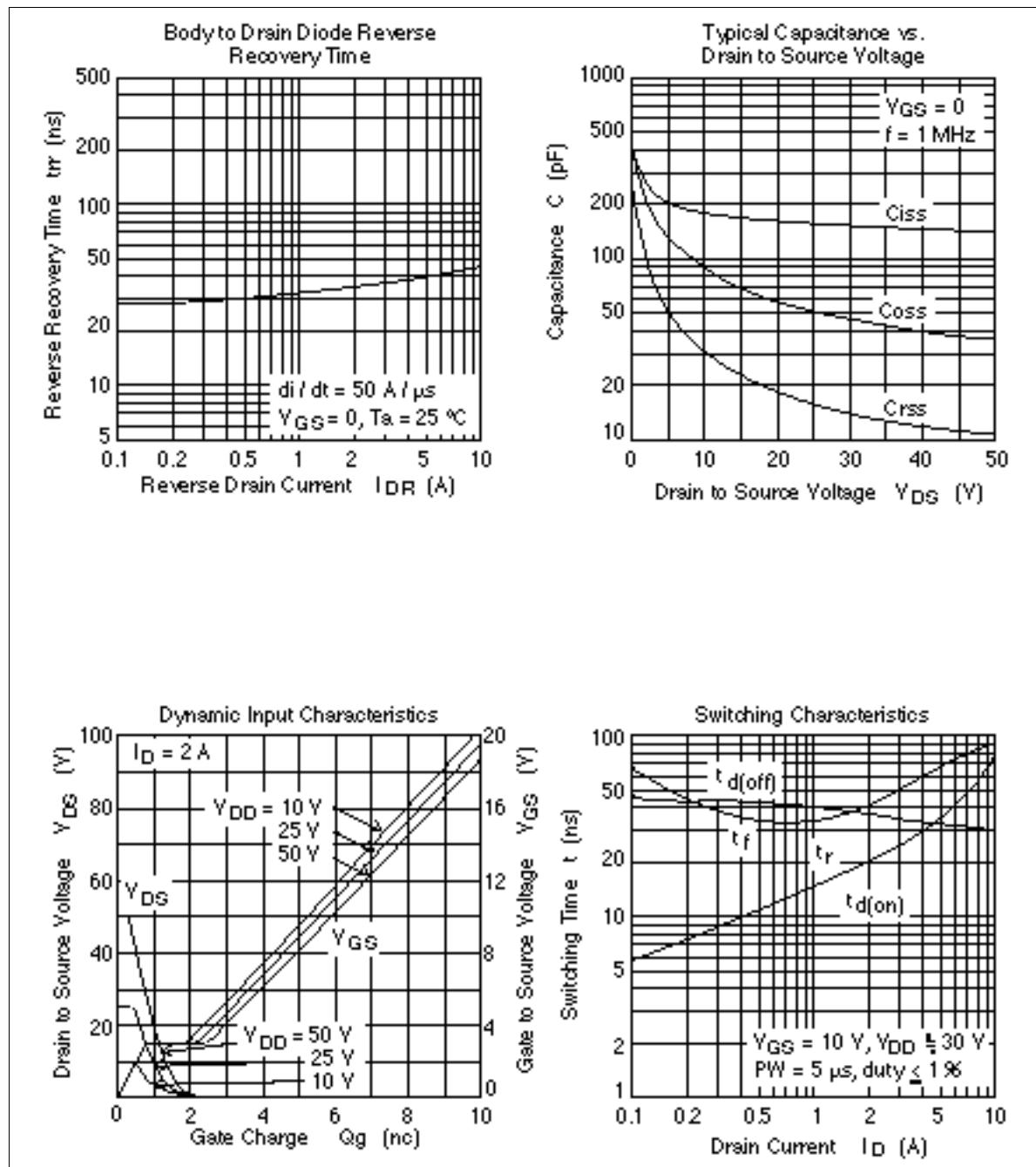
### Main Characteristics



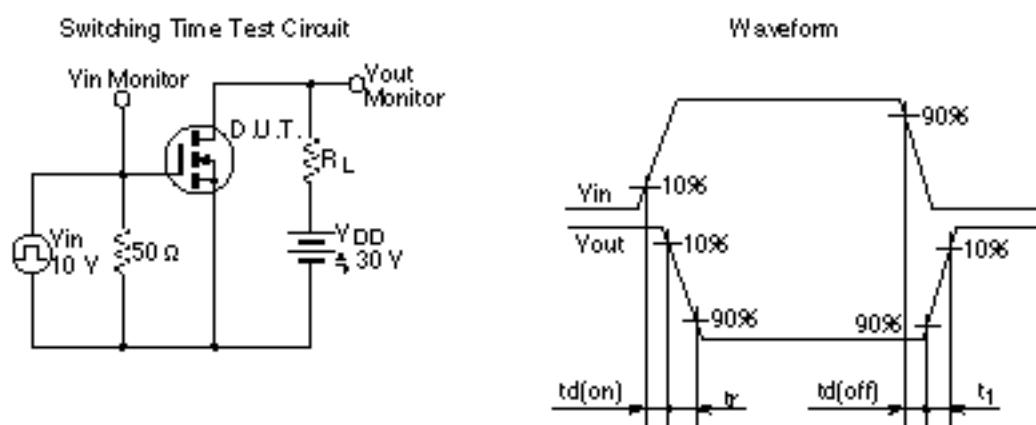
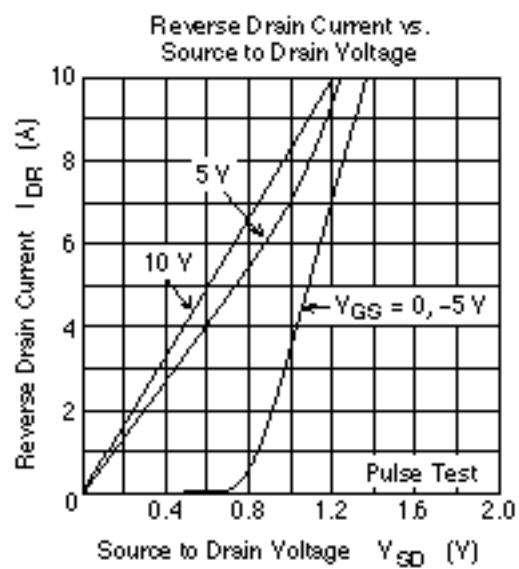
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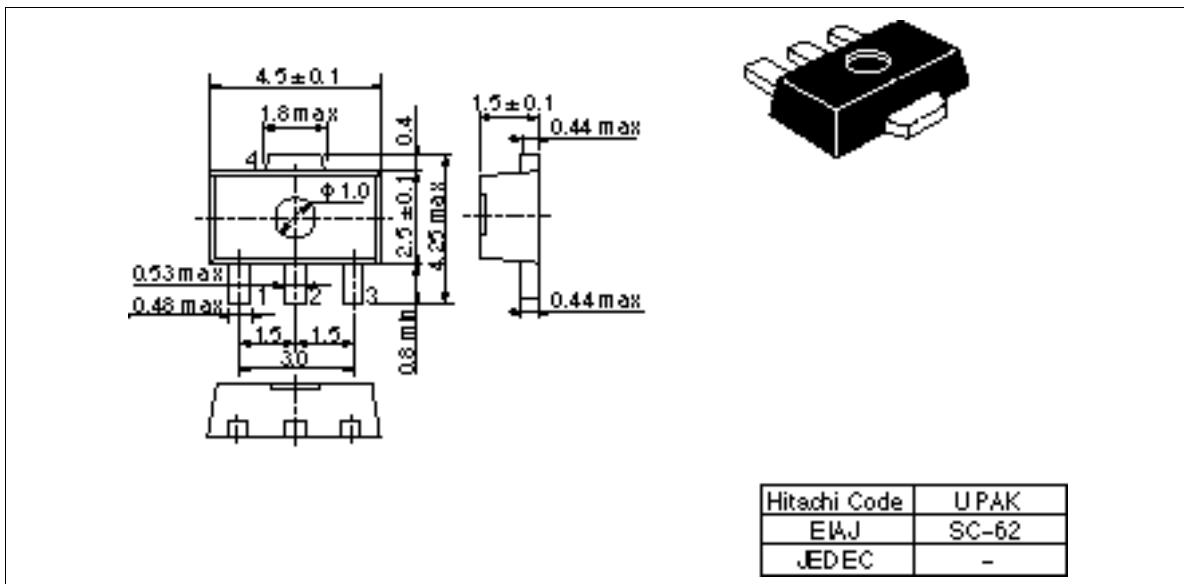
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## 2SK2788

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### Package Dimensions

Unit: mm



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