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D45C2

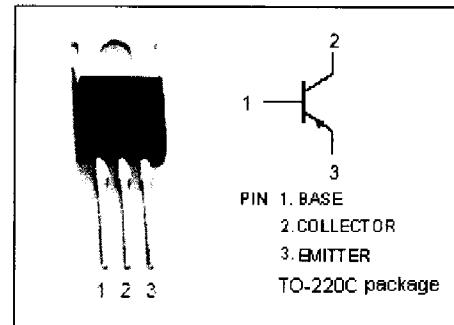
Silicon PNP Power Transistors

DESCRIPTION

- Low Saturation Voltage
- Good Linearity of h_{FE}
- Fast Switching Speeds
- Complement to Type D44C2

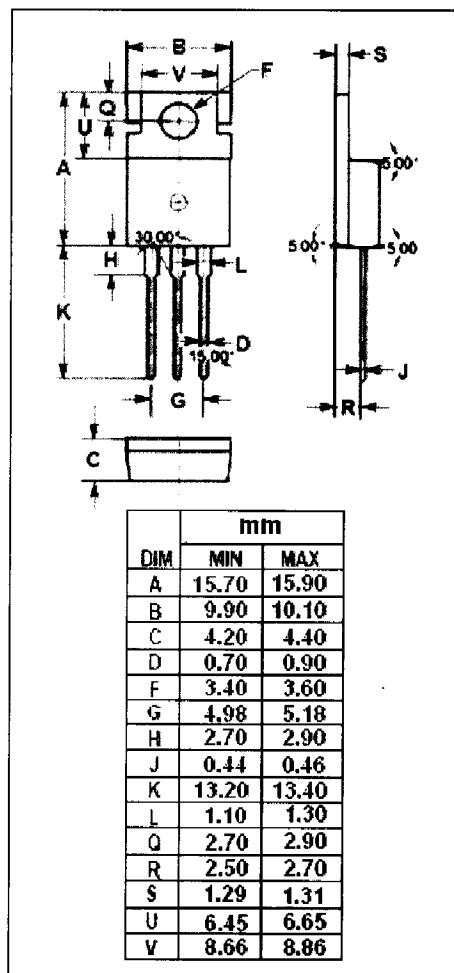
APPLICATIONS

- Designed for various specific and general purpose application such as: output and driver stages of amplifiers operating at frequencies from DC to greater than 1.0MHz series, shunt and switching regulators; low and high frequency inverters/converters and many others.



ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CES}	Collector-Emitter Voltage	-40	V
V_{CEO}	Collector-Emitter Voltage	-30	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_c	Collector Current-Continuous	-4	A
I_{CM}	Collector Current-Peak	-6	A
I_B	Base Current-Continuous	-1	A
P_c	Collector Power Dissipation @ $T_c=25^\circ C$	30	W
T_j	Junction Temperature	150	°C
T_{stg}	Storage Temperature Range	-55~150	°C



THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j-c}$	Thermal Resistance, Junction to Case	4.2	°C/W

NJ Semi-Conductors reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by NJ Semi-Conductors is believed to be both accurate and reliable at the time of going to press. However, NJ Semi-Conductors assumes no responsibility for any errors or omissions discovered in its use. NJ Semi-Conductors encourages customers to verify that datasheets are current before placing orders.



Silicon PNP Power Transistors

D45C2

ELECTRICAL CHARACTERISTICS

$T_c=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
$V_{CE(\text{sat})}$	Collector-Emitter Saturation Voltage	$I_C = -1\text{A}; I_B = -50\text{mA}$			-0.5	V
$V_{BE(\text{sat})}$	Base-Emitter Saturation Voltage	$I_C = -1\text{A}; I_B = -100\text{mA}$			-1.3	V
I_{CES}	Collector Cutoff Current	$V_{CE} = -40\text{V}, V_{BE} = 0$			-10	μA
I_{EBO}	Emitter Cutoff Current	$V_{EB} = -5\text{V}, I_C = 0$			-100	μA
h_{FE-1}	DC Current Gain	$I_C = -0.2\text{A}; V_{CE} = -1\text{V}$	40		120	
h_{FE-2}	DC Current Gain	$I_C = -1\text{A}; V_{CE} = -1\text{V}$	20			
f_T	Current-Gain—Bandwidth Product	$I_C = -20\text{mA}; V_{CE} = -4\text{V}; f_{\text{test}} = 1\text{MHz}$		40		MHz

Switching Times

t_r	Rise Time	$I_C = -1\text{A}; I_{B1} = -I_{B2} = -0.1\text{A}; V_{CC} = -20\text{V}$			0.2	μs
t_s	Storage Time				0.6	μs
t_f	Fall Time				0.3	μs