

# TK36 Phase Control Thyristor

DS4255-5.0 July 2001

Replaces January 2000 version, DS4255-4.0

#### **FEATURES**

■ High Surge Capability

#### **APPLICATIONS**

- High Power Drives
- High Voltage Power Supplies
- DC Motor Control
- Welding
- Battery Chargers

#### **VOLTAGE RATINGS**

Type Number	Repetitive Peak Voltages V <sub>DRM</sub> V <sub>RRM</sub> V	Conditions
TK36 12 M or K TK36 10 M or K TK36 08 M or K	1200 1000 800	$ \begin{array}{l} T_{vj}=0^{\circ} \text{ to } 125^{\circ}\text{C}, \\ I_{\text{DRM}}=I_{\text{RRM}}=100\text{mA}, \\ V_{\text{DRM}}, V_{\text{RRM}} t_{p}=10\text{ms}, \\ V_{\text{DSM}} \& V_{\text{RSM}}= \\ V_{\text{DRM}} \& V_{\text{RSM}}+100\text{V} \\ \text{respectively} \end{array} $

Lower voltage grades available.

#### **ORDERING INFORMATION**

When ordering, select the required part number shown in the Voltage Ratings selection table, then:-

Add K to type number for 3/4" 16 UNF thread, e.g. **TK36 12K**. or

Add M to type number for M16 thread, e.g. TK36 12M.

Note: Please use the complete part number when ordering and quote this number in any future correspondance relating to your order.

#### **KEY PARAMETERS**

V <sub>drm</sub>	1200V
I <sub>t(av)</sub>	245A
I <sub>TSM</sub>	5500A
dVdt*	<b>200V/</b> μ <b>s</b>
dl/dt	<b>500Α/</b> μ <b>s</b>

\*Higher dV/dt selections available

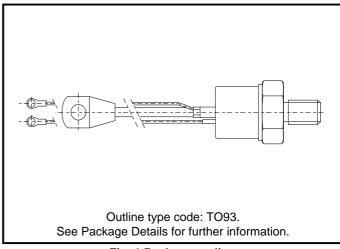


Fig. 1 Package outline



# **CURRENT RATINGS**

T<sub>case</sub> = 60°C unless stated otherwise.

Symbol	Parameter	Conditions	Max.	Units
I <sub>T(AV)</sub>	Mean on-state current	Half wave resistive load	323	А
I <sub>T(RMS)</sub>	RMS value	-	507	А
Ι <sub>τ</sub>	Continuous (direct) on-state current	-	425	А

# T<sub>case</sub> = 80°C unless stated otherwise.

Symbol	Parameter	Conditions	Max.	Units
I <sub>T(AV)</sub>	Mean on-state current	Half wave resistive load	245	А
I <sub>T(RMS)</sub>	RMS value	-	385	А
Ι <sub>τ</sub>	Continuous (direct) on-state current	-	315	А

#### SURGE RATINGS

Symbol	Parameter	Conditions	Max.	Units
I <sub>TSM</sub>	Surge (non-repetitive) on-state current	10ms half sine; T <sub>case</sub> = 125°C	4.4	kA
l²t	I <sup>2</sup> t for fusing	$V_{R} = 50\% V_{RRM} - 1/4 \text{ sine}$	96.8 x 10 <sup>3</sup>	A <sup>2</sup> s
I <sub>TSM</sub>	Surge (non-repetitive) on-state current	10ms half sine; T <sub>case</sub> = 125°C	5.5	kA
l²t	I <sup>2</sup> t for fusing	V <sub>R</sub> = 0	151.25 x 10 <sup>3</sup>	A <sup>2</sup> s

# THERMAL AND MECHANICAL DATA

Symbol	Parameter	Conditions	Min.	Max.	Units
R <sub>th(j-c)</sub>	Thermal resistance - junction to case	dc	-	0.13	°C/W
R <sub>th(c-h)</sub>	Thermal resistance - case to heatsink	Mounting torque 35.0Nm with mounting compound	-	0.06	°C/W
		On-state (conducting)	-	125	°C
T <sub>vj</sub> Virtual junction ten	Virtual junction temperature	Reverse (blocking)	-	125	°C
T <sub>stg</sub>	Storage temperature range		-40	150	°C
-	Mounting torque		30.0	35.0	Nm



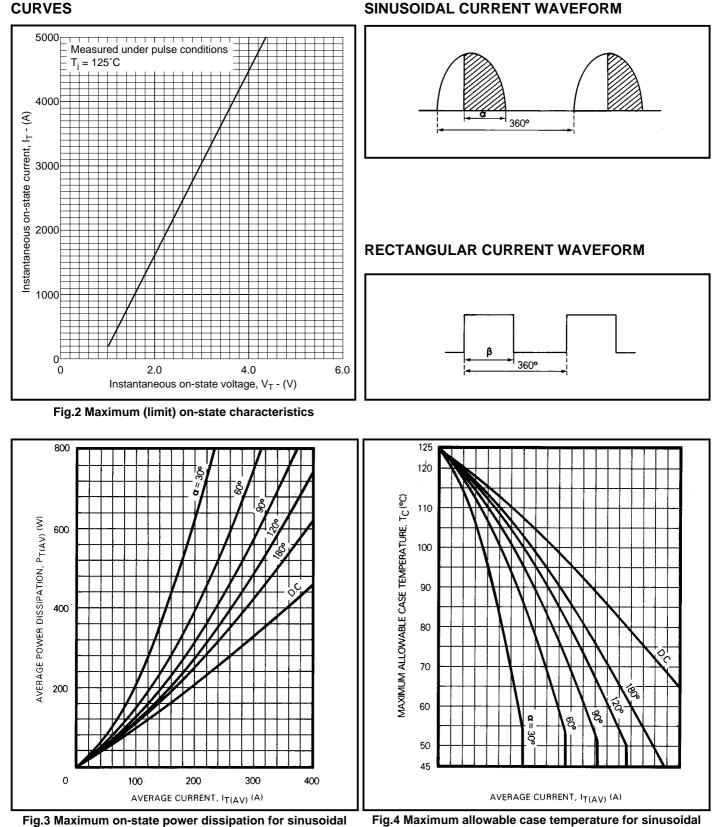
### **DYNAMIC CHARACTERISTICS**

Symbol	Parameter	Conditions		Min.	Max.	Units
V <sub>TM</sub>	Maximum on-state voltage	At 600A peak, T <sub>case</sub> = 25°C		-	1.3	V
I <sub>RRM</sub> /I <sub>DRM</sub>	Peak reverse and off-state current	At V <sub>RRM</sub> /V <sub>DRM</sub> , T <sub>case</sub> = 125°C		-	25	mA
dV/dt	Maximum linear rate of rise of off-state voltage	To 60% $V_{DRM} T_j = 125^{\circ}C$ , Gate open circuit		-	200	V/µs
-11/-14	dl/dt Rate of rise of on-state current	Gate source 20V, 20 $\Omega$ t <sub>r</sub> ≤ 0.5 $\mu$ s, T <sub>j</sub> = 125°C	Repetitive 50Hz	-	500	A/μs
ai/at			Non-repetitive	-	800	A/μs
V <sub>T(TO)</sub>	Threshold voltage	At T <sub>vj</sub> = 125°C		-	0.88	V
r <sub>T</sub>	On-state slope resistance	At T <sub>vj</sub> = 125°C		-	0.7	mΩ
t <sub>gd</sub>	Delay time	$V_{_{D}} = 300V, I_{_{G}} = 1A, I_{_{T}} = 50A, dI/dt = 50A/\mu s, dI_{_{G}}/dt = 1A/\mu s, T_{_{j}} = 25^{\circ}C$		-	1.5	μs
I <sub>L</sub>	Latching current	$T_{j} = 25^{\circ}C, V_{D} = 12V$		-	-	mA
I <sub>H</sub>	Holding current	T <sub>j</sub> = 25°C, V <sub>D</sub> = 12V, I <sub>TM</sub> = 1A		-	50	mA

### GATE TRIGGER CHARACTERISTICS AND RATINGS

Symbol	Parameter	Conditions		Max.	Units
V <sub>gt</sub>	Gate trigger voltage	$V_{\text{DRM}} = 12V, T_{\text{case}} = 25^{\circ}\text{C}, R_{\text{L}} = 6\Omega$		3.0	V
Ι <sub>gτ</sub>	Gate trigger current	$V_{\text{DRM}} = 12V, \ T_{\text{case}} = 25^{\circ}\text{C}, \ R_{\text{L}} = 6\Omega$	-	200	mA
$V_{GD}$	Gate non-trigger voltage	At $V_{\text{DRM}} T_{\text{case}} = 125^{\circ}\text{C}, R_{\text{L}} = 1k\Omega$	-	0.2	V
$V_{FGM}$	Peak forward gate voltage	Anode positive with respect to cathode	-	30	V
V <sub>FGN</sub>	Peak forward gate voltage	Anode negative with respect to cathode	-	0.25	V
V <sub>RGM</sub>	Peak reverse gate voltage		-	5	V
I <sub>FGM</sub>	Peak forward gate current	Anode positive with respect to cathode	-	4	A
P <sub>GM</sub>	Peak gate power	-	-	16	W
P <sub>G(AV)</sub>	Mean gate power		-	3	W





current waveform

current waveform



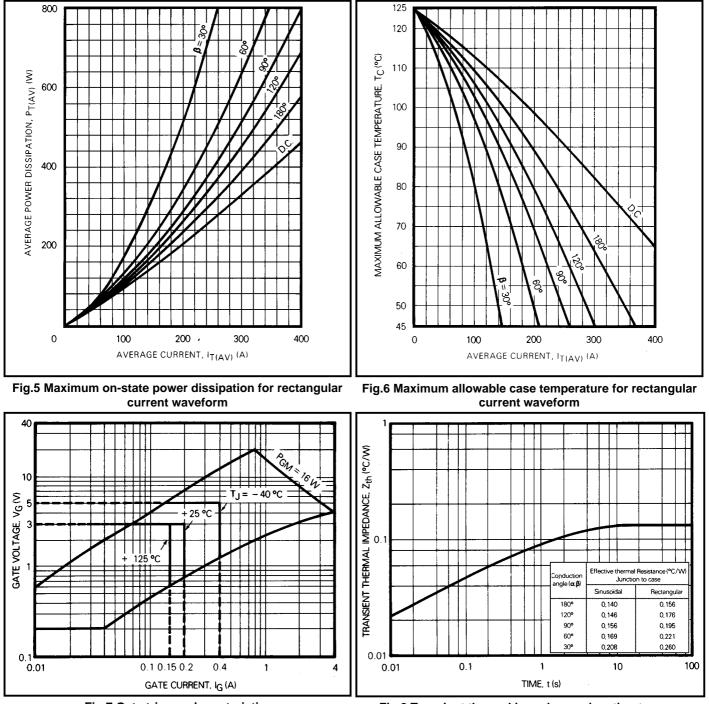
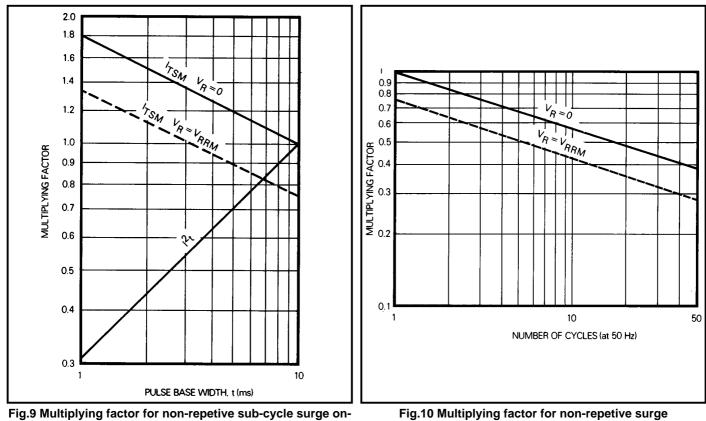


Fig.7 Gate trigger characteristics







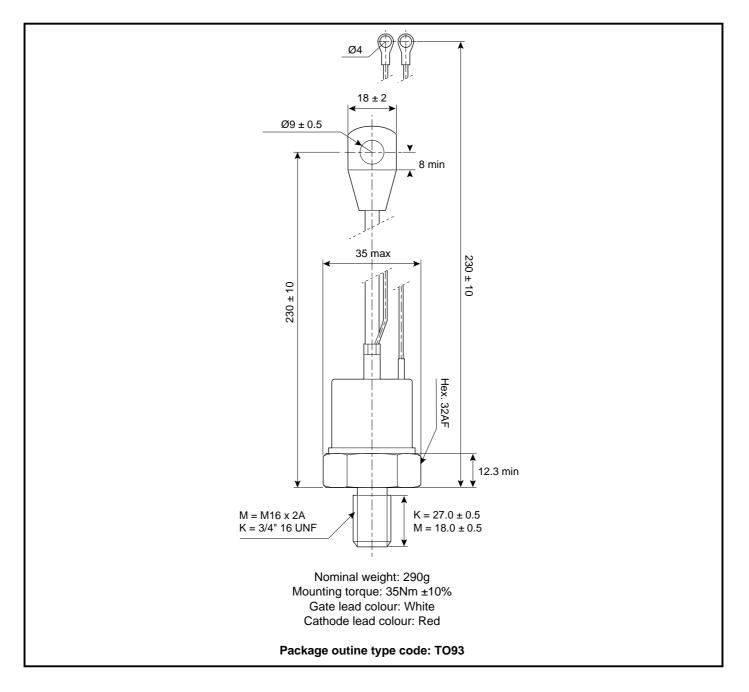
state current and I<sup>2</sup>t rating

g.10 Multiplying factor for non-repetive surge on-state current



#### PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.





#### POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group offers high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

#### HEATSINKS

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks which have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or Customer Services.



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CUSTOMER SERVICE

Target Information: This is the most tentative form of information and represents a very preliminary specification. No actual design work on the product has been started.

Preliminary Information: The product is in design and development. The datasheet represents the product as it is understood but details may change.

Advance Information: The product design is complete and final characterisation for volume production is well in hand

No Annotation: The product parameters are fixed and the product is available to datasheet specification.

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